

Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

Diodes Incorporated DXT751-13

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



Distributor of Diodes Incorporated: Excellent Integrated System Limited Datasheet of DXT751-13 - TRANS PNP 60V 3A SOT89-3 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com







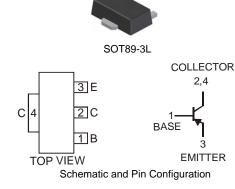
LOW V_{CE(SAT)} PNP SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Complementary NPN Type Available (DXT651)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Mechanical Data

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4 .
- Ordering Information: See Page 4
- Weight: 0.072 grams (approximate)



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-80	V
Collector-Emitter Voltage	V _{CEO}	-60	V
Emitter-Base Voltage	V _{EBO}	-5	V
Continuous Collector Current	Ic	-3	A
Peak Pulse Collector Current	I _{CM}	-6	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T _A = 25°C	PD	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @T _A = 25°C	R _{0JA}	125	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	C°

Notes: 1. No purposefully added lead.

2.

Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php. Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can 3. be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

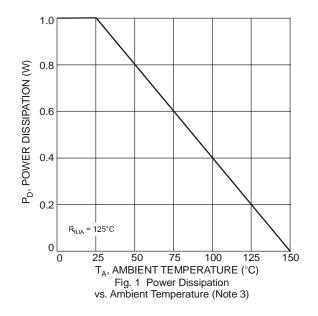


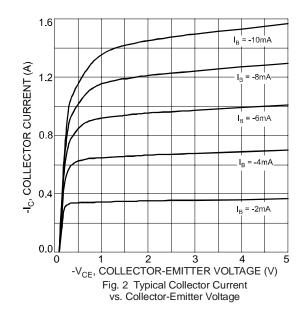


Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
OFF CHARACTERISTICS (Note 4)						
Collector-Base Breakdown Voltage	V _(BR) CBO	-80		_	V	$I_{C} = -100 \mu A, I_{E} = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-60	—	-	V	$I_{C} = -10 \text{mA}, I_{B} = 0$
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5	_		V	$I_E = -100 \mu A, I_C = 0$
Collector Cutoff Current	I _{CBO}	_	_	-0.1 -10	μΑ μΑ	V _{CB} = -60V, I _E = 0 V _{CB} = -60V, I _E = 0, T _A = 100°C
Emitter Cutoff Current	I _{EBO}	_	_	-0.1	μΑ	$V_{EB} = -4V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)						
Collector-Emitter Saturation Voltage	V _{CE(SAT)}		-0.08 -0.2	-0.3 -0.6	V	I _C = -1A, I _B = -100mA I _C = -3A, I _B = -300mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}	_	-0.9	-1.25	V	I _C = -1A, I _B = -100mA
Base-Emitter Turn-On Voltage	V _{BE(ON)}	_	-0.8	-1	V	$V_{CE} = -2V, I_{C} = -1A$
DC Current Gain	h _{FE}	70 100 80 40	200 180 160 140	 300 	_	$V_{CE} = -2V, I_C = -50mA$ $V_{CE} = -2V, I_C = -500mA$ $V_{CE} = -2V, I_C = -1A$ $V_{CE} = -2V, I_C = -2A$
AC CHARACTERISTICS						
Transition Frequency	f _T	100	145	_	MHz	$V_{CE} = -10V, I_C = -50mA, f = 100MHz$
Output Capacitance	C _{obo}	_	_	30	pF	$V_{CB} = -10V, f = 1MHz$
Switching Times	t _{on} t _{off}	_	45 200	_	ns ns	I _C = -500mA, V _{CC} = -10V I _{B1} = I _{B2} = -50mA

Notes: 4. Measured under pulsed conditions. Pulse width = 300μ s. Duty cycle $\leq 2\%$.

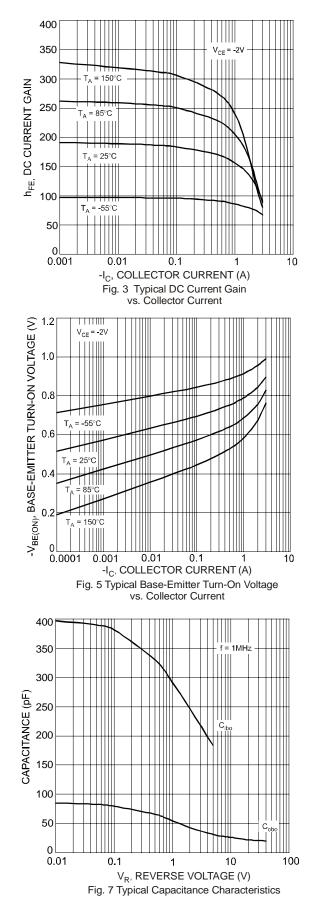


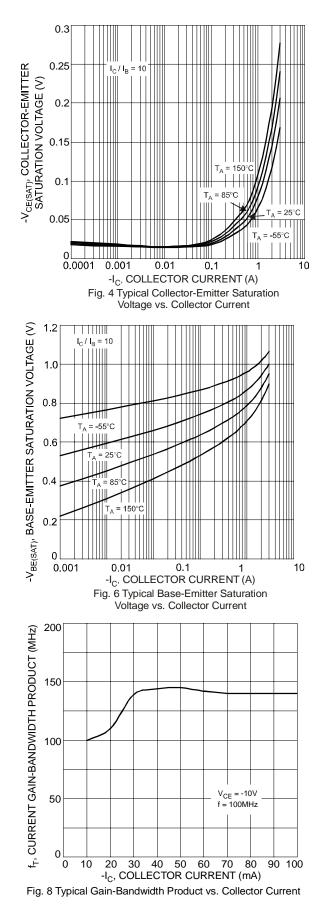




Distributor of Diodes Incorporated: Excellent Integrated System Limited Datasheet of DXT751-13 - TRANS PNP 60V 3A SOT89-3 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com









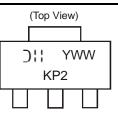


Ordering Information (Note 5)

Device	Packaging	Shipping
DXT751-13	SOT89-3L	2500/Tape & Reel

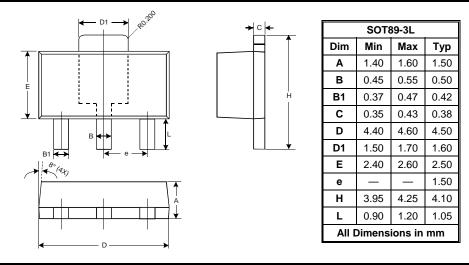
Notes: 5. For packaging details, go to our website at http://www.diodes.com/ap02007.pdf.

Marking Information

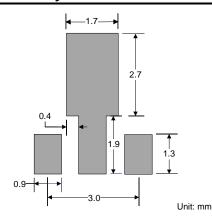


KP2 = Product Type Marking Code YWW = Date Code Marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

Package Outline Dimensions



Suggested Pad Layout



IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.