

# **Excellent Integrated System Limited**

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Vishay Semiconductor/Opto Division VLMS3100-GS08

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>







### VLMS3100, VLMS3101

Vishay Semiconductors

## Standard SMD LED PLCC-2



#### DESCRIPTION

This device has been designed for applications requiring narrow brightness and color selection.

The package of this device is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

### **PRODUCT GROUP AND PACKAGE DATA**

- Product group: LED
- Package: SMD PLCC-2
- · Product series: standard
- Angle of half intensity: ± 60°

#### **FEATURES**

- SMD LED with exceptional brightness
- · Luminous intensity categorized
- Compatible with automatic placement equipment
- · EIA and ICE standard package
- · Compatible with infrared, vapor phase and wave solder processes according to CECC
- Available in 8 mm tape
- · Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- · Low power consumption
- Luminous intensity ratio in one packaging unit  $I_{Vmax.}/I_{Vmin.} \leq 1.6$
- Preconditioning according to JEDEC® level 2a
- ESD-withstand voltage: up to 1 kV according to JESD22-A114-B
- AEC-Q101 gualified
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### APPLICATIONS

- · Automotive: backlighting in dashboards and switches
- Telecommunication: indicator and backlighting in telephone and fax

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- Indicator and backlight for audio and video equipment
- · Indicator and backlight in office equipment
- Flat backlight for LCDs, switches, and symbols
- General use

PARTS TABL	ARTS TABLE													
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I <sub>F</sub> (mA)	WAVELENGTH (nm)		at I <sub>F</sub> (mA)	FORW	ORWARD VOLTAGE (V)		at I <sub>F</sub> (mA)			
		MIN.	TYP.	MAX.	(111,4)	MIN.	TYP.	MAX.		MIN.	TYP.	MAX.	(IIIA)	
VLMS3100-GS08	Red	2.8	7.1	-	10	624	630	636	10	-	2.0	2.6	20	GaAsP on GaP
VLMS3100-GS18	Red	2.8	7.1	-	10	624	630	636	10	-	2.0	2.6	20	GaAsP on GaP
VLMS3101-GS08	Red	4.5	7.8	11.2	10	624	630	636	10	-	2.0	2.6	20	GaAsP on GaP
VLMS3101-GS18	Red	4.5	7.8	11.2	10	624	630	636	10	-	2.0	2.6	20	GaAsP on GaP





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ABSOLUTE MAXIMUM RAT VLMS3100, VLMS3101	<b>TINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise	se specified)		
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage (1)		V <sub>R</sub>	6	V
DC forward current	T <sub>amb</sub> ≤ 60 °C	IF	30	mA
Surge forward current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	0.5	А
Power dissipation	T <sub>amb</sub> ≤ 60 °C	Pv	100	mW
Junction temperature		Тj	100	°C
Operating temperature range		T <sub>amb</sub>	-40 to +100	°C
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C
Soldering temperature	t ≤ 5 s	T <sub>sd</sub>	260	°C
Thermal resistance junction / ambient	Mounted on PC board (pad size > 16 mm <sup>2</sup> )	R <sub>thJA</sub>	400	K/W

#### Note

<sup>(1)</sup> Driving LED in reverse direction is suitable for short term application

#### **OPTICAL AND ELECTRICAL CHARACTERISTICS** ( $T_{amb} = 25$ °C, unless otherwise specified) **VLMS3100. VLMS3101. RED**

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
.,		VLMS3100		2.8	7.1		mcd
Luminous intensity <sup>(1)</sup>	I <sub>F</sub> = 10 mA		Ι <sub>V</sub>	2.0		-	mcu
,		VLMS3101	$I_V$	4.5	7.8	11.2	mcd
Dominant wavelength	I <sub>F</sub> = 10 mA		$\lambda_d$	624	630	636	nm
Peak wavelength	I <sub>F</sub> = 10 mA		λρ	-	640	-	nm
Angle of half intensity	I <sub>F</sub> = 10 mA		φ	-	± 60	-	deg
Forward voltage	I <sub>F</sub> = 20 mA		V <sub>F</sub>	-	2.0	2.6	V
Reverse voltage	I <sub>R</sub> = 10 μA		V <sub>R</sub>	6	-	-	V
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		Cj	-	7	-	pF
Temperature coefficient of $V_F$	I <sub>F</sub> = 20 mA		TC <sub>VF</sub>	-	-1.8	-	mV/K
Temperature coefficient of $\lambda_d$	I <sub>F</sub> = 10 mA		TCλ <sub>d</sub>	-	0.05	-	nm/K

#### Note

 $^{(1)}~$  In one packing unit  $I_{Vmax.}/I_{Vmin.} \leq 1.6$ 

LUMINOUS INTENSITY CLASSIFICATION							
GROUP	LIGHT INTENSITY (mcd)						
STANDARD	OPTIONAL	MIN.	MAX.				
Н	1	2.80	3.55				
п	2	3.55	4.50				
J	1	4.50	5.60				
J	2	5.60	7.10				
К	1	7.10	9.00				
n.	2	9.00	11.20				
1	1	11.20	14.00				
L	2	14.00	18.00				

#### Note

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of  $\pm$  11 %. The above type numbers represent the order groups which

Ine above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups will not be orderable.

Rev. 1.5, 31-Jul-15

 VISHAY
 OSRAM

 VLMS3100
 LST670-J1L2

 VLMS3101
 LST670-J1K2

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For technical questions, contact: LED@vishay.com

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### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

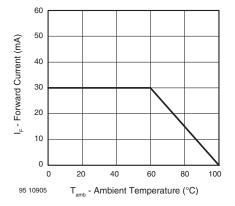


Fig. 1 - Forward Current vs. Ambient Temperature

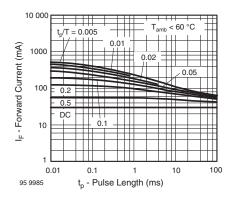


Fig. 2 - Forward Current vs. Pulse Length

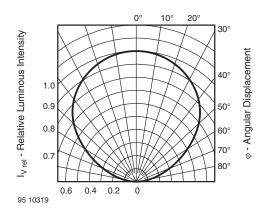


Fig. 3 - Relative Luminous Intensity vs. Angular Displacement

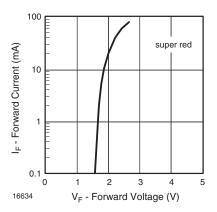


Fig. 4 - Forward Current vs. Forward Voltage

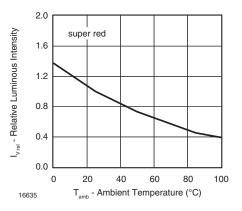


Fig. 5 - Relative Luminous Intensity vs. Ambient Temperature

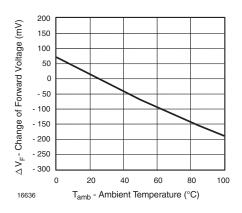


Fig. 6 - Change of Forward Voltage vs. Ambient Temperature

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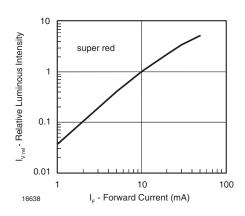


Fig. 7 - Relative Luminous Intensity vs. Forward Current

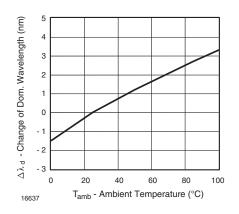


Fig. 8 - Change of Dominant Wavelength vs. Ambient Temperartue

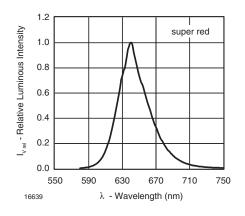


Fig. 9 - Relative Luminous Intensity vs. Wavelength



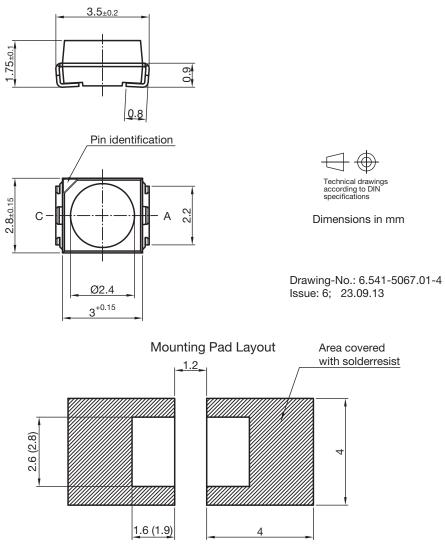


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#### **PACKAGE DIMENSIONS** in millimeters



Dimensions: Reflow and vapor phase (wave soldering)





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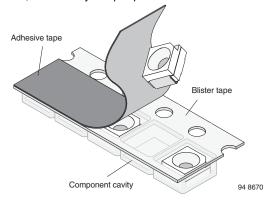
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#### **METHOD OF TAPING / POLARITY AND TAPE AND REEL**

#### SMD LED (VLM.3 - SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertation. The blister tape is a plastic strip with impressed component cavaties, covered by a top tape.

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#### TAPING OF VLM.3...

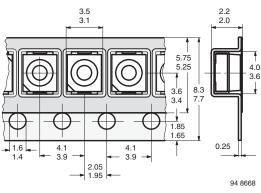


Fig. 10 - Tape Dimensions in mm for PLCC-2

#### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

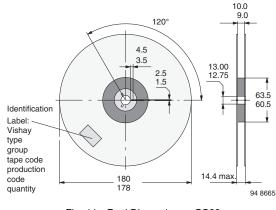


Fig. 11 - Reel Dimensions - GS08

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

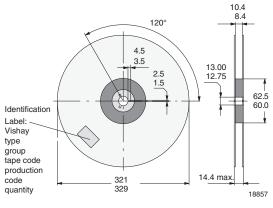


Fig. 12 - Reel Dimensions - GS18

#### **SOLDERING PROFILE**

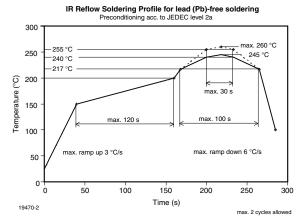


Fig. 13 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

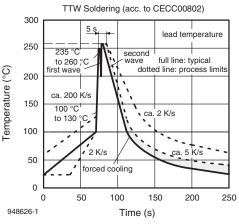


Fig. 14 - Double Wave Soldering of Opto Devices (all Packages)

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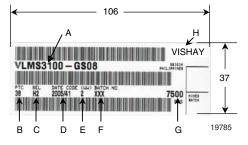
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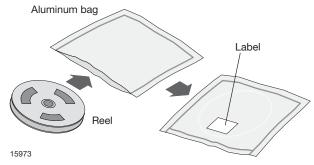
### **BAR CODE PRODUCT LABEL**



- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin):
- e.g.: R2 = code for luminous intensity group 3 = code for color group
- D) Date code year / week
- E) Day code (e.g. 3: Wednesday)
- F) Batch no.
- G) Total quantity
- H) Company code

#### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



### **FINAL PACKING**

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

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### **RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

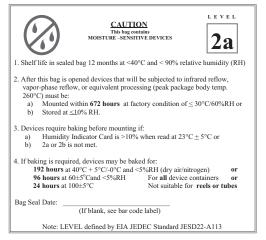
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

#### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

#### VISHAY SEMICONDUCTORS STANDARD **BAR CODE LABELS**

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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