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[Powerex Inc.](#)
[CM100RL-24NF](#)

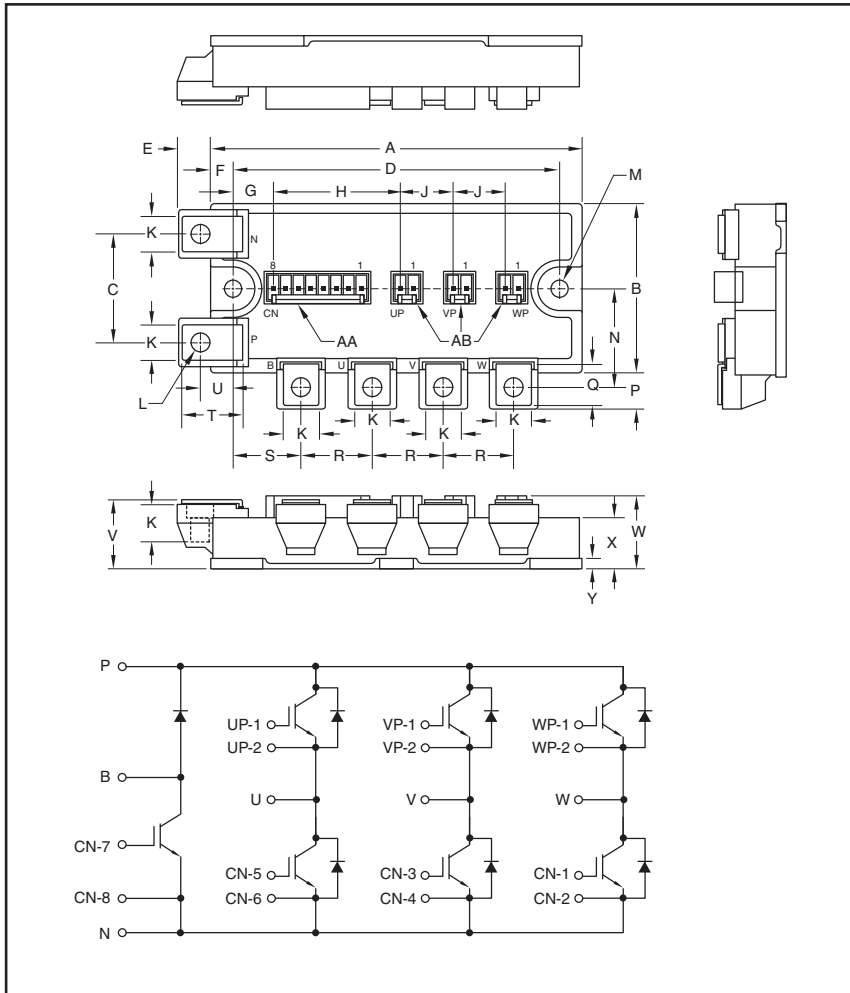
For any questions, you can email us directly:
sales@integrated-circuit.com



Powerex, Inc., 173 Pavilion Lane, Youngwood, Pennsylvania 15697 (724) 925-7272
 www.pwr.com

CM100RL-24NF

Six IGBTMOD™ + Brake
NF-Series Module
100 Amperes/1200 Volts



Outline Drawing and Circuit Diagram

Dimensions	Inches	Millimeters
A	4.72	120.0
B	2.17	55.0
C	1.39	35.0
D	4.17±0.02	106.0±0.5
E	0.43	11.0
F	0.28	7.0
G	0.54	13.62
H	1.61	40.78
J	0.67	17.0
K	0.47	12.0
L	M5	M5
M	0.22 Dia.	Dia. 5.5

Dimensions	Inches	Millimeters
N	1.23	32.0
P	0.47	11.75
Q	0.53	13.5
R	0.91	23.0
S	0.87	22.0
T	0.76	19.75
U	0.42	10.75
V	0.87+0.04/-0.02	22.0+1.0/-0.5
W	0.91	23.2
X	0.63	16.0
Y	0.12	3.0

Housing Types (J.S.T. Mfg. Co. Ltd.)

AA – B8P-VH-FB-B
 AB – B2P-VH-FB-B



Description:

Powerex IGBTMOD™ Modules are designed for use in switching applications. Each module consists of six IGBT Transistors in a three phase bridge configuration and a seventh IGBT with free-wheel diode for dynamic braking. All components and interconnects are isolated from the heat sinking baseplate, offering simplified system assembly and thermal management.

Features:

- Low Drive Power
- Low $V_{CE(sat)}$
- Discrete Super-Fast Recovery Free-Wheel Diode
- Isolated Baseplate for Easy Heat Sinking

Applications:

- AC Motor Control
- Motion/Servo Control
- Photovoltaic/Fuel Cell

Ordering Information:

Example: Select the complete module number you desire from the table below -i.e. CM100RL-24NF is a 1200V (V_{CES}), 100 Ampere Six-IGBTMOD™ + Brake Power Module.

Type	Current Rating Amperes	V_{CES} Volts (x 50)
CM	100	24



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Absolute Maximum Ratings, T_j = 25°C unless otherwise specified

Characteristics	Symbol	CM100RL-24NF	Units
Power Device Junction Temperature	T _j	-40 to 150	°C
Storage Temperature	T _{stg}	-40 to 125	°C
Mounting Torque, M5 Mounting Screws	—	31	in-lb
Mounting Torque, M5 Main Terminal Screws	—	31	in-lb
Module Weight (Typical)	—	350	Grams
Isolation Voltage, AC 1 minute, 60Hz Sinusoidal	V _{ISO}	2500	Volts

Inverter Sector

Collector-Emitter Voltage (G-E Short)	V _{CES}	1200	Volts
Gate-Emitter Voltage (C-E Short)	V _{GES}	±20	Volts
Collector Current (T _C = 80°C)*	I _C	100	Amperes
Peak Collector Current (T _j ≤ 150°C)	I _{CM}	200**	Amperes
Emitter Current***	I _E	100	Amperes
Peak Emitter Current***	I _{EM}	200**	Amperes
Maximum Collector Dissipation (T _C = 25°C, T _j < 150°C)	P _C	620	Watts

Brake Sector

Collector-Emitter Voltage (G-E Short)	V _{CES}	1200	Volts
Gate-Emitter Voltage (C-E Short)	V _{GES}	±20	Volts
Collector Current (T _C = 94°C)*	I _C	50	Amperes
Peak Collector Current (T _j ≤ 150°C)	I _{CM}	100**	Amperes
Maximum Collector Dissipation (T _C = 25°C, T _j < 150°C)	P _C	390	Watts
Repetitive Peak Reverse Voltage (Clamp Diode Part)	V _R RRM	1200	Volts
Forward Current (Clamp Diode Part)	I _F FM	50	Amperes

*T_C, T_f measured point is just under the chips.

**Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed T_{j(max)} rating.

***Represents characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDI).



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Electrical and Mechanical Characteristics, T_j = 25°C unless otherwise specified

Inverter Sector

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Collector Cutoff Current	I _{CES}	V _{CE} = V _{CES} , V _{GE} = 0V	—	—	1.0	mA
Gate-Emitter Threshold Voltage	V _{GE(th)}	I _C = 10mA, V _{CE} = 10V	6	7	8	Volts
Gate Leakage Current	I _{GES}	V _{GE} = V _{GES} , V _{CE} = 0V	—	—	0.5	μA
Collector-Emitter Saturation Voltage	V _{CE(sat)}	I _C = 1005A, V _{GE} = 15V, T _j = 25°C	—	2.1	3.0	Volts
		I _C = 100A, V _{GE} = 15V, T _j = 125°C	—	2.4	—	Volts
Input Capacitance	C _{ies}		—	—	17.5	nf
Output Capacitance	C _{oes}	V _{CE} = 10V, V _{GE} = 0V	—	—	1.5	nf
Reverse Transfer Capacitance	C _{res}		—	—	0.34	nf
Total Gate Charge	Q _G	V _{CC} = 600V, I _C = 100A, V _{GE} = 15V	—	500	—	nC
Inductive	Turn-on Delay Time	t _{d(on)}	—	—	100	ns
	Turn-on Rise Time	t _r	—	—	70	ns
Switch	Turn-off Delay Time	t _{d(off)}	—	—	300	ns
	Turn-off Fall Time	t _f	—	—	350	ns
Reverse Recovery Time*	t _{rr}	Inductive Load Switching Operation	—	—	150	ns
Reverse Recovery Charge*	Q _{rr}		—	4.8	—	μC
Emitter-Collector Voltage*	V _{EC}	I _E = 100A, V _{GE} = 0V	—	—	3.8	Volts

Thermal and Mechanical Characteristics, T_j = 25°C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case**	R _{th(j-c)Q}	Per IGBT 1/6 Module	—	—	0.20	°C/W
Thermal Resistance, Junction to Case**	R _{th(j-c)D}	Per FWDi 1/6 Module	—	—	0.28	°C/W
Contact Thermal Resistance	R _{th(c-f)}	Per 1/6 Module, Thermal Grease Applied	—	—	0.085	°C/W
External Gate Resistance	R _G		3.1	—	42	Ω

*Represents characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDi).

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Electrical and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Brake Sector

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Collector Cutoff Current	I_{CES}	$V_{CE} = V_{CES}, V_D = 0V$	—	—	1.0	mA
Gate-Emitter Threshold Voltage	$V_{GE(th)}$	$I_C = 5.0mA$	6	7	8	Volts
Gate Leakage Current	I_{GES}	$V_{GE} = V_{GES}, V_{CE} = 0V$	—	—	0.5	μA
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 50A, V_{GE} = 15V, T_j = 25^\circ\text{C}$	—	2.1	3.0	Volts
		$I_C = 50A, V_{GE} = 15V, T_j = 125^\circ\text{C}$	—	2.4	—	Volts
Input Capacitance	C_{ies}		—	—	8.5	nf
Output Capacitance	C_{oes}	$V_{CE} = 10V, V_{GE} = 0V$	—	—	0.75	nf
Reverse Transfer Capacitance	C_{res}		—	—	0.17	nf
Total Gate Charge	Q_G	$V_{CC} = 600V, I_C = 50A, V_{GE} = 15V$	—	250	—	nC
Forward Voltage Drop	V_{FM}	$I_F = 50A$	—	—	3.8	Volts

Thermal and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

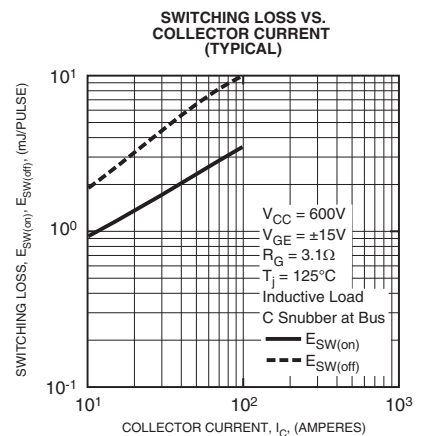
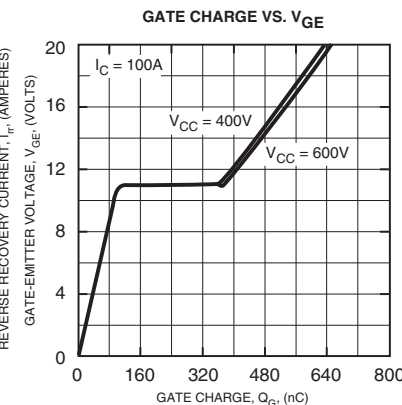
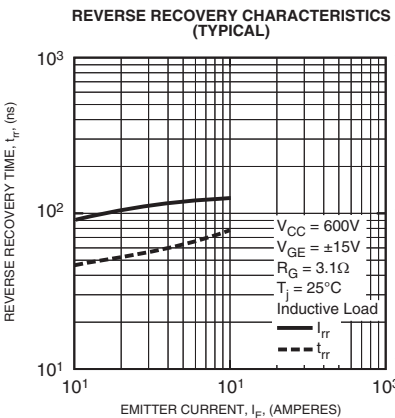
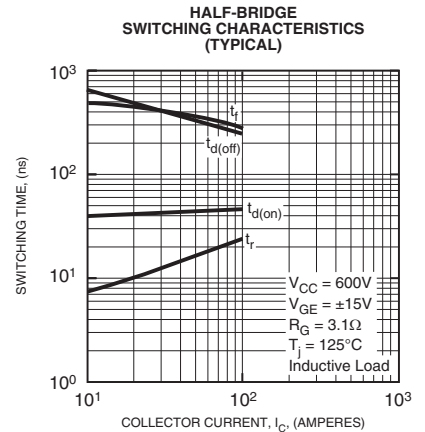
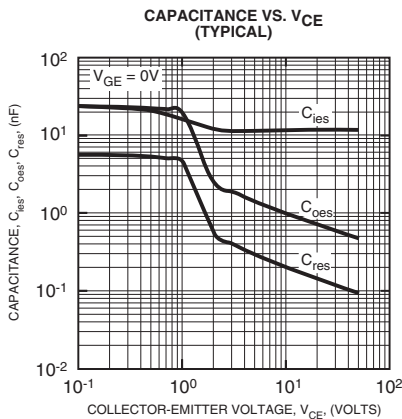
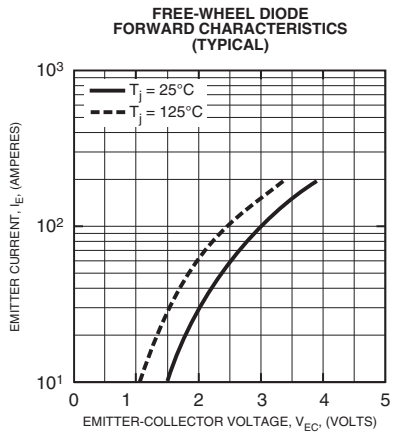
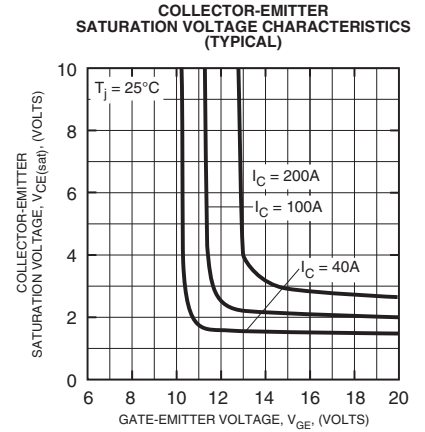
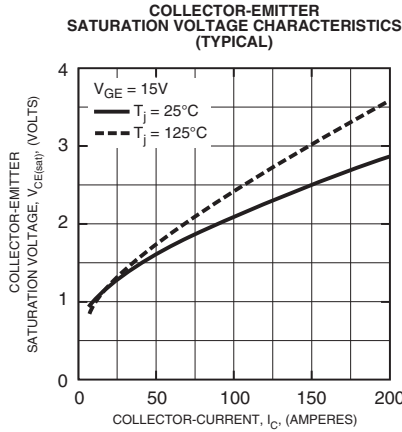
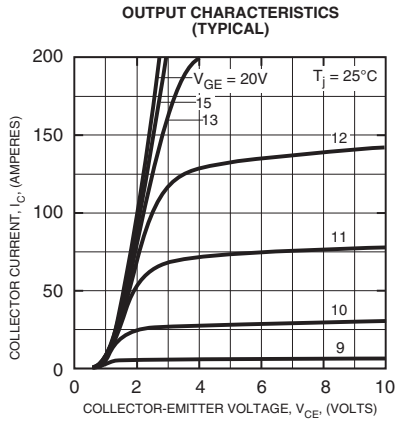
Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case*	$R_{th(j-c)Q}$	Per IGBT 1/6 Module	—	—	0.32	$^\circ\text{C}/W$
Thermal Resistance, Junction to Case*	$R_{th(j-c)D}$	Per FWDi 1/6 Module	—	—	0.43	$^\circ\text{C}/W$

* T_C, T_f measured point is just under the chips.



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