

Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

[Integrated Device Technology \(IDT\)](#)
[960009CFLF](#)

For any questions, you can email us directly:

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960009CFLF

Category:

Generic Part: 960009

Market Group: PC CLOCK

Description: PC SPECIAL PURPOSE

Output Features: $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ ASIC power 2.5V - 3.3V $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ Supports Spread Spectrum $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ 1 - Single-ended CPU $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ 5 - Pair Differential DDR outputs $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ 9 - PCI outputs $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ 1 - ASIC @ 33MHz $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ 1 - ASIC @ 80MHz - 100MHz
 Features/Benefits: $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ Selectable I2C address $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ Selectable output enable/disable via I2C $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ CPU - DDR skew typically <150ps Key Specifications: $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ CPU jitter: <150ps $\hat{\text{A}}\hat{\text{E}}\hat{\text{C}}\hat{\text{I}}$ PCI skew: <



Parameters

Package	SSOP 48 (PVG48)
Speed	NA
Temperature	C
Voltage	3.3 V
Status	Active
Sample	Yes
Minimum Order Quantity	120
Factory Order Increment	30

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Description	SSOP 300 MIL, 48LD
Class	PLASTIC
Moisture Sensitivity Level (MSL)	1
Category	Green
Moisture Exposure Floor Life	Unlimited @ <30°C/85% RH
Peak Reflow Temperature	260°C
Rebake Conditions	N/A
Length	15.9
Mark	PVG
Width	7.5
Pitch	0.64
Thickness	2.3
Status	Active