Excellent Integrated System Limited

Stocking Distributor

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Integrated Device Technology (IDT)
9FG1902AKLFT

For any questions, you can email us directly:
sales@integrated-circuit.com

9FG1902AKLFT

Category:

Generic Part: 9FG1902

Market Group: PC CLOCK

Description: PCIE BUFFER
CPU Host Bus, PCI Express and Fully-Buffered DIMM clocking. The ICS9FG1902 follows the Intel DB1900G Differential Buffer Specification, except for the power up default state. This buffer provides 19 output clocks for CPU Host Bus, PCI-Express, or Fully Buffered DIMM applications. The outputs are configured with two groups. Both groups (DIF 16:0) and (DIF 18:17) can be equal to or have a gear ratio to the input clock. The ICS9FG1902 power up default differs from the ICS9FG1901. The ICS9FG1902 powers up with DIF(16:0) at 1/2 the input frequency, when FS_A_410 = 0 (input frequency > = 200 MHz). A differential CPU clock from a CK410 or CK410B main clock generator, such as the ICS954101 or ICS932S401, drives the ICS9FG1902. The ICS9FG1902 can provide outputs up to 400MHz.

Parameters

Package	VFQFPN 72 (NLG72)
Speed	NA
Temperature	C
Voltage	3.3 V
Status	Active
Sample	No
Minimum Order Quantity	500
Factory Order Increment	500

Package

Description	VFQFP-N 10.0 X 10.0 X 0.9 MM - NO LEAD
Class	PLASTIC
Moisture Sensitivity Level (MSL)	3
Category	Green
Moisture Exposure Floor Life	168 hrs. @ <30°C/60%RH
Peak Reflow Temprature	260°C
Rebake Conditions	48 hrs.@125°C
Length	10.0
Mark	K
Width	10.0
Pitch	0.5
Thickness	1.0
Status	Active

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007
Product Change Notice	PCN# : A0701-02 Punch Singulation as Alternate Assembly Process for 10mm x 10mm VFQFP-N-72	514 KB	01/30/2007
	PCN# : A0701-02R1 Punch Singulation as Alternate Assembly Process for 10mm x 10mm VFQFP-N-72 and transfer Test facility from IDT Singapore to IDT Penang	453 KB	04/24/2007