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Advance Technical Information

# Standard Power MOSFET

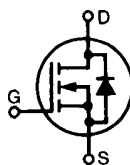
## IXTH 30N25

$$V_{DSS} = 250 \text{ V}$$

$$I_{D(\text{cont})} = 30 \text{ A}$$

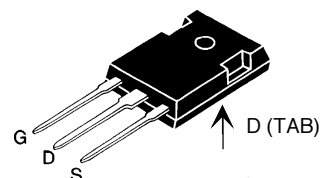
$$R_{DS(\text{on})} = 75 \text{ m}\Omega$$

N-Channel Enhancement Mode



Symbol	Test Conditions	Maximum Ratings	
$V_{DSS}$	$T_J = 25^\circ\text{C to } 150^\circ\text{C}$	250	V
$V_{DGR}$	$T_J = 25^\circ\text{C to } 150^\circ\text{C}; R_{GS} = 1 \text{ M}\Omega$	250	V
$V_{GS}$	Continuous	$\pm 20$	V
$V_{GSM}$	Transient	$\pm 30$	V
$I_{D25}$	$T_C = 25^\circ\text{C}$	30	A
$I_{DM}$	$T_C = 25^\circ\text{C}$ , pulse width limited by $T_{JM}$	120	A
$I_{AR}$		30	A
$E_{AR}$	$T_C = 25^\circ\text{C}$	30	mJ
$E_{AS}$	$T_C = 25^\circ\text{C}$	1.0	J
$dv/dt$	$I_S \leq I_{DM}$ , $di/dt \leq 100 \text{ A}/\mu\text{s}$ , $V_{DD} \leq V_{DSS}$ , $T_J \leq 150^\circ\text{C}$ , $R_G = 2 \Omega$	5	V/ns
$P_D$	$T_C = 25^\circ\text{C}$	200	W
$T_J$		-55 ... +150	$^\circ\text{C}$
$T_{JM}$		150	$^\circ\text{C}$
$T_{stg}$		-55 ... +150	$^\circ\text{C}$
$M_d$	Mounting torque	1.13/10	Nm/lb.in.
<b>Weight</b>		6	g
Maximum lead temperature for soldering 1.6 mm (0.062 in.) from case for 10 s		300	$^\circ\text{C}$

TO-247 AD



G = Gate, D = Drain,  
S = Source, TAB = Drain

### Features

- International standard package JEDEC TO-247 AD
- Low  $R_{DS(\text{on})}$  HDMOS™ process
- Rugged polysilicon gate cell structure
- High commutating  $dv/dt$  rating
- Fast switching times

### Applications

- Switch-mode and resonant-mode power supplies
- Motor controls
- Uninterruptible Power Supplies (UPS)
- DC choppers

### Advantages

- Easy to mount with 1 screw (isolated mounting screw hole)
- Space savings
- High power density

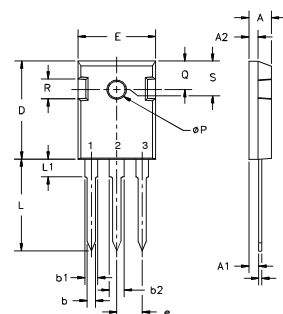
Symbol	Test Conditions	Characteristic Values ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)		
		min.	typ.	max.
$V_{DSS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	250		V
$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	2		V
$I_{GSS}$	$V_{GS} = \pm 20 \text{ V}_{DC}, V_{DS} = 0$			$\pm 100 \text{ nA}$
$I_{DSS}$	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$ $T_J = 125^\circ\text{C}$			25 $\mu\text{A}$ 250 $\mu\text{A}$
$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$ Pulse test, $t \leq 300 \mu\text{s}$ , duty cycle $d \leq 2 \%$	55	75	$\text{m}\Omega$

Symbol	Test Conditions	Characteristic Values		
		(T <sub>J</sub> = 25°C, unless otherwise specified)		
		min.	typ.	max.
<b>g<sub>fs</sub></b>	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 15 A, pulse test	24	32	S
<b>C<sub>iss</sub></b>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		3950	pF
<b>C<sub>oss</sub></b>			510	pF
<b>C<sub>rss</sub></b>			177	pF
<b>t<sub>d(on)</sub></b>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0.5 • V <sub>DSS</sub> , I <sub>D</sub> = 30A R <sub>G</sub> = 3.6 Ω (External)		19	ns
<b>t<sub>r</sub></b>			19	ns
<b>t<sub>d(off)</sub></b>			79	ns
<b>t<sub>f</sub></b>			17	ns
<b>Q<sub>g(on)</sub></b>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0.5 • V <sub>DSS</sub> , I <sub>D</sub> = 0.5 I <sub>D25</sub>		136	nC
<b>Q<sub>gs</sub></b>			32	nC
<b>Q<sub>gd</sub></b>			52	nC
<b>R<sub>thJC</sub></b>			0.65	K/W
<b>R<sub>thCK</sub></b>			0.25	K/W

### Source-Drain Diode

Symbol	Test Conditions	Characteristic Values		
		(T <sub>J</sub> = 25°C, unless otherwise specified)		
		min.	typ.	max.
<b>I<sub>S</sub></b>	V <sub>GS</sub> = 0 V			30 A
<b>I<sub>SM</sub></b>	Repetitive; pulse width limited by T <sub>JM</sub>			120 A
<b>V<sub>SD</sub></b>	I <sub>F</sub> = I <sub>S</sub> , V <sub>GS</sub> = 0 V, Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %			1.5 V
<b>t<sub>rr</sub></b>	I <sub>F</sub> = I <sub>S</sub> , -di/dt = 100 A/μs, V <sub>R</sub> = 100 V		300	ns
<b>Q<sub>rr</sub></b>			3.0	μC

### TO-247 AD Outline



Terminals: 1 - Gate 2 - Drain  
3 - Source Tab - Drain

Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.7	5.3	.185	.209
A <sub>1</sub>	2.2	2.54	.087	.102
A <sub>2</sub>	2.2	2.6	.059	.098
b	1.0	1.4	.040	.055
b <sub>1</sub>	1.65	2.13	.065	.084
b <sub>2</sub>	2.87	3.12	.113	.123
C	.4	.8	.016	.031
D	20.80	21.46	.819	.845
E	15.75	16.26	.610	.640
e	5.20	5.72	0.205	0.225
L	19.81	20.32	.780	.800
L1		4.50		.177
øP	3.55	3.65	.140	.144
Q	5.89	6.40	0.232	0.252
R	4.32	5.49	.170	.216
S	6.15	BSC	.242	BSC

IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETS and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,881,106 5,017,508 5,049,961 5,187,117 5,486,715  
4,850,072 4,931,844 5,034,796 5,063,307 5,237,481 5,381,025