Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

Fairchild Semiconductor DM74ALS30AN

For any questions, you can email us directly: sales@integrated-circuit.com





September 1986 Revised February 2000

DM74ALS30A 8-Input NAND Gate

General Description

This device contains a single gate, which performs the logic NAND function.

Features

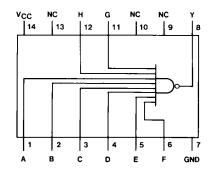
- Switching specifications at 50 pF
- \blacksquare Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky and low power Schottky TTL counterpart
- Improved AC performance over Schottky and low power Schottky counterparts

Ordering Code:

Order Number	Package Number	Package Description
DM74ALS30AM	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74ALS30ASJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
DM74ALS30AN	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

Y = ABCDEFGH

Inputs	Output
A thru H	Υ
All Inputs H	L
One or More	Н
Input L	

H = HIGH Logic Level L = LOW Logic Level

Distributor of Fairchild Semiconductor: Excellent Integrated System Limited

Datasheet of DM74ALS30AN - IC GATE NAND 1CH 8-INP 14-DIP

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

DM74ALS30A

Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range $0^{\circ}\text{C to } + 70^{\circ}\text{C}$ Storage Temperature Range $-65^{\circ}\text{C to } + 150^{\circ}\text{C}$

Typical θ_{JA}

N Package 86.5°C/W M Package 116.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
I _{OH}	HIGH Level Output Current			-0.4	mA
I _{OL}	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V, I_I = -18 \text{ mA}$				-1.5	V
V _{OH}	HIGH Level Output Voltage	$I_{OH} = -0.4 \text{ mA}$ $V_{CC} = 4.5 \text{V to } 5.5 \text{V}$	1				V
V _{OL}	LOW Level Output Voltage	V _{CC} = 4.5V	$I_{OL} = 8 \text{ mA}$		0.35	0.5	V
l _l	Input Current at Max Input Voltage	V _{CC} = 5.5V, V _{IH} = 7V				0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$				20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$				-0.1	mA
I _O	Output Drive Current	V _{CC} = 5.5V	$V_0 = 2.25V$	-30		-112	mA
I _{CC}	Supply Current	V _{CC} = 5.5V	Outputs HIGH		0.22	0.36	mA
			Outputs LOW		0.54	0.90	mA

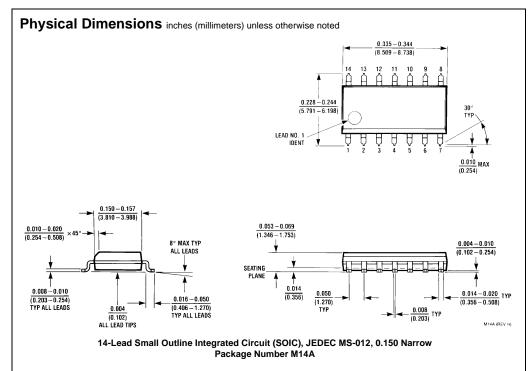
Switching Characteristics

over recommended operating free air temperature range

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	V _{CC} = 4.5V to 5.5V	2	10	ns
	LOW-to-HIGH Level Output	$R_L = 500\Omega$	3	10	115
t _{PHL}	Propagation Delay Time	C _L = 50 pF	2	12	ns
	HIGH-to-LOW Level Output		3	12	115

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

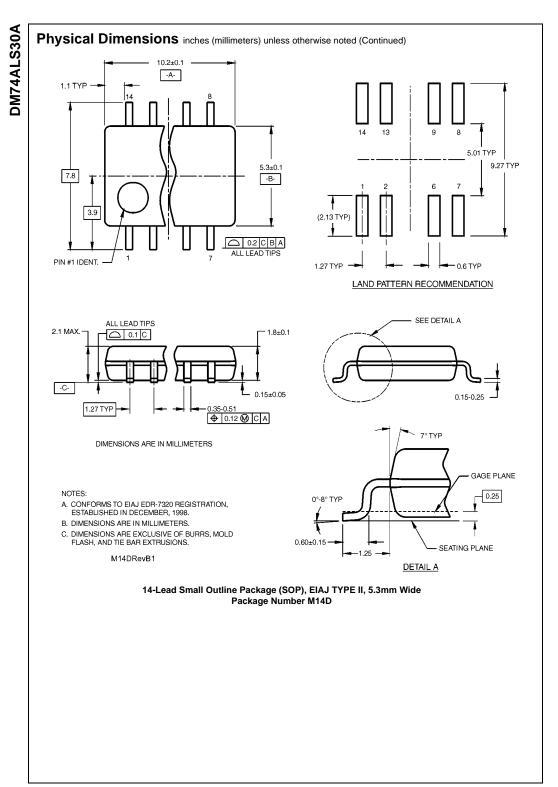




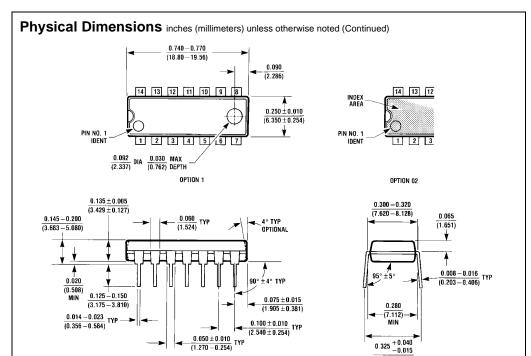
Distributor of Fairchild Semiconductor: Excellent Integrated System Limited

Datasheet of DM74ALS30AN - IC GATE NAND 1CH 8-INP 14-DIP

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com







14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

Fairchild does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and Fairchild reserves the right at any time without notice to change said circuitry and specifications.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

 $\left(8.255 + 1.016 - 0.381\right)$

www.fairchildsemi.com

N14A (REV.F)