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STMicroelectronics STPS1H100MF

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STPS1H100MF

High voltage power Schottky rectifier

Features

- Negligible switching losses
- High junction temperature capability
- Low leakage current
- Good trade-off between leakage current and forward voltage drop
- Avalanche capability specified

Description

Schottky rectifier designed for high frequency miniature switch mode power supplies such as adaptors and on-board DC/DC convertors. This device is packaged in STmite flat.

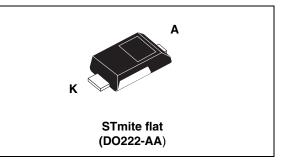


Table 1. Device summary

I _{F(AV)}	1 A
V _{RRM}	100 V
T _j (max)	175 °C
V _F (max)	0.62 V

1/7



Characteristics

STPS1H100MF

1 Characteristics

Table 2. Absolute ratings (limiting values)

Symbol	Paramete	Value	Unit	
V _{RRM}	Repetitive peak reverse voltage	100	V	
I _{F(RMS)}	Forward current rms	2	А	
I _{F(AV)}	Average forward current	$T_{c} = 160 \ ^{\circ}C \ \delta = 0.5$	1	А
I _{FSM}			50	А
I _{RRM}			1	А
I _{RSM}			1	А
P _{ARM}	Repetitive peak avalanche power $t_p = 1 \ \mu s \ T_j = 25 \ ^{\circ}C$		1500	W
T _{stg}	Storage temperature range	-65 to + 175	°C	
Тj	Maximum operating junction tempera	175	°C	
dV/dt	Critical rate of rise of reverse voltage	10000	V/µs	

1. $\frac{dPtot}{dTi} < \frac{1}{Rth(i-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case	20	°C/W

Table 4. Static electrical characteristics

Symbol	Parameter	Tests conditions		Min.	Тур	Max.	Unit
I _B ⁽¹⁾	Reverse leakage current	T _j = 25 °C	V _R = V _{RRM}			4	μA
'R '		T _j = 125 °C			0.2	0.5	mA
		$T_j = 25 \ ^{\circ}C$	I _F = 1 A			0.77	
V _F ⁽²⁾	Forward voltage drop	T _j = 125 °C			0.58	0.62	v
VF` ′		T _j = 25 °C	I _F = 2 A			0.86	v
		T _j = 125 °C			0.65	0.7	

1. Pulse test: = 5 ms, δ < 2%

2. Pulse test: = 380 μ s, δ < 2%

To evaluate the conduction losses use the following equation: P = 0.54 x $I_{F(AV)}$ + 0.08 $I_{F}^{2}_{(RMS)}$



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STPS1H100MF

Characteristics

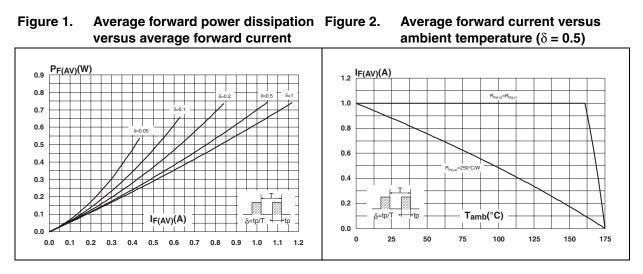


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

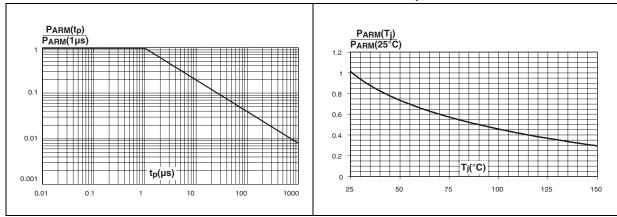
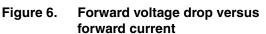
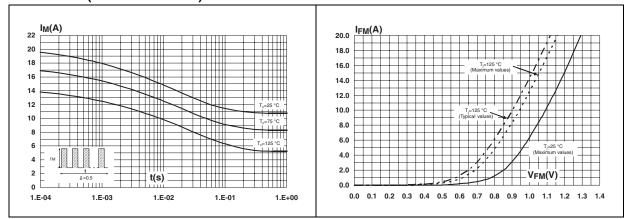


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values)

57







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Characteristics

STPS1H100MF

- Figure 7. Relative variation of thermal impedance, junction to ambient, versus pulse duration (epoxy printed circuit board, copper thickness = 35 μm, recommended pad layout)
- Figure 8. Thermal resistance, junction to ambient, versus copper surface under each lead (epoxy printed board FR4, copper thickness = 35 μm)

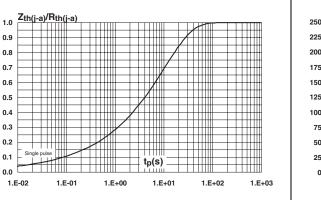


Figure 9. Reverse leakage current versus voltage applied (typical values)

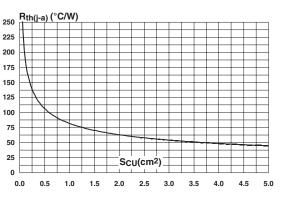
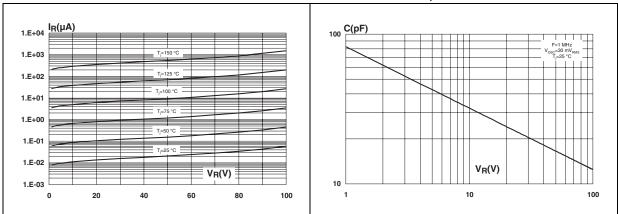


Figure 10. Junction capacitance versus reverse voltage applied (typical values)







57

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STPS1H100MF

Package information

2 Package information

• Epoxy meets UL94, V0

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at *www.st.com*.

Table 5.STmite flat dimensions

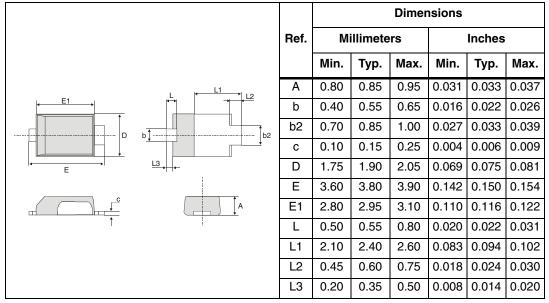
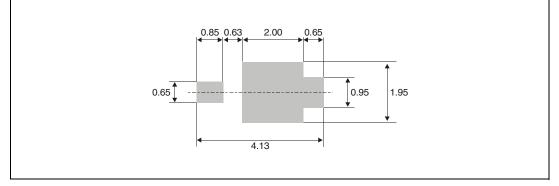


Figure 11. STmite flat recommended footprint (all dimensions in mm)





Ordering information

STPS1H100MF

57

3 Ordering information

Table 6.Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS1H100MF	M11	STmite flat	16 mg	12000	Tape and reel

4 Revision history

Table 7.Document revision history

Date	Revision	Changes
15-May-2008	1	First issue.





STPS1H100MF

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