

Excellent Integrated System Limited

Stocking Distributor

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<u>Vishay Semiconductor/Diodes Division</u> <u>UH4PBC-M3/87A</u>

For any questions, you can email us directly: sales@integrated-circuit.com

Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite Datasheet of UH4PBC-M3/87A - DIODE ARRAY GP 100V 2A TO277A

FEATURES

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



UH4PBC, UH4PCC, UH4PDC

• Very low profile - typical height of 1.1 mm

• Ultrafast recovery times for high frequency • Low forward voltage drop, low power loss

· Ideal for automated placement · Oxide planar chip junction

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AUTOMOTIVE GRADE Available

HALOGEN

For definitions of compliance

High Current Density Surface Mount Ultrafast Rectifiers



LF maximum peak of 260 °C
 AEC-Q101 qualified
(SMPC) • Material categorization: For definitions of please see www.vishay.com/doc?99912

PRIMARY CHARACTERISTICS						
I _{F(AV)}	2 x 2.0 A					
V_{RRM}	100 V, 150 V, 200 V					
I _{FSM}	40 A					
t _{rr}	25 ns					
V_F at $I_F = 2.0 \text{ A}$	0.77 V					
T _J max.	175 °C					

MECHANICAL DATA

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and automotive grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102 M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix

meets JESD 201 class 2 whisker test

TYPICAL APPLICATIONS

high frequency rectification freewheeling application in switching mode converters and inverters for consumer computer, automotive, telecommunication applications.

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	UH4PBC	UH4PCC	UH4PDC	UNIT
Device marking code			H4BC	H4CC	H4DC	
Maximum repetitive peak reverse voltage		V_{RRM}	100	150	200	V
Maximum average forward rectified current (fig. 1)	total devive		4.0		А	
	per diode	I _{F(AV)}	2.0			
Peak forward surge current 10 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	40		А	
Operating junction and storage temperature range		T _J , T _{STG}	- 55 to + 175			°C

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PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 1.0 A	T _A = 25 °C	V _F ⁽¹⁾	0.84	-	V
	I _F = 2.0 A	1A = 23 C		0.93	1.05	
	I _F = 1.0 A	T _A = 125 °C		0.68	-	
	I _F = 2.0 A			0.77	0.85	
Reverse current per diode	Rated V _R	T _A = 25 °C	I _R ⁽²⁾	-	5	μΑ
	nated v _R	T _A = 125 °C		6.4	25	
Maximum reverse recovery time per diode	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		- t _{rr}	20	25	ns
Typical reverse recovery time per diode	$I_F = 1.0 \text{ A}, \text{ dI/dt} = 50 \text{ A/}\mu\text{s}, \\ V_R = 30 \text{ V}, I_{rr} = 0.1 I_{RM}$			24	-	
Typical softness factor (t _b /t _a)per diode	- I _F = 2 A, dl/dt = 200 A/μs, V _R = 200 V, I _{rr} = 0.1 I _{RM} T _A = 125 °C		S	0.3	-	-
Typical reverse recovery current per diode			I _{RM}	5.4	-	Α
Typical stored charge per diode			Q_{rr}	88	-	nC
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	21	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	UH4PBC UH4PCC UH4PDC		UNIT			
Typical thermal resistance per diode	R ₀ JA (1)	60			°C/W		
Typical thermal resistance per diode	$R_{ hetaJL}$	4			C/VV		

Note

⁽¹⁾ Units mounted on recommended PCB 1 oz. pad layout

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
UH4PDC-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel			
UH4PDC-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel			
UH4PDCHM3/86A (1)	0.10	86A	1500	7" diameter plastic tape and reel			
UH4PDCHM3/87A (1)	0.10	87A	6500	13" diameter plastic tape and reel			

Note

(1) Automotive grade



UH4PBC, UH4PCC, UH4PDC

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RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

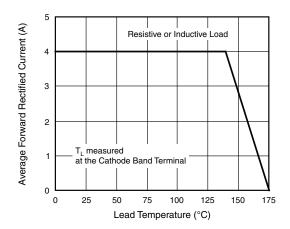


Fig. 1 - Maximum Forward Current Derating Curve

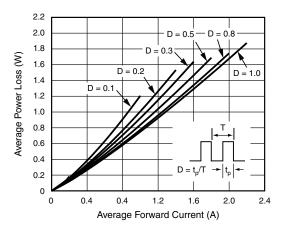


Fig. 2 - Forward Power Loss Characteristics Per Diode

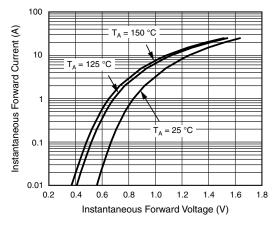


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

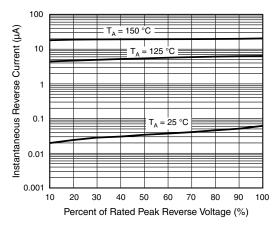


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

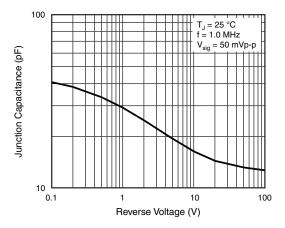


Fig. 5 - Typical Junction Capacitance Per Diode

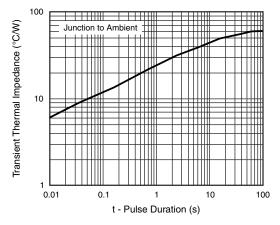


Fig. 6 - Typical Transient Thermal Impedance Per Diode



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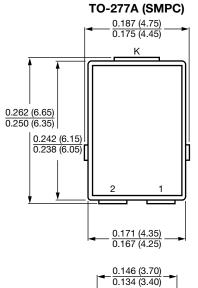
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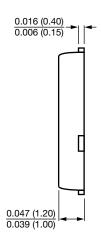


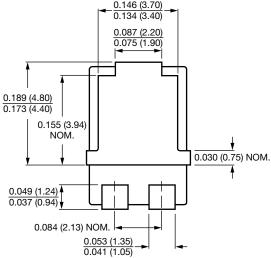
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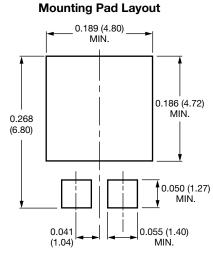
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)









Conform to JEDEC TO-277A



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