

SANYO Semiconductors DATA SHEET

SCH2201 — NPN Epitaxial Planar Silicon Transistor

Switching, Driver Applications

Applications

· Low-frequency General-Purpose amplifier, high-speed switching, motor drivers, muting.

Features

- · Composite type with 2 NPN transistors contained in a single package, facilitating high-density mounting.
- Ultrasmall package permitting applied sets to be small and slim.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		20	V
Collector-to-Emitter Voltage	VCEO		15	٧
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		0.8	Α
Collector Current (Pulse)	ICP		1.6	Α
Collector Dissipation	PC	When mounted on ceramic substrate (900mm²X0.8mm) 1unit	0.4	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =12V, I _E =0A			100	nA
Emitter Cutoff Current	I _{EBO}	V _{EB} =4V, I _C =0A			100	nA
DC Current Gain	hFE	VCE=2V, IC=50mA	300		800	
Gain-Bandwidth Product	fT	V _{CE} =2V, I _C =50mA		440		MHz
Output Capacitance	Cob	VCR=10V f=1MHz		4		рF

Marking: EB Continued on next page.

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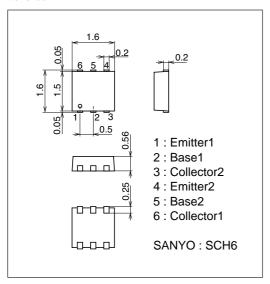
SCH2201

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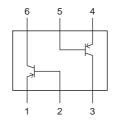
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=400mA, IB=20mA		140	280	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =400mA, I _B =20mA		0.8	1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =10μA, I _E =0A	20			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	15			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =10μA, I _C =0A	5			V
Turm-ON Time	ton	See specified test circuit.		30		ns
Storage Time	t _{stg}	See specified test circuit.		165		ns
Fall Time	tf	See specified test circuit.		25		ns

Package Dimensions

unit : mm (typ) 7028-004



Electrical Connection



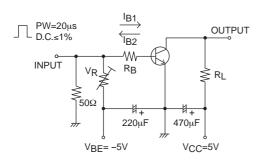
1 : Emitter1 2 : Base1

3 : Collector2 4 : Emitter2

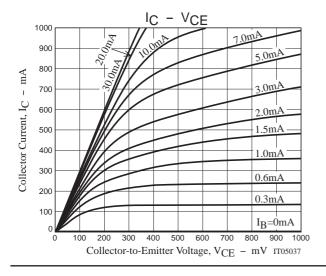
5 : Base2 6 : Collector1

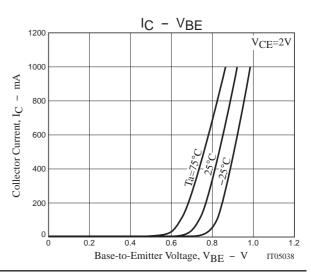
Top view

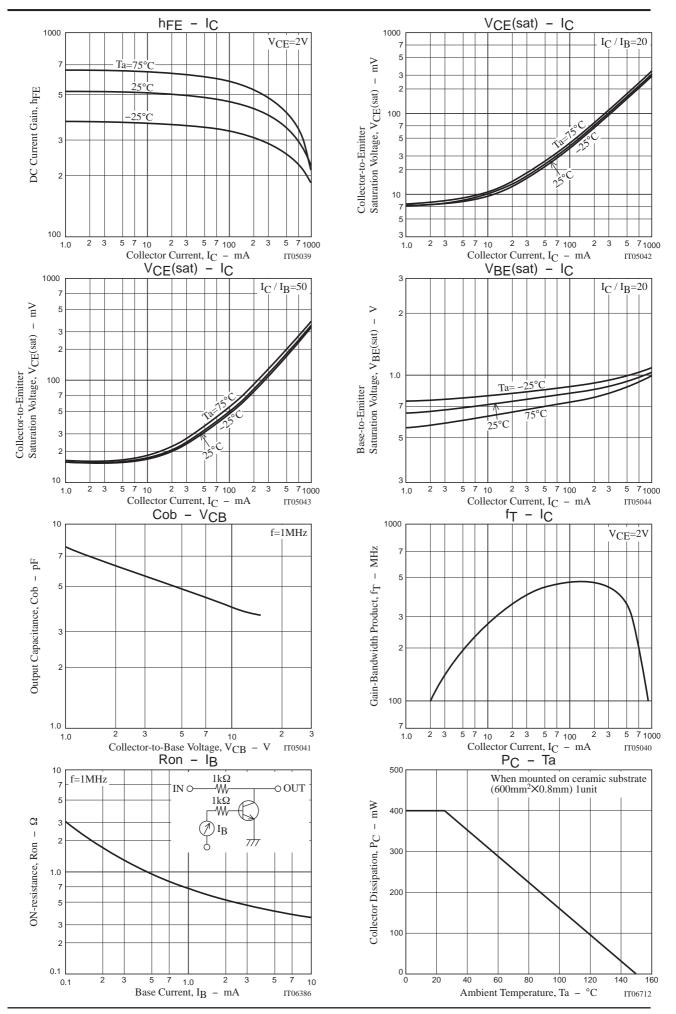
Switching Time Test Circuit



$$I_{C}=20I_{B1}=-20I_{B2}=400mA$$







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