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[DMG9926USD-13](#)

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**DMG9926USD**

**DUAL N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR**

NEW PRODUCT

**Product Summary**

$V_{(BR)DSS}$	$R_{DS(ON)} \text{ max}$	$I_D \text{ max}$ $T_A = +25^\circ\text{C}$
20V	$24\text{m}\Omega @ V_{GS} = 4.5\text{V}$	8A
	$29\text{m}\Omega @ V_{GS} = 2.5\text{V}$	5.5A
	$37\text{m}\Omega @ V_{GS} = 1.8\text{V}$	4.8A

**Description**

This MOSFET has been designed to minimize the on-state resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

**Applications**

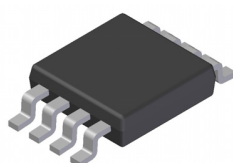
- Power Management Functions
- DC-DC Converters
- 

**Features**

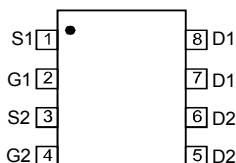
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

**Mechanical Data**

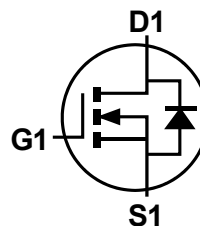
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.072g (approximate)



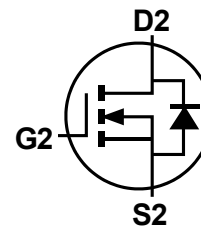
TOP VIEW



TOP VIEW  
Internal Schematic



N-Channel MOSFET



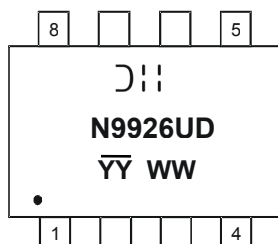
N-Channel MOSFET

**Ordering Information (Note 4)**

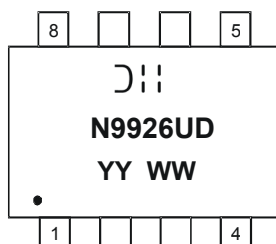
Part Number	Case	Packaging
DMG9926USD-13	SO-8	2,500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

**Marking Information**



Chengdu A/T Site



Shanghai A/T Site

☺ = Manufacturer's Marking  
 N9926UD = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Year (ex: 13 = 2013)  
 WW = Week (01 - 53)  
 YY = Date Code Marking for SAT (Shanghai Assembly/ Test site)  
 YY = Date Code Marking for CAT (Chengdu Assembly/ Test site)



**DMG9926USD**

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
Drain Current (Note 5)	Steady State	T <sub>A</sub> = +25°C	I <sub>D</sub>	8	A
		T <sub>A</sub> = +70°C		6.7	
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	30	A

## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	1.3	W
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	96	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	—	0.9	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	19	24	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 8.2A
			23	29		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 3.3A
			29	37		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 2A
Forward Transfer Admittance	Y <sub>fs</sub>	—	7	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4A
Diode Forward Voltage	V <sub>SD</sub>	0.5	—	0.9	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	867	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	85	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	81	—	pF	
Gate Resistance	R <sub>G</sub>	—	1.29	—	Ω	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz
<b>SWITCHING CHARACTERISTICS (Note 8)</b>						
Total Gate Charge	Q <sub>g</sub>	—	8.8	—	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 8.2A
Gate-Source Charge	Q <sub>gs</sub>	—	1.2	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	3	—	nC	
Turn-On Delay Time	t <sub>d(on)</sub>	—	13.2	—	ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.5V, R <sub>L</sub> = 10Ω, R <sub>G</sub> = 6Ω
Turn-On Rise Time	t <sub>r</sub>	—	12.6	—	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	—	64.8	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	21.7	—	ns	

- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout.
  - Repetitive rating, pulse width limited by function temperature.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.



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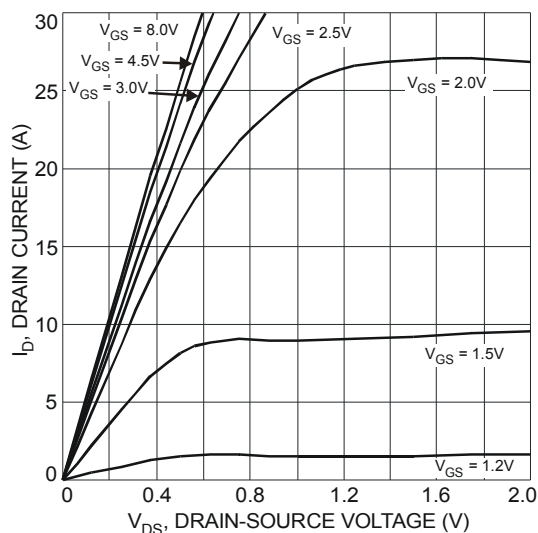


Fig. 1 Typical Output Characteristics

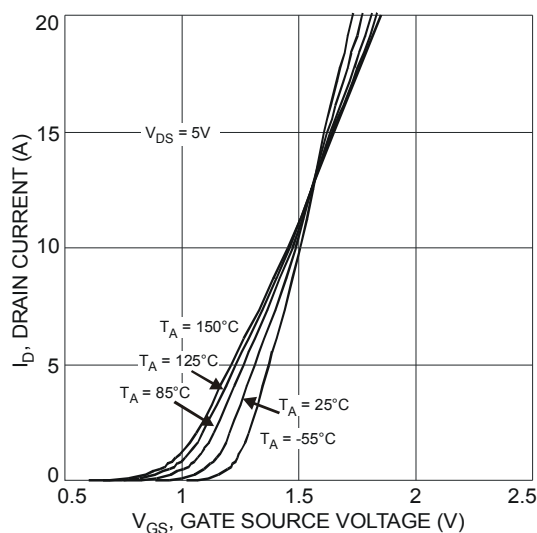


Fig. 2 Typical Transfer Characteristics

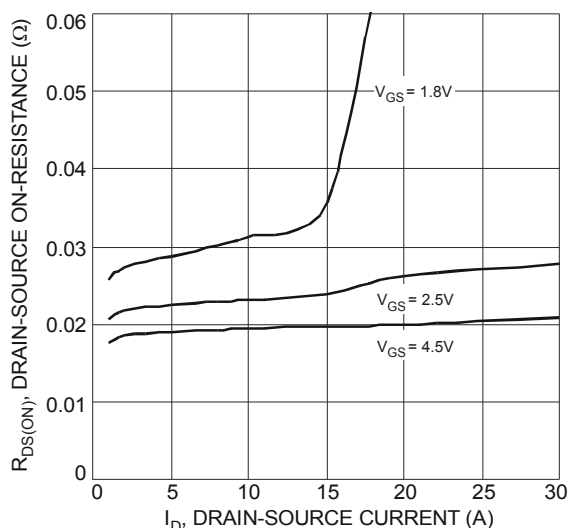


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

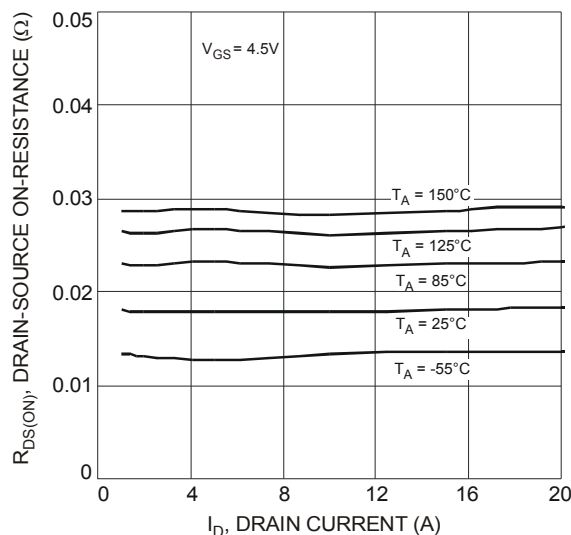


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

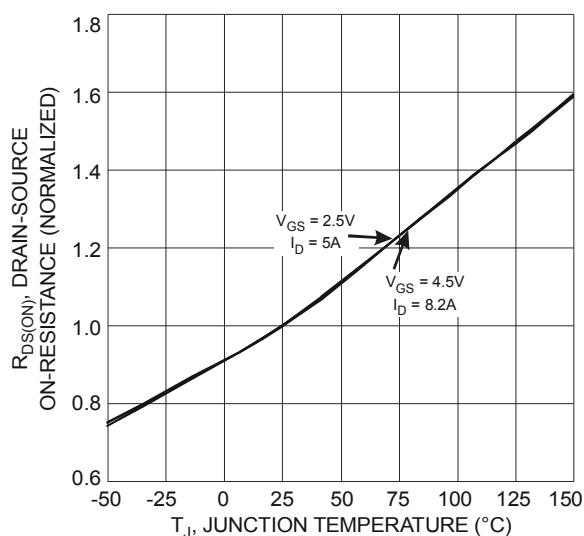


Fig. 5 On-Resistance Variation with Temperature

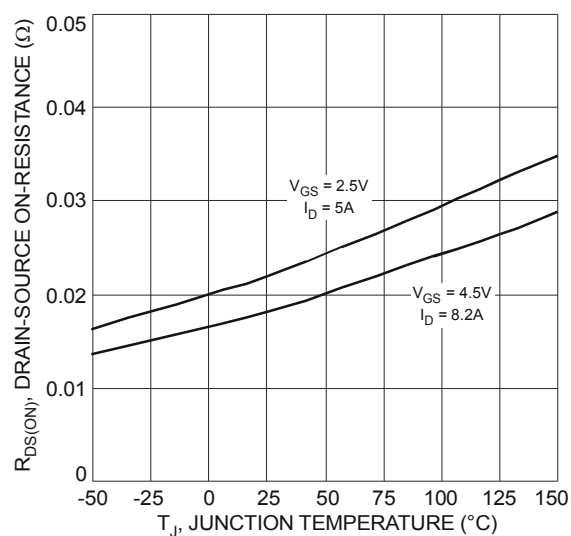


Fig. 6 On-Resistance Variation with Temperature



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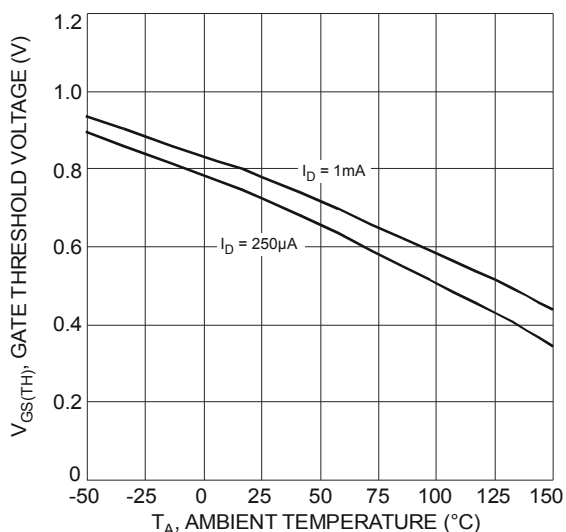


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

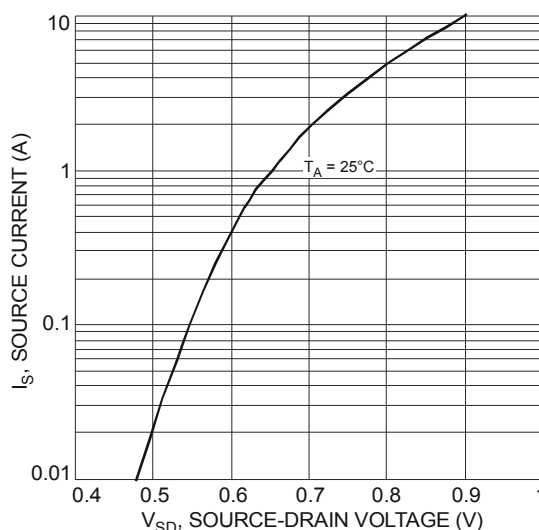


Fig. 8 Diode Forward Voltage vs. Current

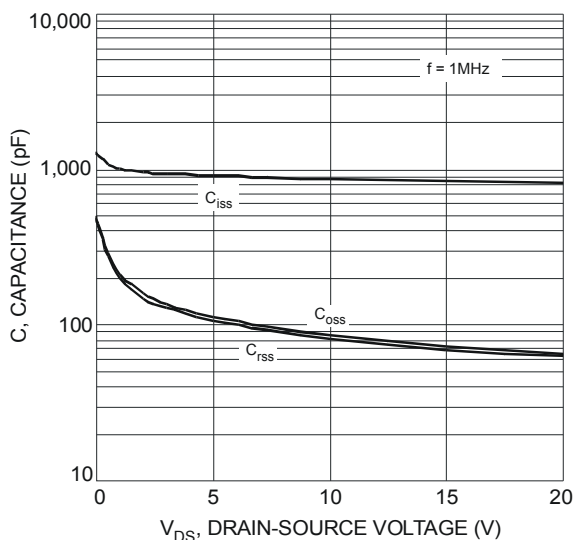


Fig. 9 Typical Capacitance

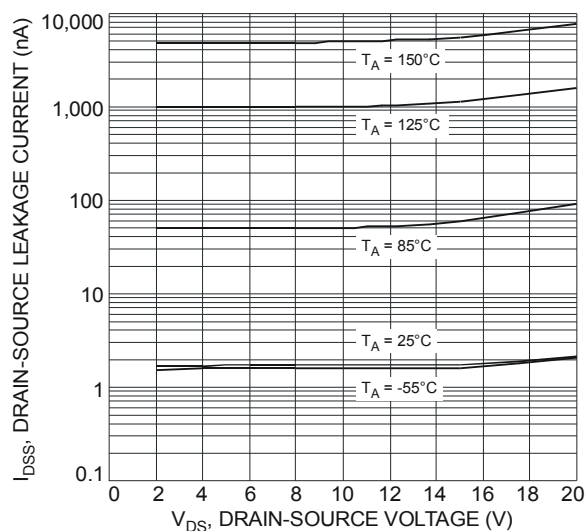


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

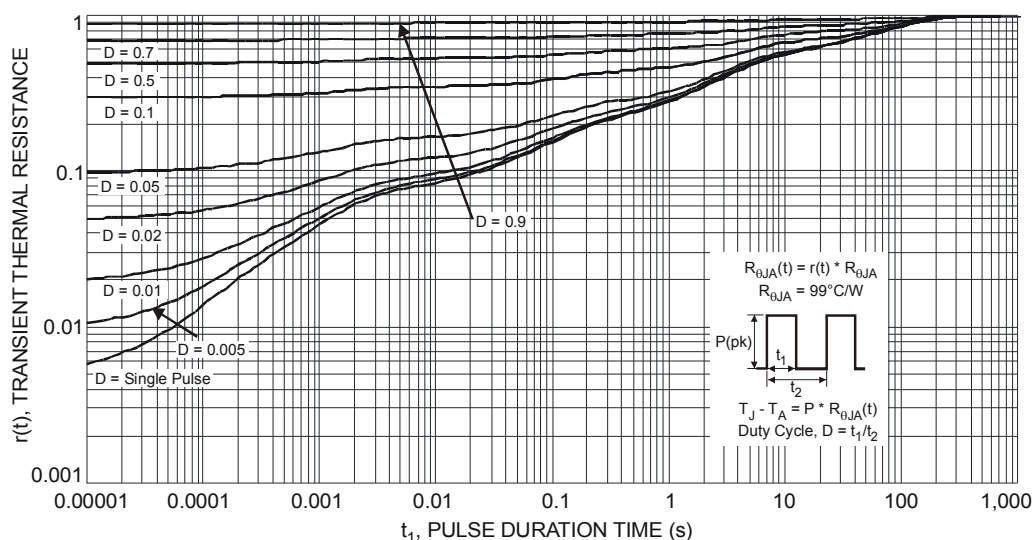
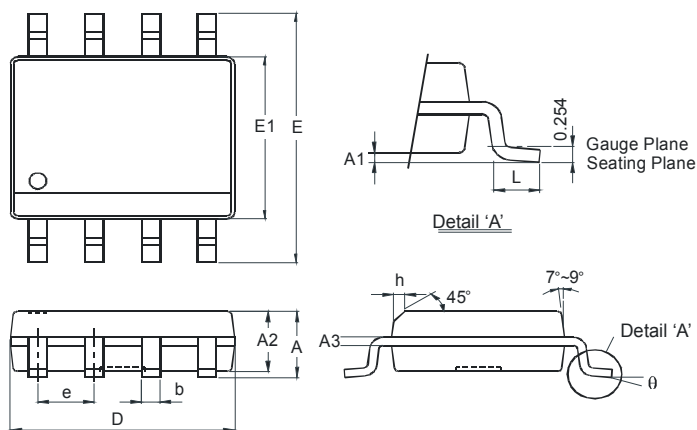


Fig. 11 Transient Thermal Response

## Package Outline Dimensions

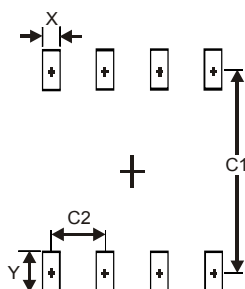
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOP-8L		
Dim	Min	Max
<b>A</b>	-	1.75
<b>A1</b>	0.10	0.20
<b>A2</b>	1.30	1.50
<b>A3</b>	0.15	0.25
<b>b</b>	0.3	0.5
<b>D</b>	4.85	4.95
<b>E</b>	5.90	6.10
<b>E1</b>	3.85	3.95
<b>e</b>	1.27 Typ	
<b>h</b>	-	0.35
<b>L</b>	0.62	0.82
<b>θ</b>	0°	8°
All Dimensions in mm		

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
<b>X</b>	0.60
<b>Y</b>	1.55
<b>C1</b>	5.4
<b>C2</b>	1.27



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NEW PRODUCT

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