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Diodes Incorporated DMN2040LTS-13

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# Distributor of Diodes Incorporated: Excellent Integrated System Limited

Datasheet of DMN2040LTS-13 - MOSFET 2N-CH 20V 6.7A 8TSSOP

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DMN2040LTS

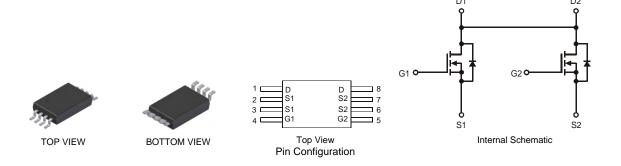
#### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Features**

- **Dual N-Channel MOSFET**
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: TSSOP-8L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.039 grams (approximate)



#### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage		$V_{GSS}$	±12	V	
Continuous Drain Current (Note 3)	Steady State	$T_A = 25$ °C $T_A = 70$ °C	I <sub>D</sub>	6.7 4.9	А
Pulsed Drain Current (Note 4)			I <sub>DM</sub>	30	Α

#### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P <sub>D</sub>	0.89	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = 25°C	$R_{\theta JA}$	140	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

- 1. No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
  Device mounted on FR-4 PCB with minimum recommended pad layout.
- 4. Repetitive rating, pulse width limited by junction temperature.





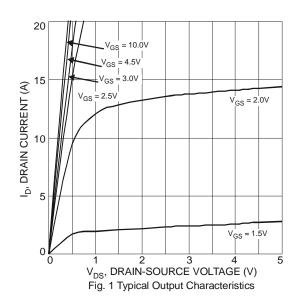
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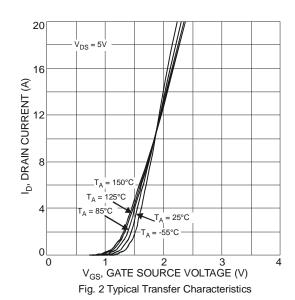
#### Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	1	-	1.0	μΑ	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	-	1.2	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
Static Drain-Source On-Resistance	D		19	26 36	mΩ	$V_{GS} = 4.5V, I_D = 6.0A$
Static Diam-Source Off-Resistance	R <sub>DS</sub> (ON)	-	26			$V_{GS} = 2.5V, I_D = 5.2A$
Forward Transfer Admittance	Y <sub>fs</sub>	1	8	-	S	$V_{DS} = 10V, I_D = 6A$
Diodes Forward Voltage	$V_{SD}$	-	0.7	1.2	V	Is = 1.7A, V <sub>GS</sub> = 0V
DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	C <sub>iss</sub>	ı	570	-	рF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	Coss	ı	85	-	рF	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	75	-	pF	
Gate Resistance	Rg	-	1.23	-	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
SWITCHING CHARACTERISTICS (Note 6)						
Total Gate Charge	Qg	ı	5.2	-	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 7A
Gate-Source Charge	$Q_{gs}$	-	0.86	-	nC	
Gate-Drain Charge	$Q_{gd}$	-	1.25	-	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	-	5.2	-	ns	
Turn-On Rise Time	t <sub>r</sub>	-	13.5	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t <sub>D(off)</sub>	-	19.8	-	ns	$R_L = 1.5\Omega$ , $R_G = 1\Omega$
Turn-Off Fall Time	t <sub>f</sub>	-	6.1	-	ns	

Notes:

- 5. Short duration pulse test used to minimize self-heating effects.
- 6. Guaranteed by design. Not subject to production testing.

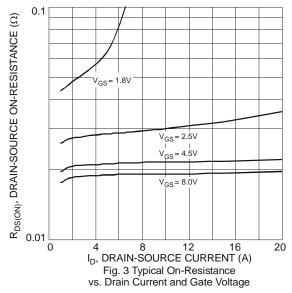


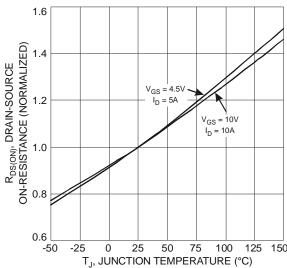


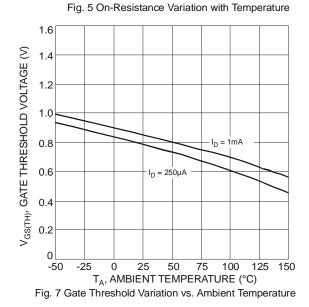




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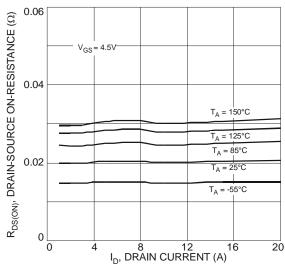


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

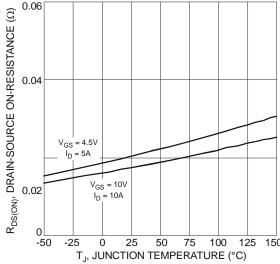


Fig. 6 On-Resistance Variation with Temperature

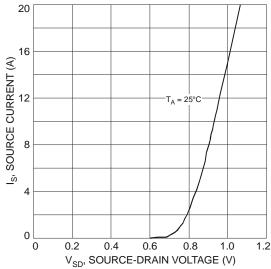
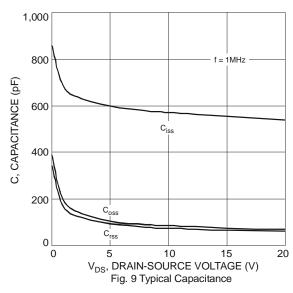


Fig. 8 Diode Forward Voltage vs. Current





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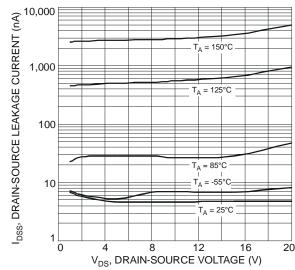
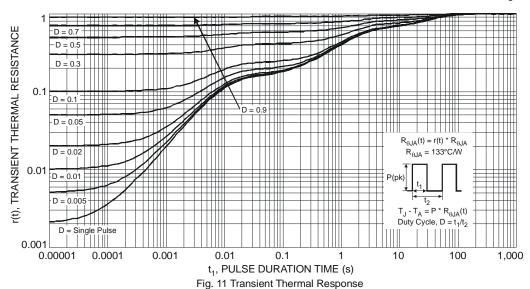


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

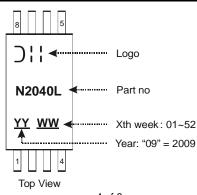


Ordering Information (Note 7)

1			
	Part Number	Case	Packaging
١	DMN2040LTS-13	TSSOP-8L	2500 / Tape & Reel

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



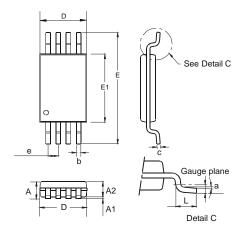
DMN2040LTS Document number: DS31941 Rev. 2 - 2 4 of 6 www.diodes.com





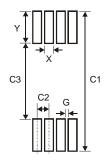
DMN2040LTS

# **Package Outline Dimensions**



TSSOP-8L				
Dim	Min	Max	Тур	
а	0.09	-	-	
Α	-	1.20	-	
A1	0.05	0.15	-	
A2	0.825	1.025	0.925	
b	0.19	0.30	_	
С	0.09	0.20	_	
D	2.90	3.10	3.025	
е	_	_	0.65	
Е	_	_	6.40	
E1	4.30	4.50	4.425	
L	0.45	0.75	0.60	
All	All Dimensions in mm			

### **Suggested Pad Layout**



Dimensions	Value (in mm)
Х	0.45
Y	1.78
C1	7.72
C2	0.65
C3	4.16
G	0.20



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