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Skyworks Solutions Inc.
AA101-80LF

For any questions, you can email us directly:
sales@integrated-circuit.com

DATA SHEET

AA101-80, AA101-80LF: GaAs IC 5-Bit Digital Attenuator 1 dB LSB Positive Control 0.5–2.5 GHz

Features

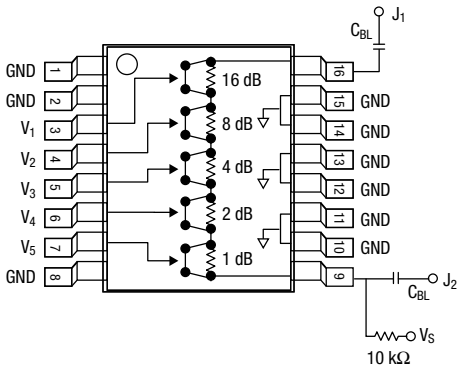
- Attenuation 1 dB steps to 31 dB with high accuracy
- Single positive control (3 to 5 V) for each bit
- Low DC power consumption
- Small low-cost SSOP-16 plastic package
- Available lead (Pb)-free and RoHS-compliant MSL-1 @ 260 °C per JEDEC J-STD-020

Description

The AA101-80 is a 5-bit, single positive control GaAs IC FET digital attenuator in a low-cost SSOP-16 package. This attenuator has an LSB of 1 dB and a total attenuation of 31 dB. The attenuator requires external DC blocking capacitors, positive supply voltage (V_S) and five individual bit control voltages (V_1–V_5). The AA101-80 is particularly suited where high attenuation accuracy, low insertion loss and low intermodulation products are required. Typical applications include base station, wireless data, and wireless local loop gain level control circuits.

NEW Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.

Pin Out



DC blocking capacitors (C_{BL}) and biasing resistor must be supplied externally for positive voltage operation.
C_{BL} = 47 pF for operation >500 MHz.

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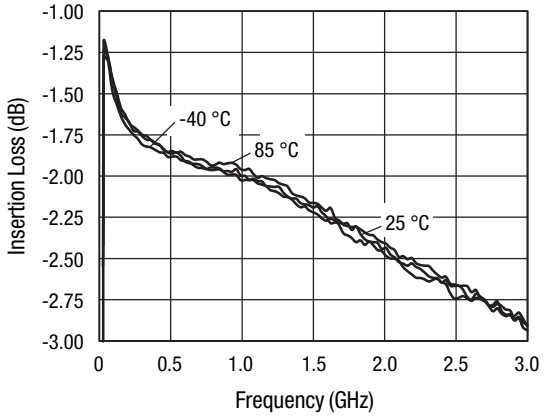
Electrical Specifications at -40 °C to +85 °C (0, 5 V)

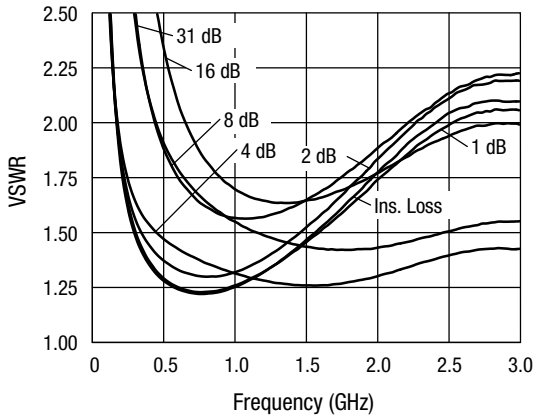
Parameter[1]	Condition	Frequency	Min.	Typ.	Max.	Unit
Insertion loss		0.5–1.0 GHz 1.0–2.0 GHz 2.0–2.5 GHz		2.0 2.6 2.9	2.4 3.0 3.3	dB dB dB
Attenuation range				31		dB
Attenuation accuracy[2]		0.5–1.0 GHz 1.0–2.0 GHz 2.0–2.5 GHz	± (0.2 + 3% of attenuation setting in dB) ± (0.3 + 5% of attenuation setting in dB) ± (0.3 + 6% of attenuation setting in dB)			dB dB dB
VSWR (I/O)[3]		0.5–2.5 GHz		1.5:1	2.2:1	
Switching characteristics Rise, fall On, off Video feedthru	 10/90% or 90/10% RF 50% CTL to 90/10% RF T_{RISE} = 1 ns, BW = 500 MHz			 100 300 70		 ns ns mV
Input power for 1 dB compression	V_S = 3 V V_S = 5 V	0.5–2.5 GHz 0.5–2.5 GHz	18 23	21 27		dBm dBm
Intermodulation intercept point (IP3)	For two-tone input power 5 dBm V_S = 3 V V_S = 5 V	 0.5–2.5 GHz 0.5–2.5 GHz	 37 39	 43 45		 dBm dBm
Control voltages	V_{LOW} = 0 to 0.2 V @ 20 µA max. V_{HIGH} = 3 V @ 100 µA max. to 5 V @ 200 µA max. $V_S = V_{HIGH} \pm 0.2$ V					

1. All measurements made in a 50 Ω system, unless otherwise specified.
2. Attenuation referenced to insertion loss.
3. Input/output.

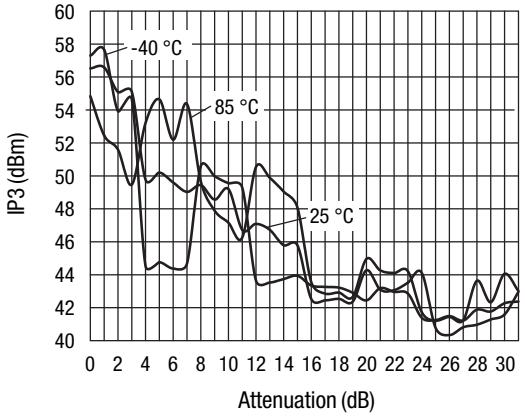
Typical Performance Data (0, 5 V)



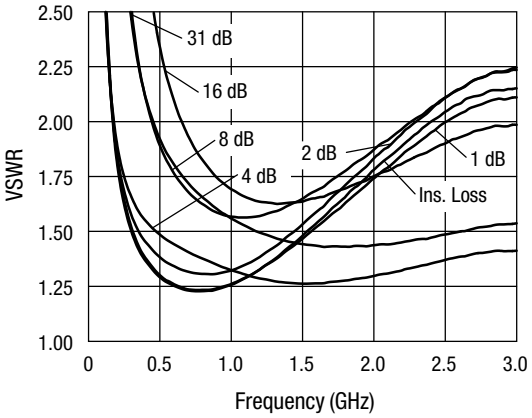
Insertion Loss vs. Frequency



VSWR vs. Frequency (25 ˚C)



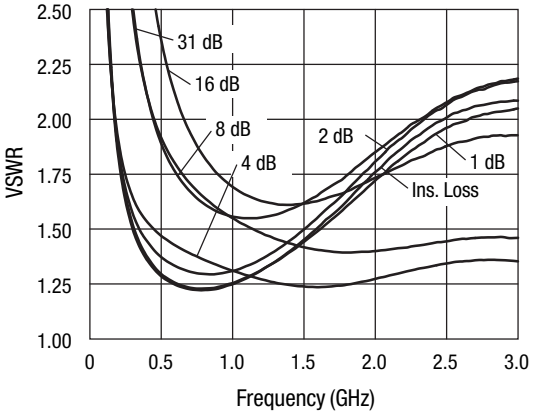
IP3 vs. Attenuation and Temperature (500 MHz)



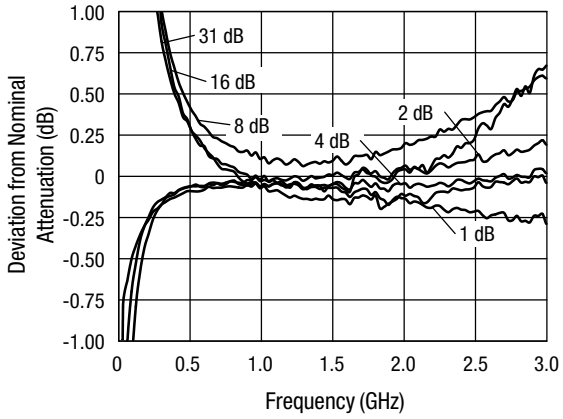
VSWR vs. Frequency (85 ˚C)

Compression Point vs. Attenuation, Voltage, and Temperature

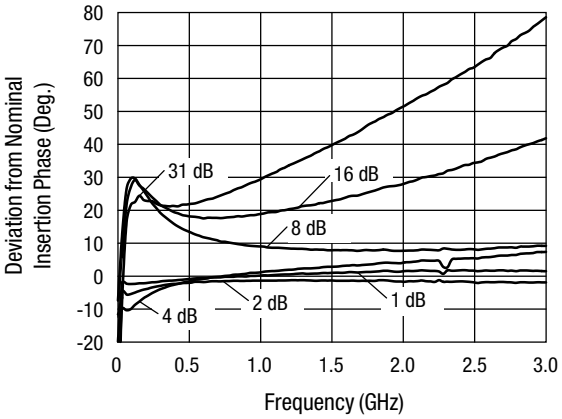
Attenuation State	Control Voltage (V)	Input Power @ 1 dB Compression		
		25 °C (dBm)	85 °C (dBm)	-40 °C (dBm)
Ins. loss	5	29.4	29.2	29.6
1	5	29.3	29.5	29.7
2	5	28.7	28.9	29.0
4	5	34.5	34.3	34.6
8	5	27.4	27.4	27.7
16	5	27.9	27.6	28.2
31	5	27.7	25.2	28.4



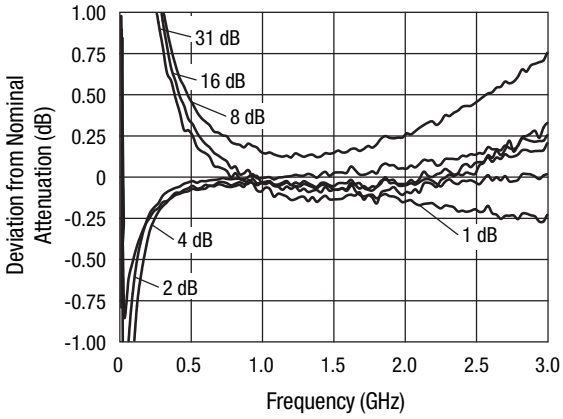
VSWR vs. Frequency (-40 °C)



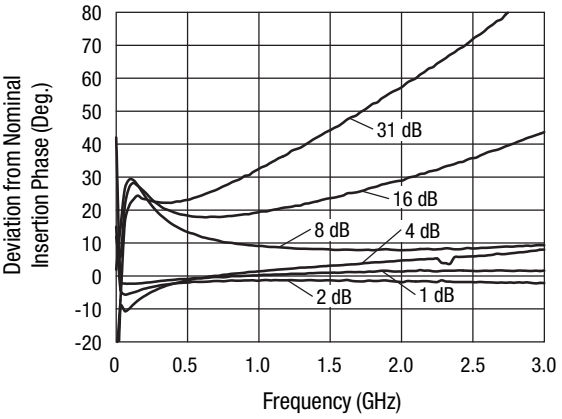
Attenuation Accuracy vs. Frequency (25 ˚C)



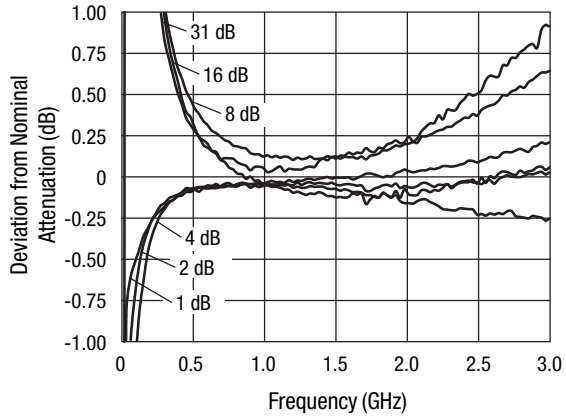
Attenuation Phase Accuracy vs. Frequency (25 ˚C)



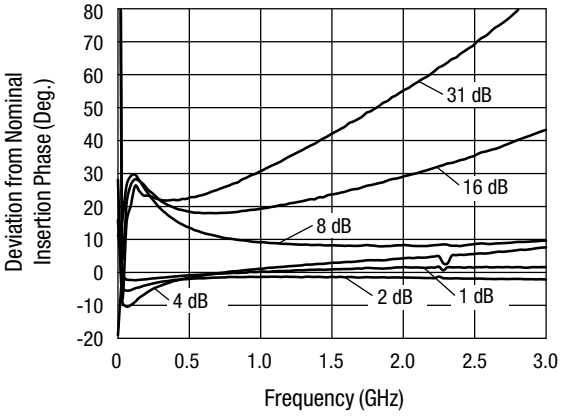
Attenuation Accuracy vs. Frequency (85 °C)



Attenuation Phase Accuracy vs. Frequency (85 °C)

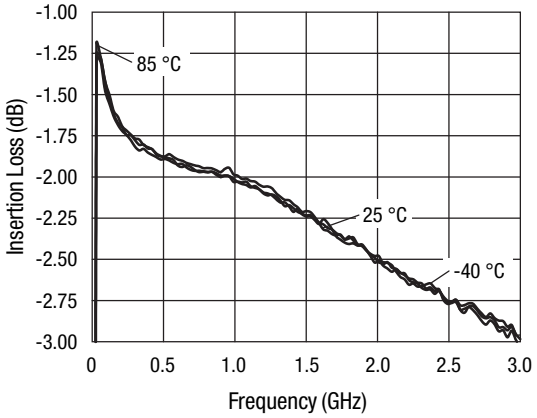


Attenuation Accuracy vs. Frequency (-40 °C)

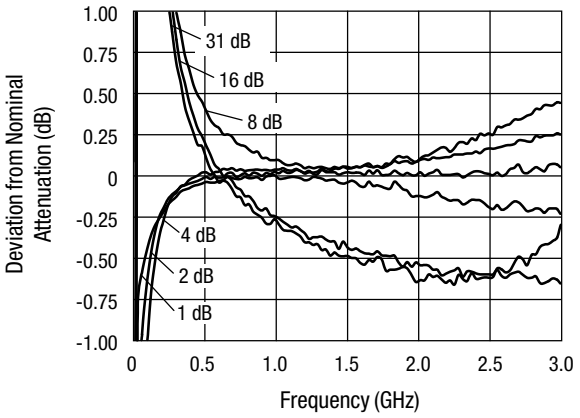


Attenuation Phase Accuracy vs. Frequency (-40 ˚C)

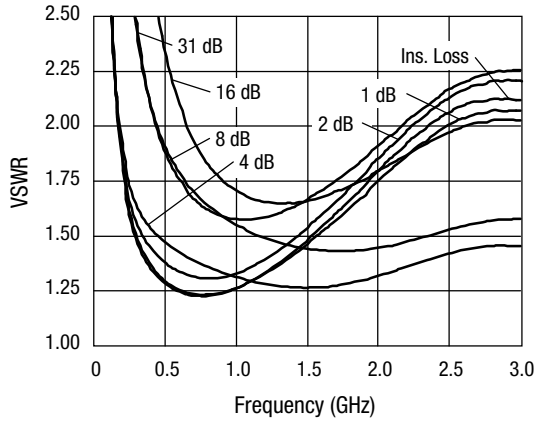
Typical Performance Data (0, 3 V)



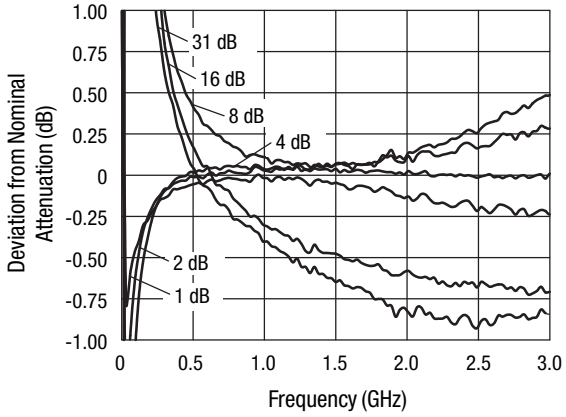
Insertion Loss vs. Frequency



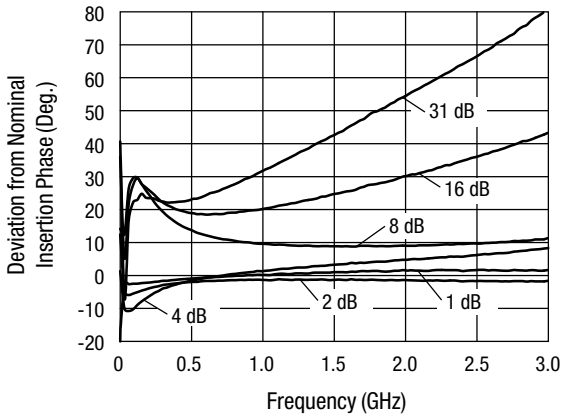
Attenuation Accuracy vs. Frequency (25 ˚C)



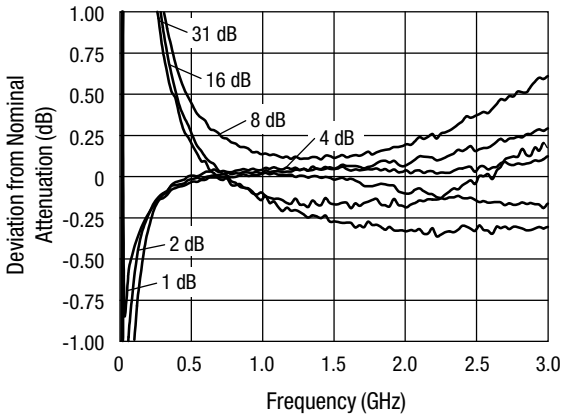
VSWR vs. Frequency (25 ˚C)



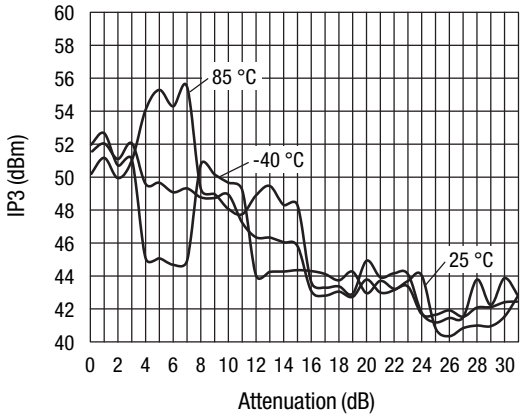
Attenuation Accuracy vs. Frequency (85 °C)



Attenuation Phase Accuracy vs. Frequency (25 ˚C)



Attenuation Accuracy vs. Frequency (-40 ˚C)



IP3 vs. Attenuation and Temperature

Truth Table

V_1	V_2	V_3	V_4	V_5	Attenuation
16 dB	8 dB	4 dB	2 dB	1 dB	J_1–J_2
V_{HIGH}	V_{HIGH}	V_{HIGH}	V_{HIGH}	V_{HIGH}	Reference I.L.
V_{HIGH}	V_{HIGH}	V_{HIGH}	V_{HIGH}	0	1 dB
V_{HIGH}	V_{HIGH}	V_{HIGH}	0	V_{HIGH}	2 dB
V_{HIGH}	V_{HIGH}	0	V_{HIGH}	V_{HIGH}	4 dB
V_{HIGH}	0	V_{HIGH}	V_{HIGH}	V_{HIGH}	8 dB
0	V_{HIGH}	V_{HIGH}	V_{HIGH}	V_{HIGH}	16 dB
0	0	0	0	0	31 dB max. atten.

V_{HIGH} = 3 to 5 V ($V_S = V_{HIGH} \pm 0.2$ V).

Compression Point vs. Attenuation, Voltage, and Temperature

Attenuation State	Control Voltage (V)	Input Power @ 1 dB Compression		
		25 °C (dBm)	85 °C (dBm)	-40 °C (dBm)
Ins. loss	3	23.1	23.3	24.3
1	3	23.5	23.3	24.2
2	3	22.7	22.7	23.7
4	3	33.8	33.2	33.8
8	3	31.7	31.7	22.7
16	3	20.2	20.5	21.4
31	3	21.9	20.6	23.6

Absolute Maximum Ratings

Characteristic	Value
RF input power	2 W > 500 MHz 0/8 V 0.75 W @ 50 MHz 0/8 V
Supply voltage	8 V
Control voltage	-0.2 V, +8 V
Operating temperature	-40 °C to +85 °C
Storage temperature	-65 °C to +150 °C

Performance is guaranteed only under the conditions listed in the specifications table and is not guaranteed under the full range(s) described by the Absolute Maximum specifications. Exceeding any of the absolute maximum/minimum specifications may result in permanent damage to the device and will void the warranty.

CAUTION: *Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.*

Recommended Solder Reflow Profiles

Refer to the *"Recommended Solder Reflow Profile"* Application Note.

Tape and Reel Information

Refer to the *"Discrete Devices and IC Switch/Attenuators Tape and Reel Package Orientation"* Application Note.

SSOP-16