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Si8465DB
Vishay Siliconix

P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, e}	Q _g (Typ.)
- 20	0.104 at V _{GS} = - 4.5 V	- 3.8	6 nC
	0.148 at V _{GS} = - 2.5 V	- 3.2	

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

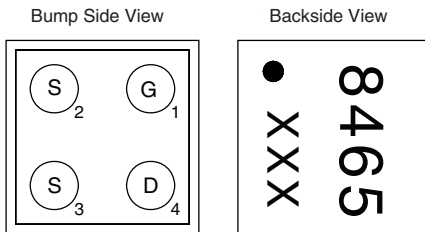


RoHS
COMPLIANT
HALOGEN
FREE

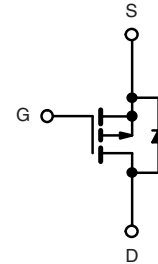
APPLICATIONS

- Load Switches, Battery Switches and Charger Switches in Portable Device Applications
- DC/DC Converters

MICRO FOOT



Device Marking: 8465
xxx = Date/Lot Traceability Code



P-Channel MOSFET

Ordering Information: Si8465DB-T2-E1 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	- 20	V
Gate-Source Voltage	V _{GS}	± 12	
Continuous Drain Current (T _J = 150 °C)	I _D	T _A = 25 °C	- 3.8 ^a
		T _A = 70 °C	- 3 ^a
		T _A = 25 °C	- 2.5 ^b
		T _A = 70 °C	- 2.0 ^b
Pulsed Drain Current	I _{DM}	- 15	A
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	- 1.5 ^a
		T _A = 25 °C	- 0.65 ^b
Maximum Power Dissipation	P _D	T _A = 25 °C	1.8 ^a
		T _A = 70 °C	1.1 ^a
		T _A = 25 °C	0.78 ^b
		T _A = 70 °C	0.5 ^b
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C
Package Reflow Conditions ^c	VPR	260	
	IR/Convection	260	

Notes:

- Surface mounted on 1" x 1" FR4 board with full copper, t = 10 s.
- Surface mounted on 1" x 1" FR4 board with minimum copper, t = 10 s.
- Refer to IPC/JEDEC (J-STD-020C), no manual or hand soldering.
- In this document, any reference to case represents the body of the MICRO FOOT device and foot is the bump.
- Based on T_A = 25 °C.

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THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, b}	t = 10 s	R _{thJA}	55	70	°C/W
Maximum Junction-to-Ambient ^{c, d}	t = 10 s		125	160	

Notes:

- a. Surface mounted on 1" x 1" FR4 board with full copper.
- b. Maximum under steady state conditions is 100 °C/W.
- c. Surface mounted on 1" x 1" FR4 board with minimum copper.
- d. Maximum under steady state conditions is 190 °C/W.

SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 20			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 12		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J		2.8			
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 0.6		- 1.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 12 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 70 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≤ - 5 V, V _{GS} = - 4.5 V	- 10			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.5 A		0.086	0.104	Ω
		V _{GS} = - 2.5 V, I _D = - 1.5 A		0.122	0.148	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 10 V, I _D = - 1.5 A		7		S
Dynamic^b						
Input Capacitance	C _{iss}	V _{DS} = - 10 V, V _{GS} = 0 V, f = 1 MHz		450		pF
Output Capacitance	C _{oss}		125			
Reverse Transfer Capacitance	C _{rss}		95			
Total Gate Charge	Q _g	V _{DS} = - 10 V, V _{GS} = - 10 V, I _D = - 1 A		12	18	nC
		V _{DS} = - 10 V, V _{GS} = - 4.5 V, I _D = 1 A		6	9	
Gate-Source Charge	Q _{gs}	V _{DS} = - 10 V, V _{GS} = - 4.5 V, I _D = 1 A		0.85		
Gate-Drain Charge	Q _{gd}		2.2			
Gate Resistance	R _g	V _{GS} = - 0.1 V, f = 1 MHz		7.5		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 10 V, R _L = 10 Ω I _D ≅ - 1 A, V _{GEN} = - 4.5 V, R _g = 1 Ω		20	30	ns
Rise Time	t _r		20	30		
Turn-Off Delay Time	t _{d(off)}		25	40		
Fall Time	t _f		10	15		
Turn-On Delay Time	t _{d(on)}		7	15		
Rise Time	t _r		10	15		
Turn-Off Delay Time	t _{d(off)}	V _{DD} = - 10 V, R _L = 10 Ω I _D ≅ - 1 A, V _{GEN} = - 10 V, R _g = 1 Ω		25	40	
Fall Time	t _f			10	15	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _A = 25 °C			- 1.5	A
Pulse Diode Forward Current	I _{SM}				- 15	
Body Diode Voltage	V _{SD}	I _S = - 1 A, V _{GS} = 0 V		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 1 A, dI/dt = 100 A/μs, T _J = 25 °C		20	40	ns
Body Diode Reverse Recovery Charge	Q _{rr}		10	20	nC	
Reverse Recovery Fall Time	t _a		10		ns	
Reverse Recovery Rise Time	t _b		10			

Notes:

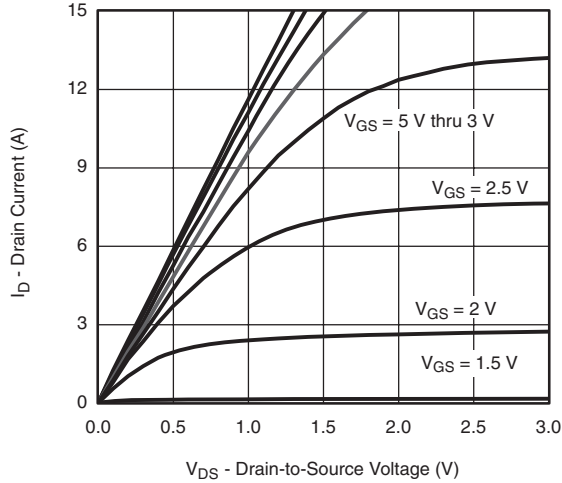
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

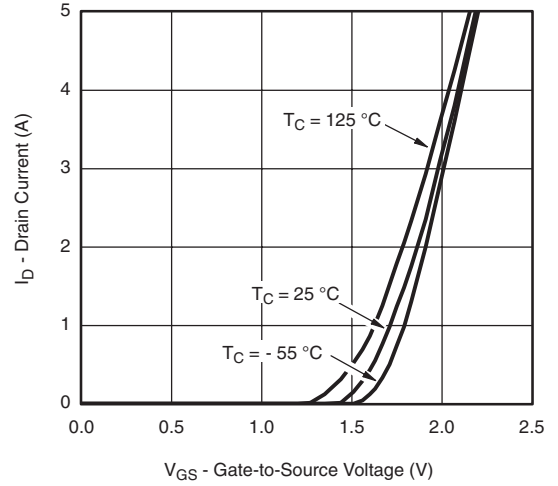


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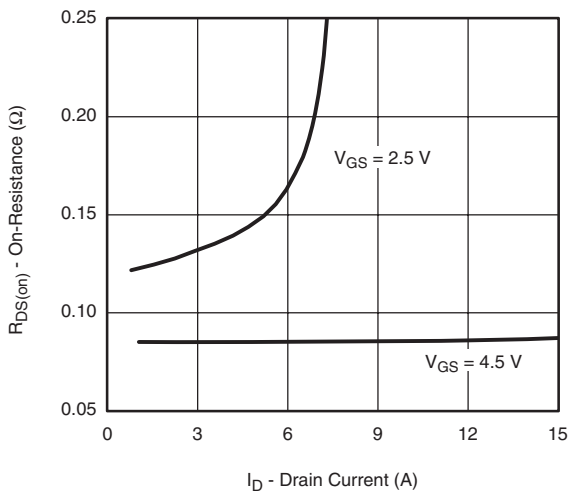
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



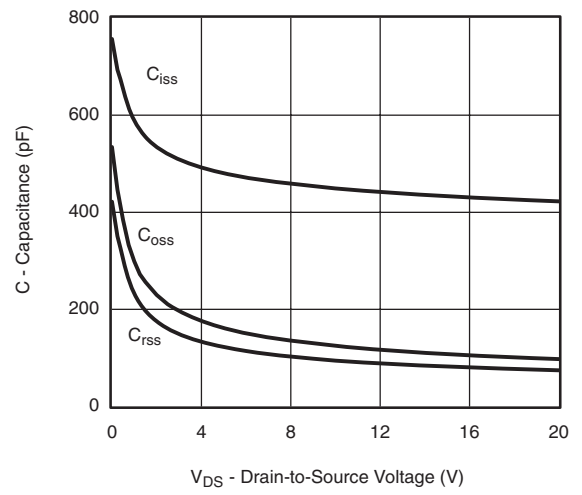
Output Characteristics



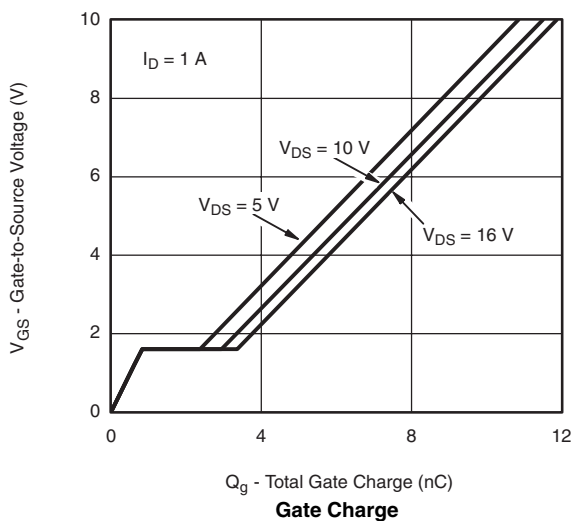
Transfer Characteristics



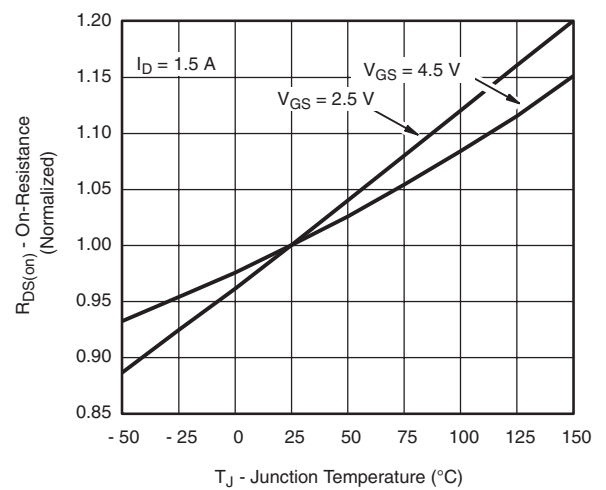
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



Gate Charge



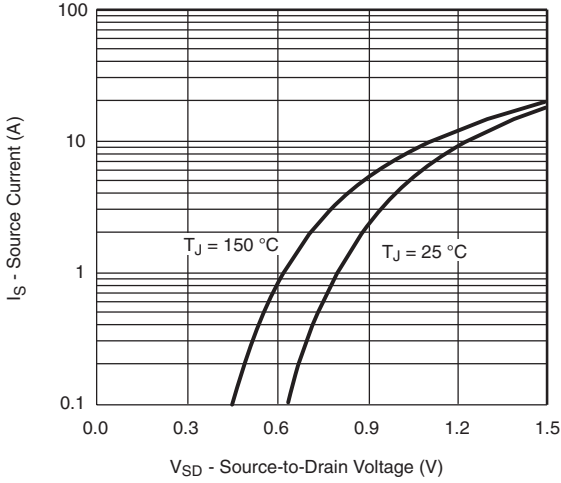
On-Resistance vs. Junction Temperature

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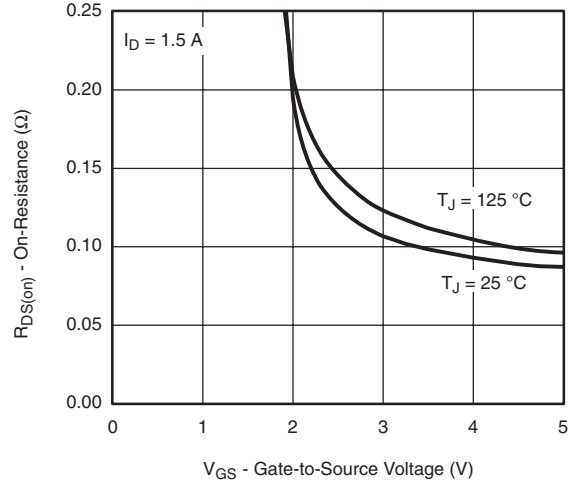
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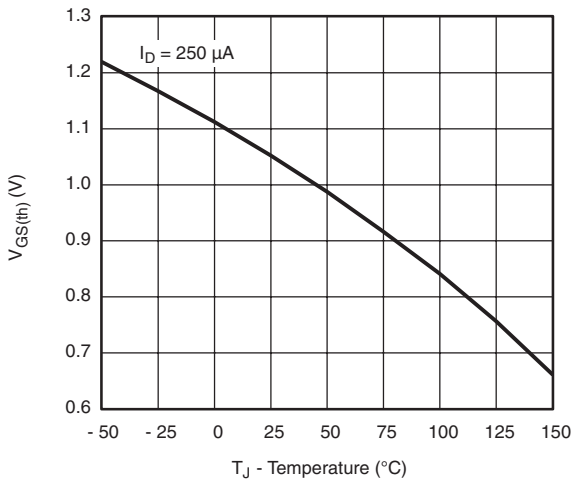
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



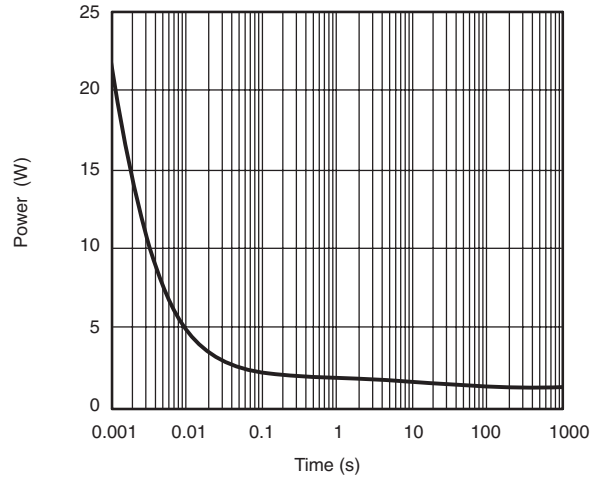
Source-Drain Diode Forward Voltage



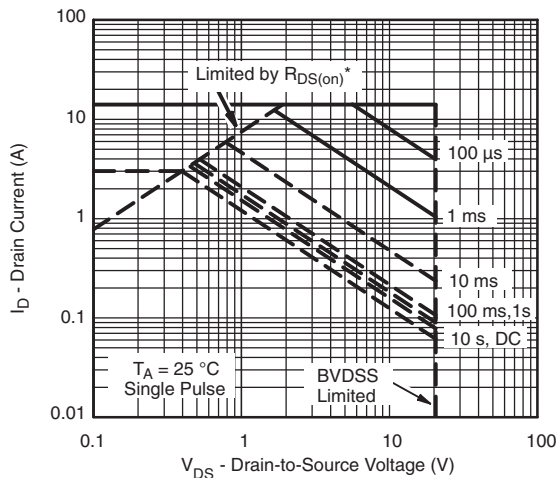
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient

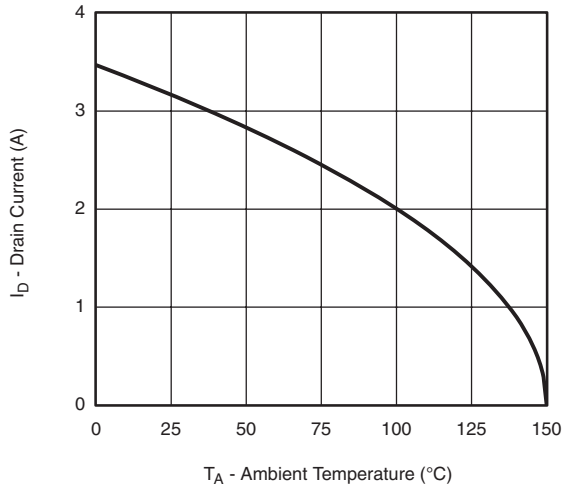


Safe Operating Area, Junction-to-Ambient

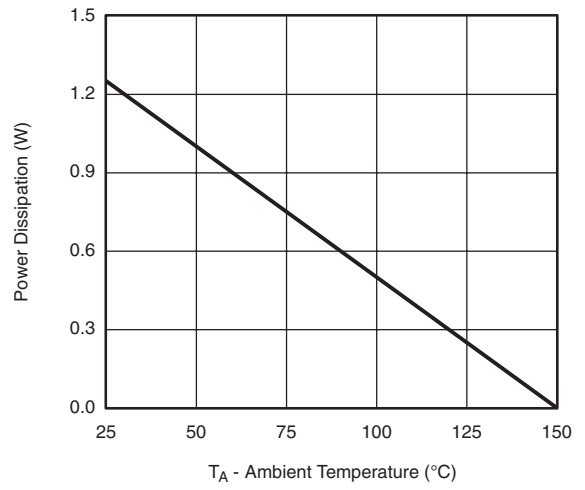


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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*



Power Derating

Note:
When Mounted on 1" x 1" FR4 with Full Copper.

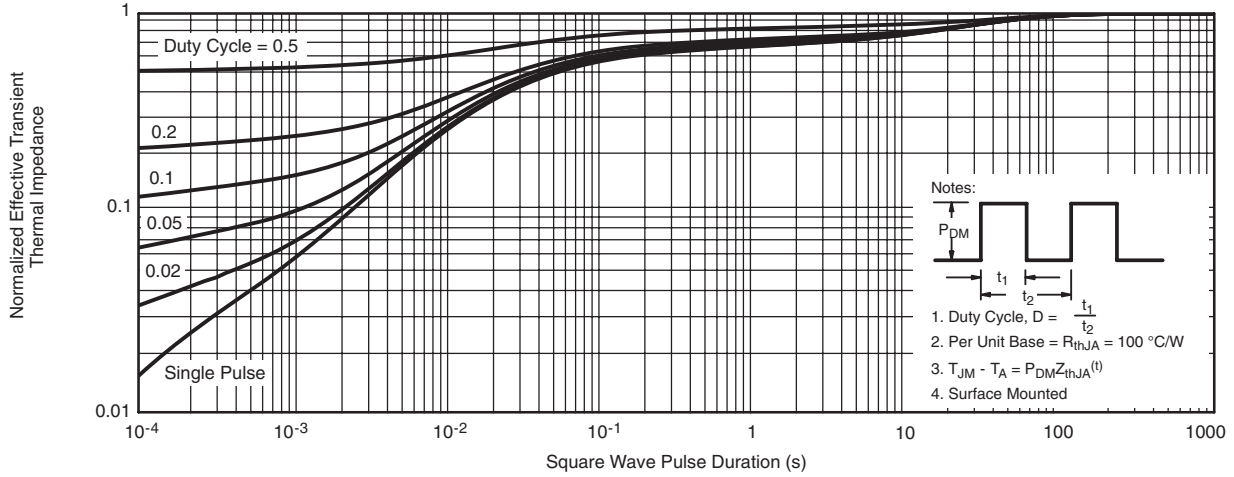
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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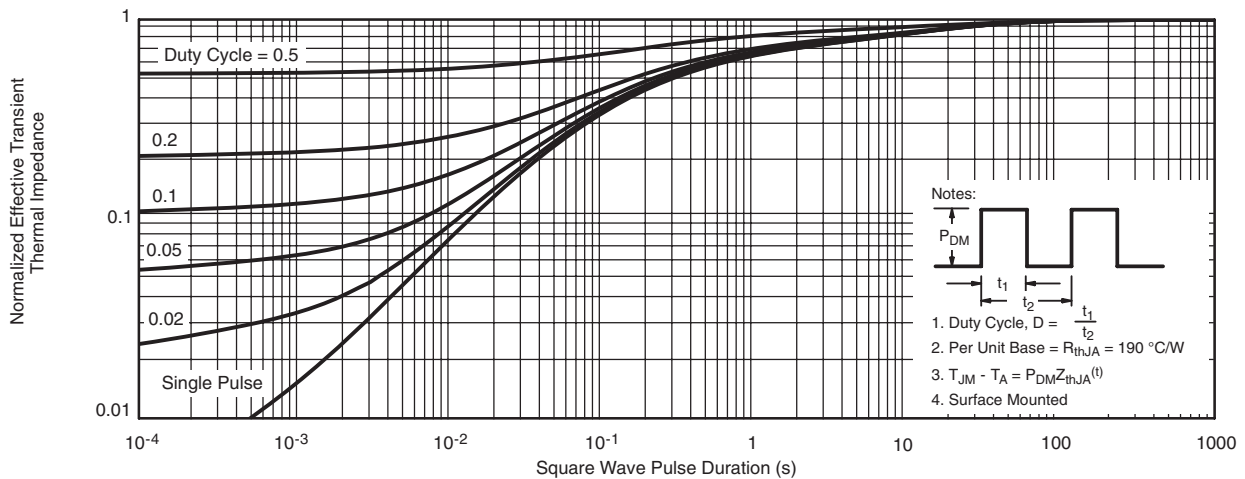
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient (1" x 1" FR4 Board with Full Copper)



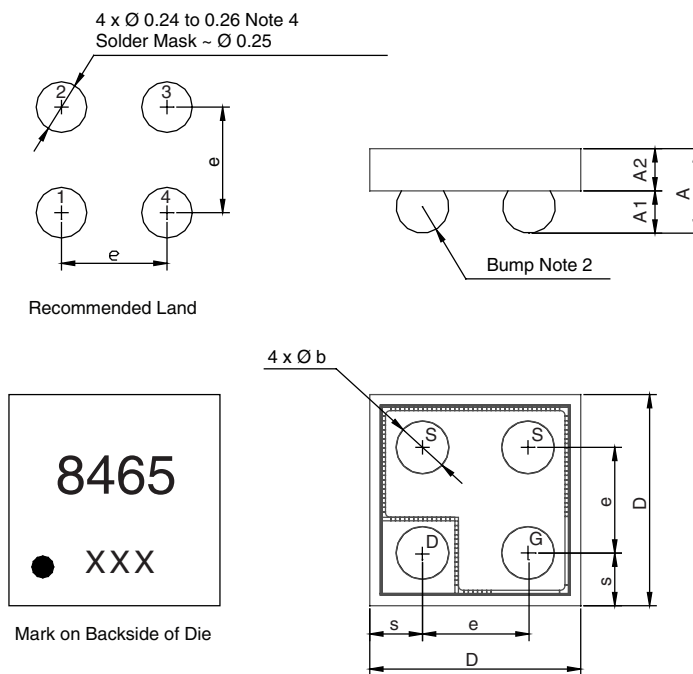
Normalized Thermal Transient Impedance, Junction-to-Ambient (1" x 1" FR4 Board with Minimum Copper)



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PACKAGE OUTLINE

MICRO FOOT: 4-BUMP (2 x 2, 0.5 mm PITCH)



Notes (Unless otherwise specified):

1. All dimensions are in millimeters.
2. Four (4) solder bumps are lead (Pb)-free 95.5Sn/3.8Ag/0.7Cu with diameter \varnothing 0.30 mm to 0.32 mm.
3. Backside surface is coated with a Ti/Ni/Ag layer.
4. Non-solder mask defined copper landing pad.
5. • is location of pin 1.

Dim.	Millimeters ^a			Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	0.462	0.505	0.548	0.0181	0.0198	0.0215
A ₁	0.220	0.250	0.280	0.0086	0.0098	0.0110
A ₂	0.242	0.255	0.268	0.0095	0.0100	0.0105
b	0.300	0.310	0.320	0.0118	0.0122	0.0126
e	0.500			0.0197		
s	0.230	0.250	0.270	0.0090	0.0098	0.0106
D	0.920	0.960	1.000	0.0362	0.0378	0.0394

Notes:

- a. Use millimeters as the primary measurement.

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