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Fairchild Semiconductor FCA76N60N

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# FCA76N60N N-Channel SupreMOS<sup>®</sup> MOSFET

## 600 V, 76 A, 36 mΩ

#### Features

- + R<sub>DS(on)</sub> = 28 m $\Omega$  (Typ. ) @ V<sub>GS</sub> = 10 V, I<sub>D</sub> = 38 A
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 218 nC)
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub> = 914 pF)
- 100% Avalanche Tested
- RoHS Compliant

### Application

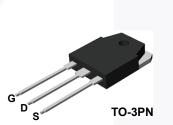
- Solar Inverter
- AC-DC Power Supply

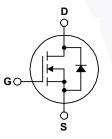
### May 2014

FCA76N60N — N-Channel SupreMOS<sup>®</sup> MOSFET

### Description

The SupreMOS<sup>®</sup> MOSFET is Fairchild Semiconductor's next generation of high voltage super-junction (SJ) technology employing a deep trench filling process that differentiates it from the conventional SJ MOSFETs. This advanced technology and precise process control provides lowest Rsp on-resistance, superior switching performance and ruggedness. SupreMOS MOSFET is suitable for high frequency switching power converter applications such as PFC, server/telecom power, FPD TV power, ATX power, and industrial power applications.





#### MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol		FCA76N60N	Unit			
V <sub>DSS</sub>	Drain to Source Voltage	600	V			
V <sub>GSS</sub>	Gate to Source Voltage			±30	V	
ID	Drain Current	- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)	- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)		Α	
		- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)	- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)			
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	228	Α	
E <sub>AS</sub>	Single Pulsed Avalanch	8022	mJ			
I <sub>AR</sub>	Avalanche Current (			76	Α	
E <sub>AR</sub>	Repetitive Avalanche Er	nergy	(Note 1)	5.40	mJ	
dv/dt	MOSFET dv/dt Ruggedness (Note 3)			100	V/ns	
	Peak Diode Recovery dv/dt			12		
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C)	$(T_{\rm C} = 25^{\rm o}{\rm C})$		W	
	Power Dissipation	- Derate Above 25°C	- Derate Above 25°C		W/ºC	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +150	°C	
ΤL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C	

#### **Thermal Characteristics**

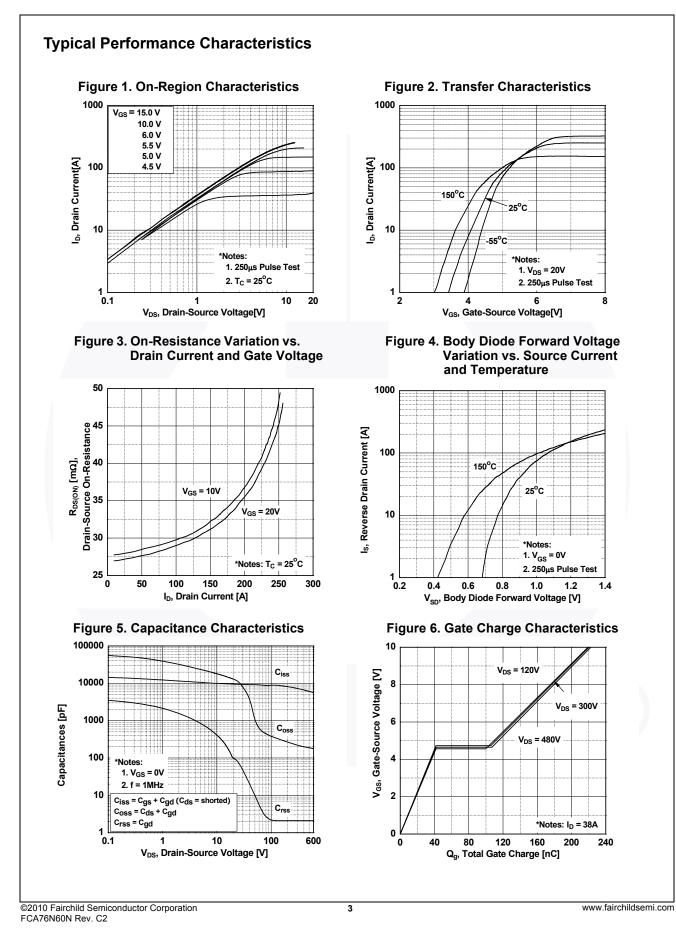
Symbol	Parameter	FCA76N60N	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.23	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient, Max.	40	0/10

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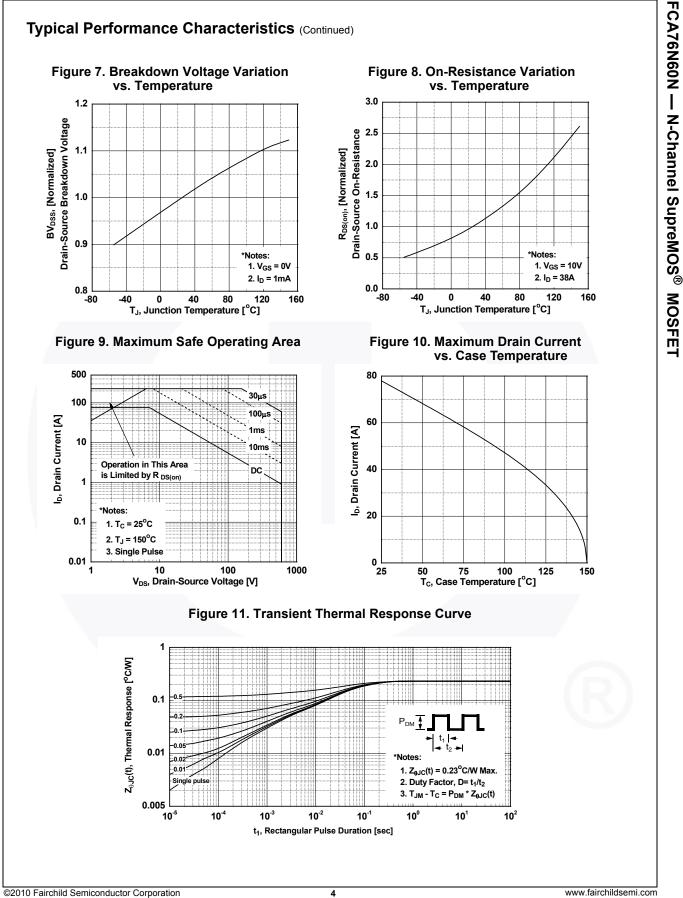


Part Number FCA76N60N		•	<b>Package</b> TO-3PN	• •		Tape Width		Quantity 30 units	
Electrica	l Chara	acteristics T <sub>c</sub> = 25°C	C unless ot	herwise noted.					
Symbol		Parameter		Test Conditio	ons	Min.	Тур.	Max.	Uni
Off Charac	teristics								
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage			I <sub>D</sub> = 1 mA, V <sub>GS</sub> = 0 V,T <sub>J</sub> = 25 <sup>o</sup> C		600	-	_	V
$\Delta BV_{DSS}$	Breakdown Voltage Temperature Coefficient			$I_D = 1 \text{ mA}, \text{ V}_{GS} = 0 \text{ V}, \text{ I}_J = 23 \text{ C}$ $I_D = 1 \text{ mA}, \text{ Referenced to } 25^{\circ}\text{C}$ $V_{DS} = 480 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		000	-	-	
$\Delta D_{VDSS}$ / $\Delta T_{J}$			۱ <sub>D</sub>			-	0.73	-	V/ºC
			V			-	-	10	
IDSS	Zero Ga	te Voltage Drain Current		<sub>DS</sub> = 480 V, T <sub>J</sub> = 125 <sup>o</sup>		-	-	100	μA
I <sub>GSS</sub>	Gate to Body Leakage Current			$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$		-	-	±100	nA
On Charac	toristics								
				- 1 - 250 4		2.0		4.0	V
V <sub>GS(th)</sub>		reshold Voltage		$_{GS} = V_{DS}, I_D = 250 \ \mu A$	1	2.0	-	4.0	V
R <sub>DS(on)</sub>		ain to Source On Resistant	00 / D			-	28.5	36.0	mΩ
9 <sub>FS</sub>	⊢orward	Transconductance	V	<sub>DS</sub> = 40 V, I <sub>D</sub> = 38 A		-	88	-	S
Dynamic C	haracte	ristics							
C <sub>iss</sub>	Input Ca	pacitance		100.11.11	,	-	9310	12385	pF
C <sub>oss</sub>	Output C	Capacitance		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, f = 1 MHz		-	370	495	pF
C <sub>rss</sub>	Reverse	Transfer Capacitance				-	3.1	5.0	pF
C <sub>oss</sub>	Output C	Capacitance	V	<sub>DS</sub> = 380 V, V <sub>GS</sub> = 0 \	/, f = 1 MHz	-	196	-	pF
C <sub>oss(eff.)</sub>	Effective	Output Capacitance	V	$V_{DS} = 0 V \text{ to } 380 V, V_{GS} = 0 V$ $V_{DS} = 380 V, I_D = 38 A,$		-	914	-	pF
Q <sub>g(tot)</sub>	Total Ga	te Charge at 10V				-	218	285	nC
Q <sub>gs</sub>	Gate to S	Source Gate Charge				-	39	-	nC
Q <sub>gd</sub>	Gate to I	Drain "Miller" Charge	V	<sub>GS</sub> = 10 V	(Note 4)	-	66	-	nC
ESR	Equivale	nt Series Resistance (G-S)	f /	= 1 MHz	(11018 4)	-	1.0	-	Ω
Switching	1							1	
t <sub>d(on)</sub>		Delay Time		- 200 \/   - 20 A	_	-	34	78	ns
t <sub>r</sub>		Rise Time		$V_{DD} = 380 \text{ V}, \text{ I}_{D} = 38 \text{ A},$ $V_{GS} = 10 \text{ V}, \text{ R}_{G} = 4.7 \Omega$		-	24	58	ns
t <sub>d(off)</sub>		Delay Time	V			-	235	480	ns
t <sub>f</sub>	Turn-Off	Fall Time			(Note 4)		32	74	ns
Drain-Sour	ce Diod	e Characteristics							
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current				-	-	76	Α	
I <sub>SM</sub>	Maximun	n Pulsed Drain to Source Di	iode Forwa	Forward Current		-		228	Α
V <sub>SD</sub>	Drain to \$	Source Diode Forward Volta	ige V	<sub>GS</sub> = 0 V, I <sub>SD</sub> = 38 A		-	- /	1.2	V
t <sub>rr</sub>	Reverse	Recovery Time	V	<sub>GS</sub> = 0 V, I <sub>SD</sub> = 38 A,		-	613	-	ns
Q <sub>rr</sub>	Reverse	Recovery Charge	dl	dI <sub>F</sub> /dt = 100 A/μs		-	16	-	μC
	= 25 Ω, startir	imited by maximum junction tempera Ing $T_J = 25^{\circ}C$ . $_{DD} \le 380 V$ , starting $T_J = 25^{\circ}C$ . rating temperature typical characteri							



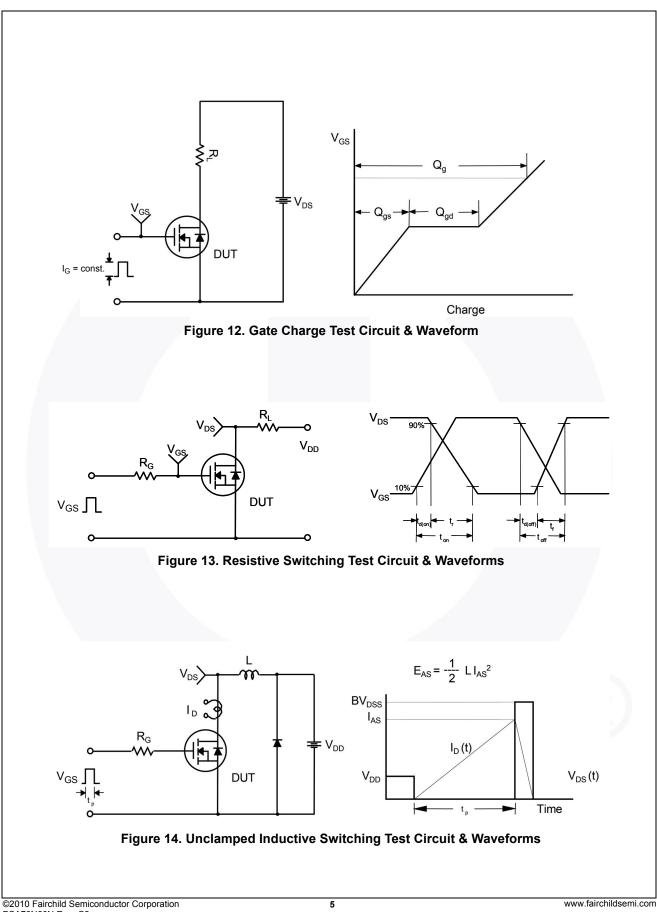






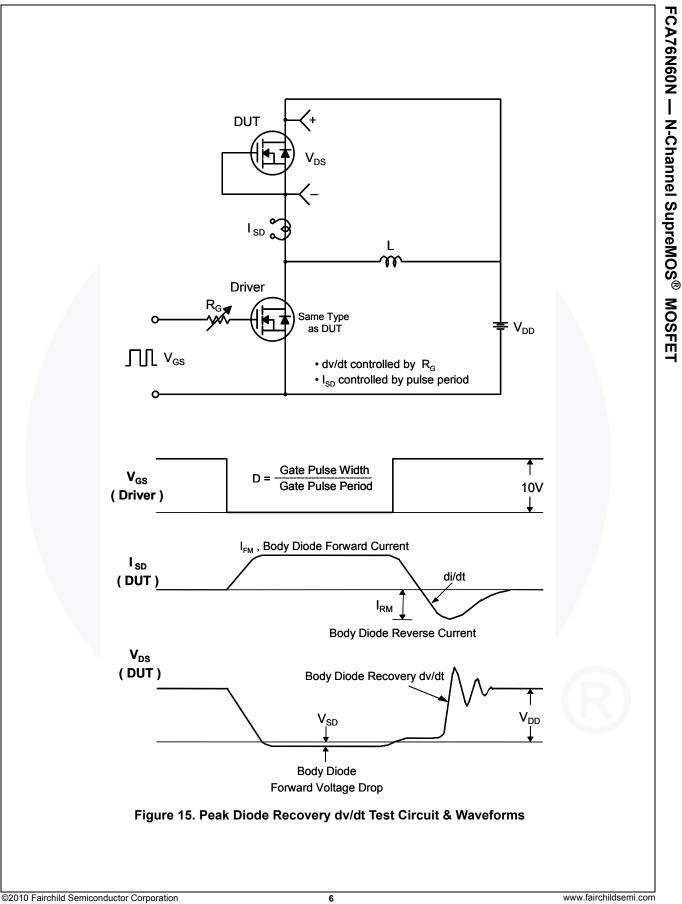
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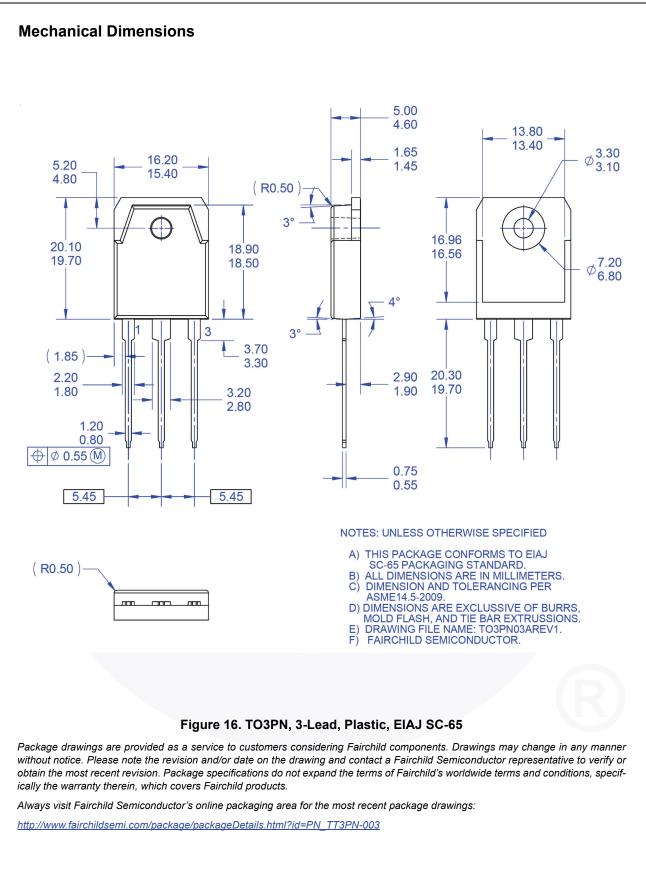


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