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Vishay/Siliconix DG2723DN-T1-E4

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Vishay Siliconix

DG2723

700 MHz, -3 dB Bandwidth; Dual SPDT Analog Switch

DESCRIPTION

DG2723 is a low R_{ON}, high bandwidth analog switch configured in dual SPDT. It achieves 5.5 Ω switch on resistance, greater than 700 MHz -3 dB bandwidth with 5 pF load, and a channel to channel crosstalk at -36 dB and isolation at -29 dB. Fabricated with high density sub micro CMOS process, the DG2723 provides low parasitic capacitance, handles bidirectional signal flow with minimized phase distortion. Guaranteed 1.3 V logic high threshold makes it possible to interface directly with low voltage MCUs. The DG2723 is designed for a wide range of operating voltages from 2.7 V to 5.5 V that can be driven directly from one cell Li-ion battery. On-chip protection circuit protects again fault events when signals at "com" pins goes beyond V+.

Latch up current is 500 mA, as per JESD78, and its ESD tolerance exceeds 5 kV. Packaged in ultra small miniQFN-10 (1.4 mm x 1.8 mm x 0.55 mm), it is ideal for portable high speed mix signal switching application.

As a committed partner to the community and the environment, Vishay Siliconix manufactures this product with lead (Pb)-free device termination. The miniQFN-10 package has a nickel-palladium-gold device termination and is represented by the lead (Pb)-free "-E4" suffix to the ordering part number. The nickel-palladium-gold device terminations meet all JEDEC[®] standards for reflow and MSL rating. As a further sign of Vishay Siliconix's commitment, the DG2723 is fully RoHS complaint.

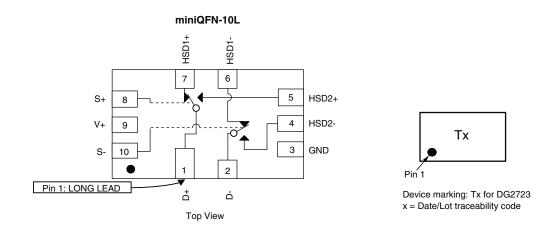
FEATURES

- Wide operation voltage range
- Low on-resistance, 5.5 Ω (typical at 3 V)
- Low capacitance, 5.6 pF (typical)
- -3 dB high bandwidth with 5pF load: 700 MHz (typical)
- Low bit to bit skew: 40 pS (typical)
- Low power consumption
- Low logic threshold: V
- Power down protection: D+/D- pins can tolerate up to 5.5 V when V+ = 0 V
- Logic (S+ and S-) above V+ tolerance
- 5 kV ESD protection (HBM)
- Latch-up current 500 mA per JESD78
- Lead (Pb)-free low profile miniQFN-10 (1.4 mm x 1.8 mm x 0.55 mm)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Cellular phones
- Portable media players
- PDA
- Digital camera
- GPS
- Notebook computer
- TV, monitor, and set top box

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



S14-2470-Rev. D, 22-Dec-14

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ORDERING INFORMATION							
TEMP. RANGE	PACKAGE	PART NUMBER					
-40 °C to 85 °C	miniQFN-10	DG2723DN-T1-E4					

TRUTH TABLE					
S+ (PIN 8)	S- (PIN 10)	FUNCTION			
Х	0	D- = HSD1-			
Х	1	D- = HSD2-			
0	Х	D+ = HSD1+			
1	Х	D+ = HSD2+			

PIN DESCRIPTIONS				
PIN NAME	DESCRIPTION			
S+	Select Input for D+			
S-	Select Input for D-			
HSD±, HSD2±, D±	Data Port			

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)				
PARAMETER		LIMIT	UNIT	
	V+	-0.3 to 6	v	
Reference to GND	S+, S-, D±, HSD1±, HSD2± ^a	-0.3 to (V+ + 0.3)	v	
Current (Any Terminal except S+, S-, D	±, HSD1±, HSD2±)	30		
Continuous Current (S+, S-, D±, HSD1±	± 250	mA		
Peak Current (Pulsed at 1 ms, 10 % Duty Cycle)		± 500		
Storage Temperature (D Suffix)		-65 to 150	°C	
Power Dissipation (Packages) ^b	ower Dissipation (Packages) ^b miniQFN-10 ^c		mW	
ESD (Human Body Model)		5	kV	
Latch-up (Current Injection)		500	mA	

Notes

a. Signals on S+, S-, D±, HSD1±, HSD2± exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

b. All leads welded or soldered to PC board.

c. Derate 2.6 mW/°C above 70 °C.

SPECIFICATIONS (V+ = 3 V)							
PARAMETER	SYMBOL	TEST CONDITIONS OTHERWISE UNLESS SPECIFIED	TEMP. ^a	LIMITS -40 °C to 85 °C			UNIT
		OTHERWISE UNLESS SPECIFIED		MIN. ^b	TYP. °	MAX. ^b	
Analog Switch							
Analog Signal Range ^d	V _{ANALOG}	R _{DS(on)}	Full	0	-	V+	V
On-Resistance	D	V+ = 3 V, I _{D+} = 8 mA, V _{HSD1/2+} = 0.4 V	Room	-	5.5	8	
OII-Resistance	R _{DS(on)}	$v + = 3 v$, $I_{D\pm} = 0 IIIA$, $v_{HSD1/2\pm} = 0.4 v$	Full	-	-	9	
On-Resistance Match d	ΔR_{ON}	V+ = 3 V, $I_{D\pm}$ = 8 mA, $V_{HSD1/2\pm}$ = 0.4 V	Room	-	0.8	-	Ω
On-Resistance Resistance Flatness ^d	R _{ON} Flatness	V+ = 3 V, I _{D±} = 8 mA, V _{HSD1/2±} = 0 V, 1 V	Room	-	2	-	
Switch Off Leakage Current	I _(off)		Full	-100	-	100	nA
Channel On Leakage Current	I _(on)		Full	-200	-	200	ΠA
Digital Control							
Input Voltage High	V _{INH}	V+ = 3 V to 3.6 V	Full	1.3	-	-	
Input Voltage High		V+ = 4.3 V	Full	1.5	-	-	V
Input Voltage Low	V _{INL}	V+ = 3 V to 4.3 V	Full	-	-	0.5	
Input Capacitance	C _{IN}		Full	-	6.5	-	pF
Input Current	$I_{\rm INL}$ or $I_{\rm INH}$	$V_{IN} = 0 \text{ or } V+$	Full	-1	-	1	μA

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SPECIFICATIONS (V+ = 3 V)							
PARAMETER	SYMBOL		TEMP. ^a	LIMITS -40 °C to 85 °C			UNIT
		OTHERWISE UNLESS SPECIFIED		MIN. ^b	TYP. °	MAX. ^b	
Dynamic Characteristics							
Break-Before-Make Time ^{e, d}	t _{BBM}		Room	_	5	-	
Break Belore Make Time	'BBM		Full				
S-, S+ Turn-On Time ^{e, d}	t _{ON}	V+ = 3 V, V _{D1/2±} = 1.5 V, R _L = 50 Ω ,	Room	_	_	30	ns
o-, of Tuni-on time	UN	C _L = 35 pF	Full		-	30	115
S S+ Turn-Off Time ^{e, d}	t _{OFF}		Room	_	-	25	
o, of full-on fille	"OFF		Full				
Charge Injection ^d	Q _{INJ}	$C_L = 1 \text{ nF}, \text{R}_{\text{GEN}} = 0 \Omega, \text{V}_{\text{GEN}} = 0 \text{V}$		-	3	-	pC dB
Off-Isolation ^d	OIRR	V + = 3 V to 3.6 V, R_L = 50 Ω , C_L = 5 pF,		-	-29	-	
Crosstalk ^d	X _{TALK}	f = 240 MHz		-	-36	-	
Bandwidth ^d	BW	V+ = 3 V to 3.6 V, C_L = 5 pF, R_L = 50 $\Omega,$ -3 dB		-	700	-	MHz
Channel-Off Capacitance ^d	$C_{D1\pm (off)}$	- V+ = 3.3 V, f = 1 MHz		-	2.5	-	- pF
Channel-On Capacitance	$C_{D2\pm (off)}$			-	2.5	-	
Channel-On Capacitance ^d	$C_{D\pm (off)}$			-	2.5	-	
Channel On Capacitance	$C_{D\pm (on)}$			-	6.5	-	
Channel-to-Channel Skew ^d	t _{SK(O)}			-	50	-	
Skew Off Opposite Transitions of the Same Output ^d	t _{SK(p)}	V+ = 3 V to 3.6 V, R_L = 50 Ω,C_L = 5 pF		-	20	-	ps
Total Jitter ^d	tj			-	200	-	
Power Supply							
Power Supply Range	V+			2.6	-	5.5	V
Power Supply Current	l+	$V_{IN} = 0 V$, or V+	Full	-	-	2	μA

Notes

a. Room = 25 $^{\circ}$ C, Full = as determined by the operating suffix.

b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this datasheet.

c. Typical values are for design aid only, not guaranteed nor subject to production testing.

d. Guarantee by design, not subjected to production test.

e. V_{IN} = input voltage to perform proper function.

f. Crosstalk measured between channels.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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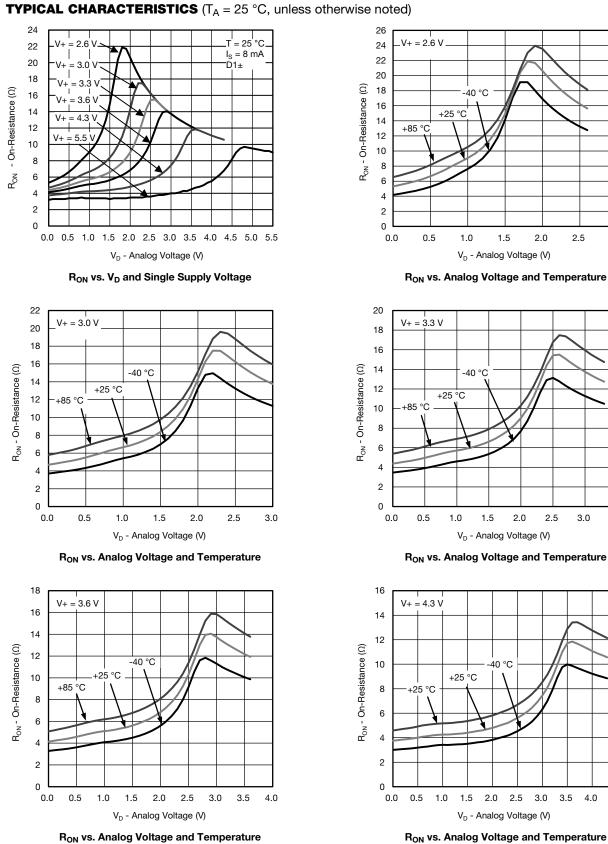
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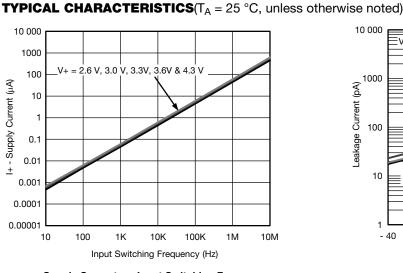


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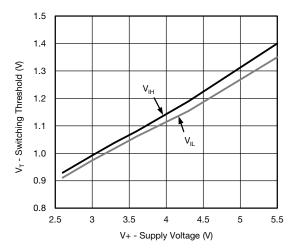
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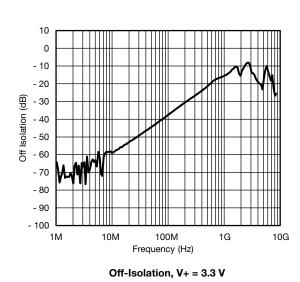
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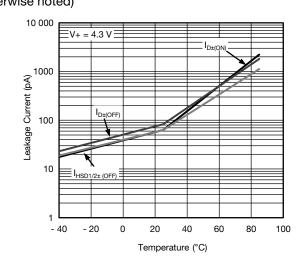


Supply Current vs. Input Switching Frequency

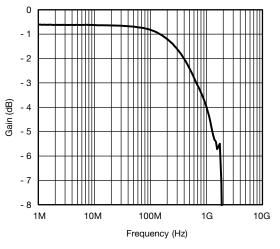


Switching Threshold vs. Supply Voltage

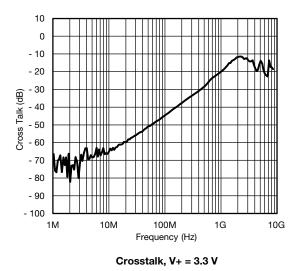


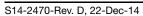


Leakage Current vs. Temperature









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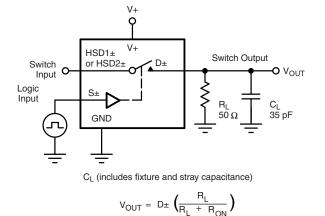
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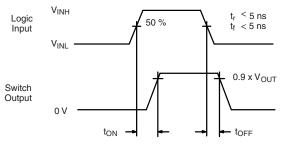
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TEST CIRCUITS





Logic "1" = Switch on Logic input waveforms inverted for switches that have the opposite logic sense.



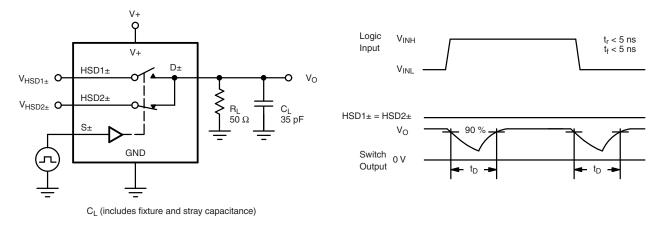


Fig. 2 - Break-Before-Make Interval

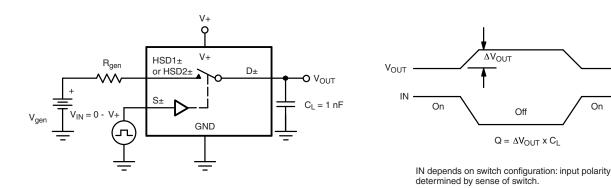


Fig. 3 - Charge Injection

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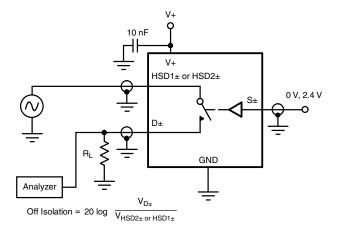


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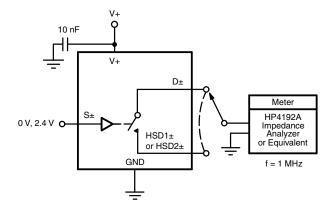


Fig. 4 - Off-Isolation

Fig. 5 - Channel Off/On Capacitance

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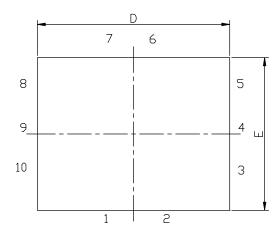


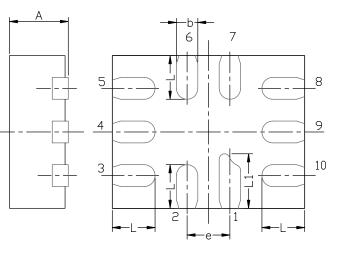
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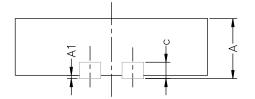
Package Information

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MINI QFN-10L CASE OUTLINE







DIM	MILLIMETERS			INCHES			
DIVI	MIN.	NAM.	MAX.	MIN.	NAM.	MAX.	
A	0.45	0.55	0.60	0.0177	0.0217	0.0236	
A1	0.00	-	0.05	0.000	-	0.002	
b	0.15	0.20	0.25	0.006	0.008	0.010	
С		0.150 or 0.127 REF ⁽¹⁾			0.006 or 0.005 REF ⁽¹)	
D	1.70	1.80	1.90	0.067	0.071	0.075	
E	1.30	1.40	1.50	0.051	0.055	0.059	
е		0.40 BSC			0.016 BSC		
L	0.35	0.40	0.45	0.014	0.016	0.018	
L1	0.45	0.50	0.55	0.0177	0.0197	0.0217	

Note

⁽¹⁾ The dimension depends on the leadframe that assembly house used.

ECN T16-0163-Rev. B, 16-May-16 DWG: 5957

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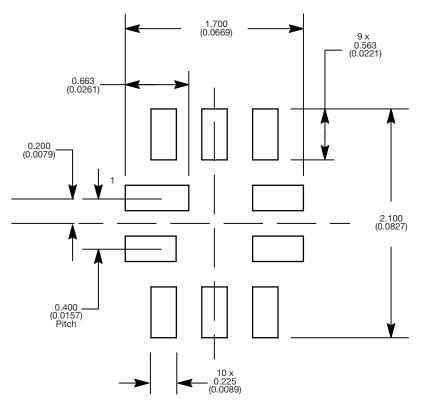




PAD Pattern

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RECOMMENDED MINIMUM PADS FOR MINI QFN 10L



Mounting Footprint Dimensions in mm (inch)





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