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[SI3445DV-T1-E3](#)

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Si3445DV
 Vishay Siliconix

P-Channel 1.8-V (G-S) MOSFET

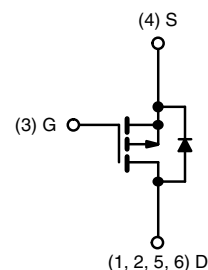
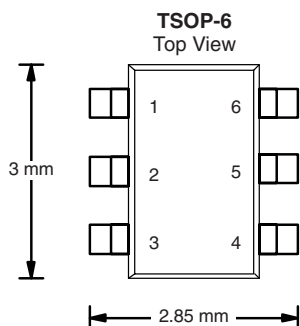
PRODUCT SUMMARY		
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)
- 8	0.042 at V _{GS} = - 4.5 V	± 5.6
	0.060 at V _{GS} = - 2.5 V	± 4.7
	0.080 at V _{GS} = - 1.8 V	± 2.9

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- 1.8 V Rated
- Compliant to RoHS Directive 2002/95/EC



RoHS
 COMPLIANT
 HALOGEN
FREE
 Available



Ordering Information: Si3445DV-T1-E3 (Lead (Pb)-free)
 Si3445DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	- 8	V
Gate-Source Voltage	V _{GS}	± 8	
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 25 °C	± 5.6	A
	T _A = 70 °C	± 4.5	
Pulsed Drain Current	I _{DM}	± 20	
Continuous Source Current (Diode Conduction) ^{a, b}	I _S	- 1.7	
Maximum Power Dissipation ^{a, b}	T _A = 25 °C	2.0	W
	T _A = 70 °C	1.3	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 5 s	R _{thJA}		62.5	°C/W
	Steady State		106		

Notes:

a. Surface Mounted on FR4 board.

b. t ≤ 5 s.

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SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	- 0.45		- 1.0	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -8\text{ V}, V_{GS} = 0\text{ V}$			- 1	μA
		$V_{DS} = -8\text{ V}, V_{GS} = 0\text{ V}, T_J = 70\text{ }^\circ\text{C}$			- 5	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq -5\text{ V}, V_{GS} = -4.5\text{ V}$	- 15			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -5.6\text{ A}$		0.034	0.042	Ω
		$V_{GS} = -2.5\text{ V}, I_D = -4.7\text{ A}$		0.048	0.060	
		$V_{GS} = -1.8\text{ V}, I_D = -2.0\text{ A}$		0.062	0.080	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -10\text{ V}, I_D = -5.6\text{ A}$		15		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.7\text{ A}, V_{GS} = 0\text{ V}$		- 0.7	- 1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -4\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -5.6\text{ A}$		15	25	nC
Gate-Source Charge	Q_{gs}			3		
Gate-Drain Charge	Q_{gd}			2		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -4\text{ V}, R_L = 4\text{ }\Omega$ $I_D \cong -1\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 6\text{ }\Omega$		20	40	ns
Rise Time	t_r			50	100	
Turn-Off Delay Time	$t_{d(off)}$			110	220	
Fall Time	t_f			60	120	
Source-Drain Reverse Recovery Time	t_{rr}		$I_F = -1.7\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		60	

Notes:

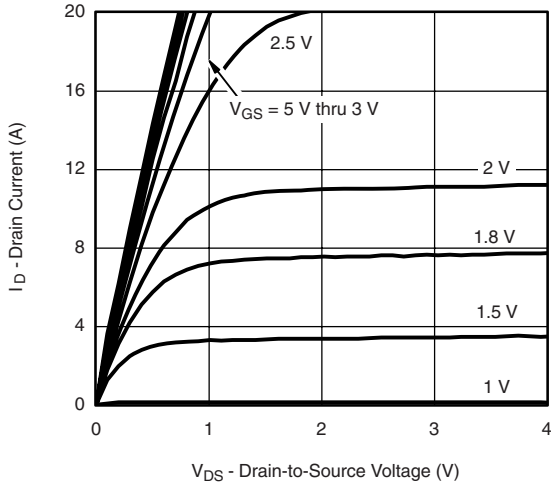
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

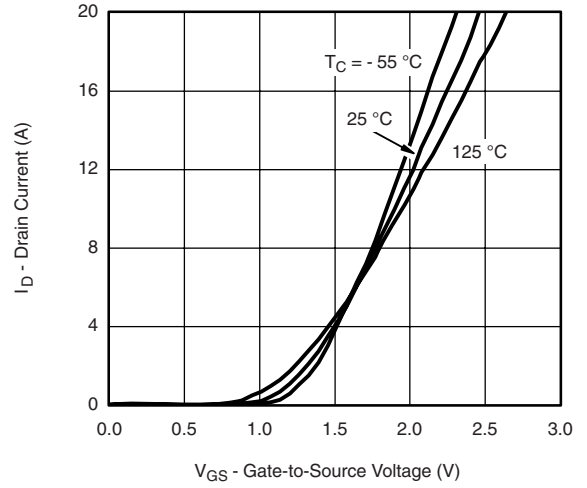


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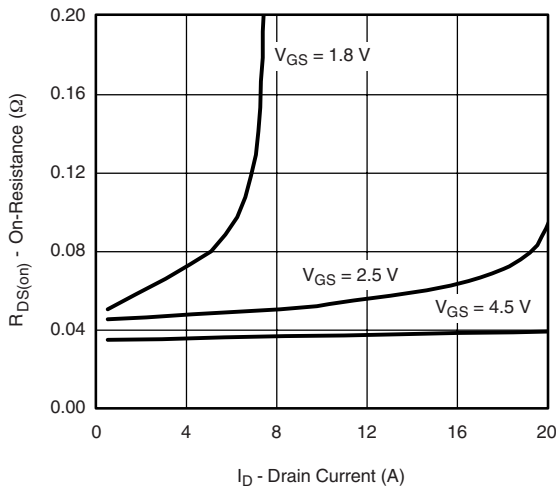
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



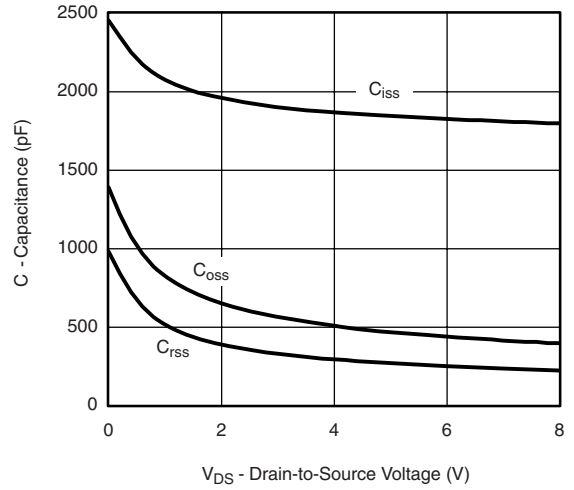
Output Characteristics



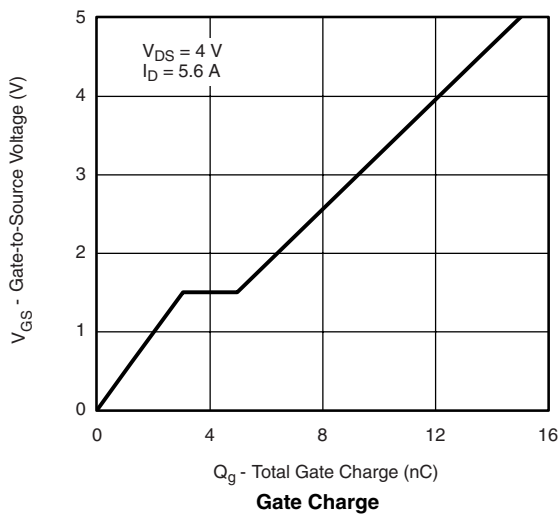
Transfer Characteristics



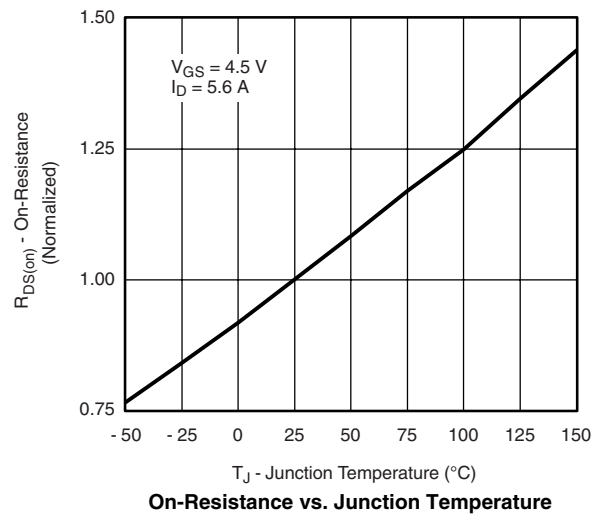
On-Resistance vs. Drain Current



Capacitance



Gate Charge



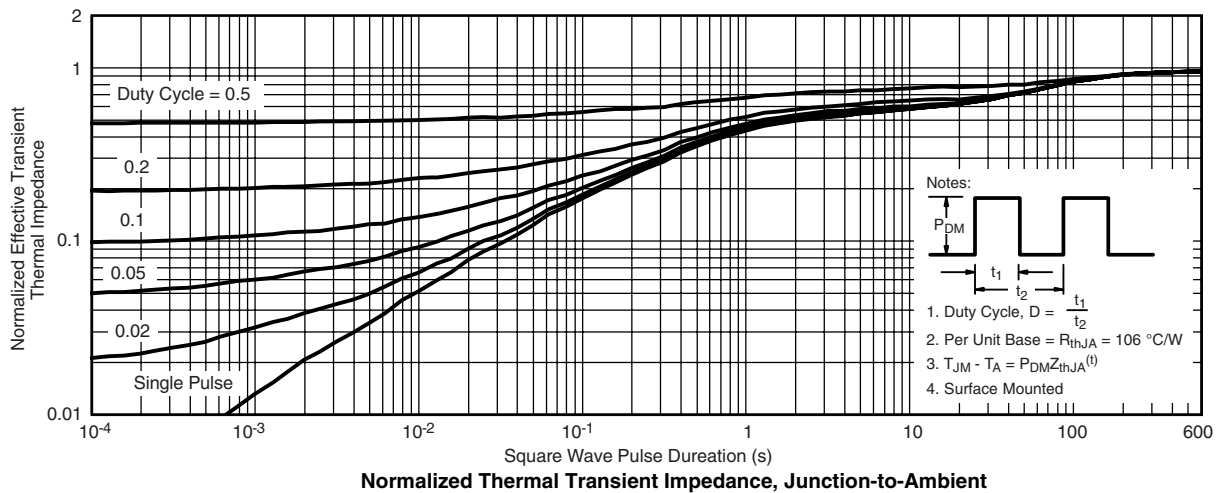
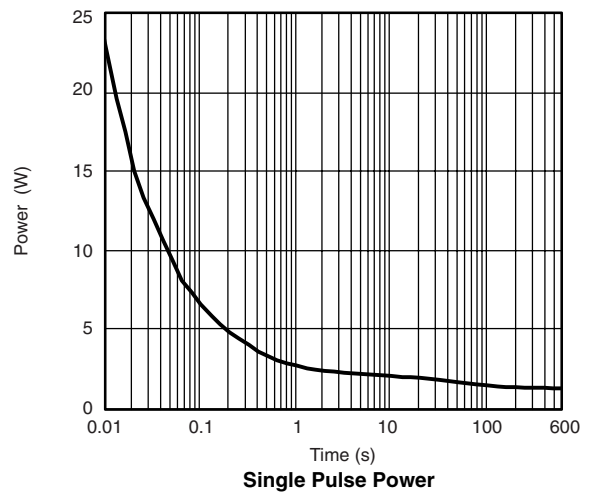
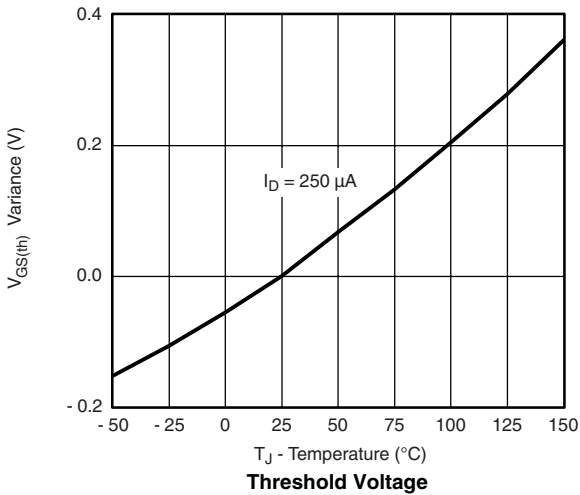
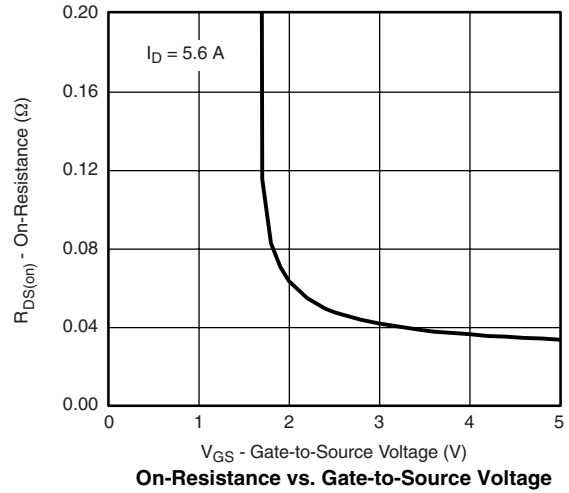
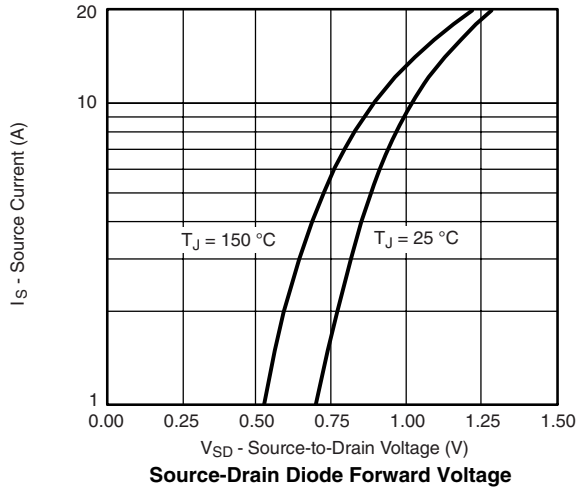
On-Resistance vs. Junction Temperature

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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