

## **Excellent Integrated System Limited**

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

Vishay/Siliconix SI4404DY-T1-E3

For any questions, you can email us directly: sales@integrated-circuit.com





## **Si4404DY**

Vishay Siliconix

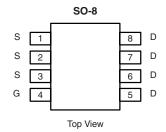
## N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
30	0.0065 at V <sub>GS</sub> = 10 V	23		
	$0.008 \text{ at V}_{GS} = 4.5 \text{ V}$	17		

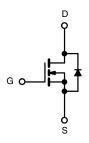
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested





Ordering Information: Si4404DY-T1-E3 (Lead (Pb)-free) Si4404DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted							
Parameter		Symbol	10 s	Steady State	Unit		
Drain-Source Voltage		V <sub>DS</sub>	30		V		
Gate-Source Voltage		V <sub>GS</sub>	± 20				
Continuos Dunio Coment /T 450 90\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	23	15			
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		19	12			
Pulsed Drain Current (10 μs Pulse Width)		I <sub>DM</sub>	60		Α		
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.9	1.3			
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	3.5	1.6	W		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		2.2	1	, vv		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maniana la Andria de	t ≤ 10 s	- R <sub>thJA</sub>	29	35		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		67	80	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	13	16		

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a. Surface Mounted on 1" x 1" FR4 board.

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Datasheet of SI4404DY-T1-E3 - MOSFET N-CH 30V 15A 8-SOIC

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#### **Si4404DY**

## Vishay Siliconix



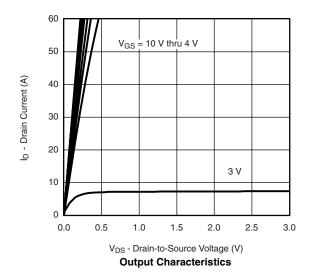
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1.0		3.0	٧		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1			
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α		
	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 23 A		0.0045	0.0065			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 17 \text{ A}$		0.0068	0.008	Ω		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 23 A		80		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 2.9 A, V <sub>GS</sub> = 0 V		0.8	1.2	٧		
Dynamic <sup>b</sup>								
Total Gate Charge	$Q_g$			36	55			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 23 \text{ A}$		15		nC		
Gate-Drain Charge	$Q_{gd}$			12		1		
Gate Resistance	$R_{g}$		1.5	2.2	3.7	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>			20	30			
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		15	23			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 1 A, $V_{GEN}$ = 10 V, $R_G$ = 6 $\Omega$		105	160	ns		
Fall Time	t <sub>f</sub>			40	60			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.9 A, dI/dt = 100 A/μs		50	80			

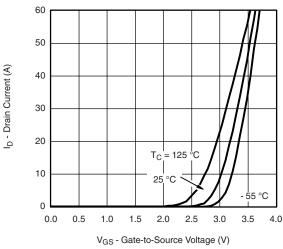
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





**Transfer Characteristics** 

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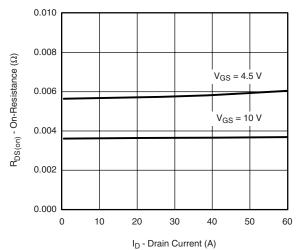
R<sub>DS(on)</sub> - On-Resi stance (Normalized)



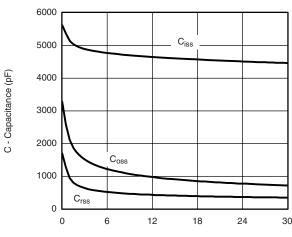
## **Si4404DY**

## Vishay Siliconix

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

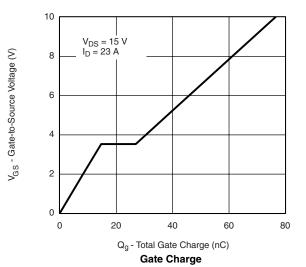


On-Resistance vs. Drain Current



 $V_{DS}$  - Drain-to-Source Voltage (V)





T<sub>J</sub> = 150 °C

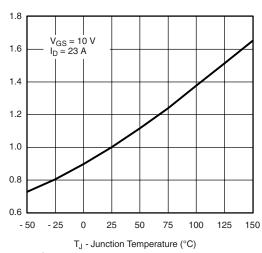
T<sub>J</sub> = 150 °C

T<sub>J</sub> = 25 °C

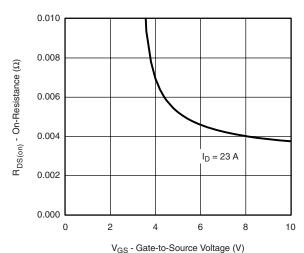
T<sub>J</sub> = 25 °C

0.4

V<sub>SD</sub> - Source-to-Drain Voltage (V) **Source-Drain Diode Forward Voltage** 



On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

Document Number: 71247 S09-0228-Rev. H, 09-Feb-09

0.00

60

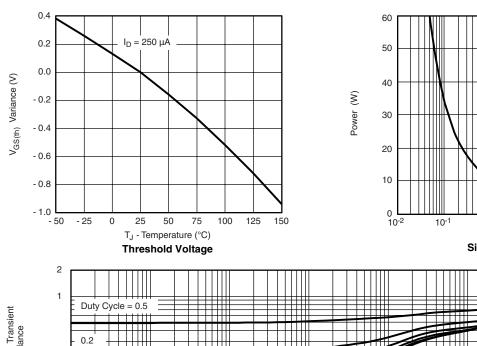


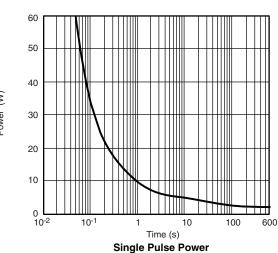
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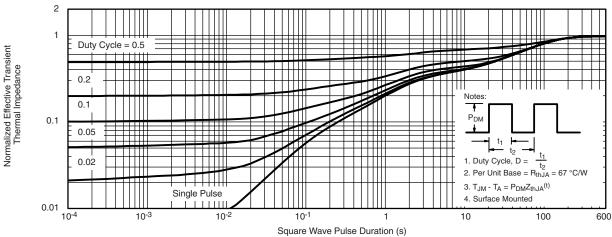
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# VISHAY

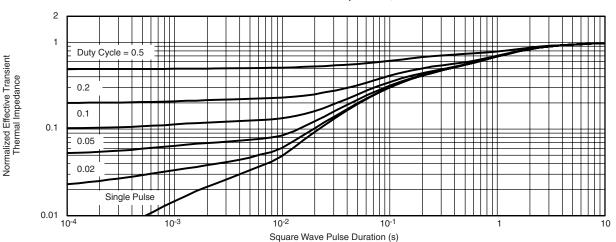
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted











Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?71247">www.vishay.com/ppg?71247</a>.



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