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Si4562DY
Vishay Siliconix

N- and P-Channel 2.5-V (G-S) MOSFET

PRODUCT SUMMARY

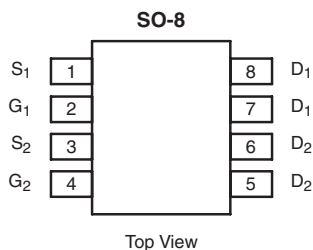
	V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)
N-Channel	20	0.025 at V _{GS} = 4.5 V	7.1
		0.035 at V _{GS} = 2.5 V	6.0
P-Channel	- 20	0.033 at V _{GS} = - 4.5 V	- 6.2
		0.050 at V _{GS} = - 2.5 V	- 5.0

FEATURES

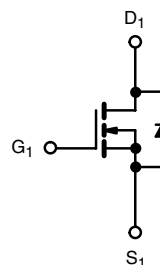
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET: 2.5 Rated
- Compliant to RoHS directive 2002/95/EC



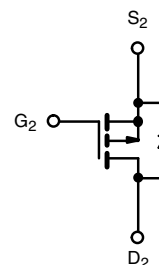
RoHS
COMPLIANT
HALOGEN
FREE
Available



Ordering Information: Si4562DY-T1-E3 (Lead (Pb)-free)
Si4562DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V _{DS}	20	- 20	V
Gate-Source Voltage		V _{GS}	± 12		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	7.1	- 6.2	A
	T _A = 70 °C		5.7	- 4.9	
Pulsed Drain Current		I _{DM}	40	- 40	
Continuous Source Current (Diode Conduction) ^a		I _S	1.7	- 1.7	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	2.0		W
	T _A = 70 °C		1.3		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	N- or P-Channel	Unit
Maximum Junction-to-Ambient ^a	R _{thJA}	62.5	°C/W

Notes:

a. Surface Mounted on FR4 board, t ≤ 10 s.

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions		Min.	Typ.	Max.	Unit	
Static								
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	N-Ch	0.6		1.6	V	
		V _{DS} = V _{GS} , I _D = - 250 μA	P-Ch	- 0.6		- 1.6		
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 12 V	N-Ch P-Ch			± 100 ± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V	N-Ch			1	μA	
		V _{DS} = - 20 V, V _{GS} = 0 V	P-Ch			- 1		
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C	N-Ch			5		
		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C	P-Ch			- 5		
On-State Drain Current ^b	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 4.5 V	N-Ch	20			A	
		V _{DS} ≤ - 5 V, V _{GS} = - 4.5 V	P-Ch	- 20				
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 7.1 A	N-Ch		0.019	0.025	Ω	
		V _{GS} = - 4.5 V, I _D = - 6.2 A	P-Ch		0.027	0.033		
		V _{GS} = 2.5 V, I _D = 6.0 A	N-Ch		0.025	0.035		
		V _{GS} = - 2.5 V, I _D = - 5.0 A	P-Ch		0.040	0.050		
Forward Transconductance ^b	g _{fs}	V _{DS} = 10 V, I _D = 7.1 A	N-Ch		27		S	
		V _{DS} = - 10 V, I _D = - 6.2 A	P-Ch		20			
Diode Forward Voltage ^b	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V	N-Ch			1.2	V	
		I _S = - 1.7 A, V _{GS} = 0 V	P-Ch			- 1.2		
Dynamic ^b								
Total Gate Charge	Q _g	N-Channel V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 7.1 A	N-Ch P-Ch		25 22	50 35	nC	
Gate-Source Charge	Q _{gs}		N-Ch P-Ch		6.5 7			
Gate-Drain Charge	Q _{gd}	P-Channel V _{DS} = - 10 V, V _{GS} = - 4.5 V, I _D = - 6.2 A	N-Ch P-Ch		4 3.5			
Turn-On Delay Time	t _{d(on)}	N-Channel V _{DD} = 10 V, R _L = 10 Ω I _D ≅ 1 A, V _{GEN} = 4.5 V, R _g = 6 Ω	N-Ch P-Ch		40 27	60 50	ns	
Rise Time	t _r		N-Ch P-Ch		40 32	60 50		
Turn-Off Delay Time	t _{d(off)}		P-Channel V _{DD} = - 10 V, R _L = 10 Ω	N-Ch P-Ch		90 95		150 150
Fall Time	t _f		I _D ≅ - 1 A, V _{GEN} = - 4.5 V, R _g = 6 Ω	N-Ch P-Ch		40 45		60 70
Sorce-Drain Reverse Recovery Tme	t _{rr}	I _F = 1.7 A, dI/dt = 100 A/μs	N-Ch		40	80		
		I _F = - 1.7 A, dI/dt = 100 A/μs	P-Ch		40	80		

Notes:

a. For design aid only; not subject to production testing.

b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

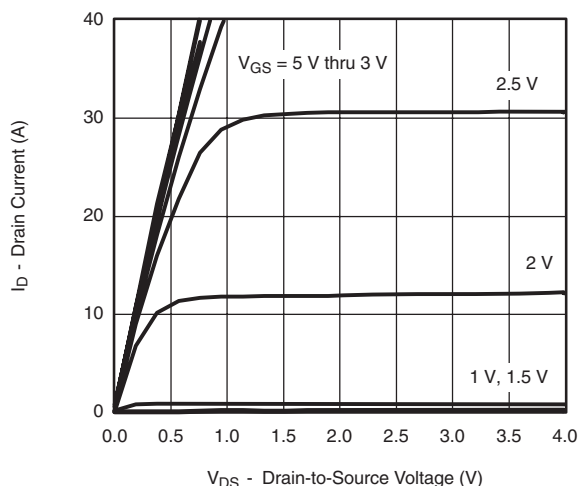
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



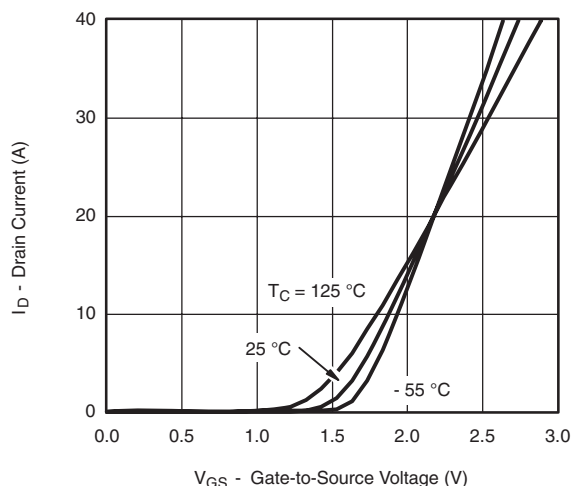
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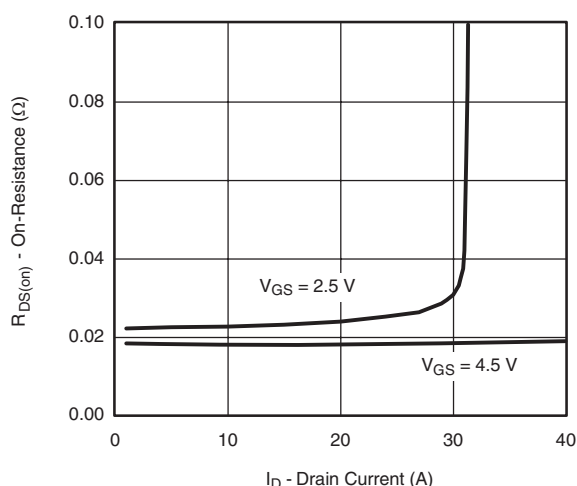
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



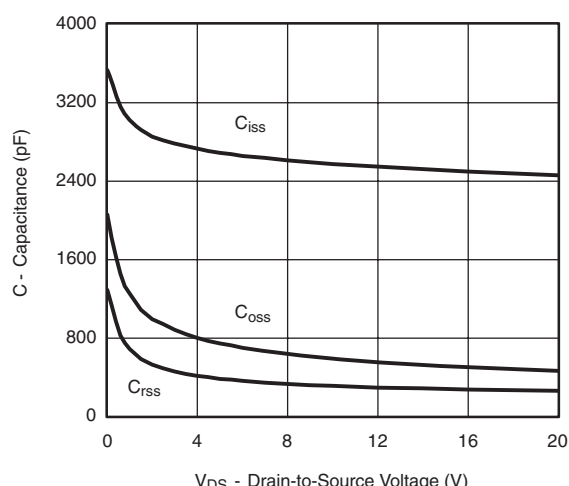
Output Characteristics



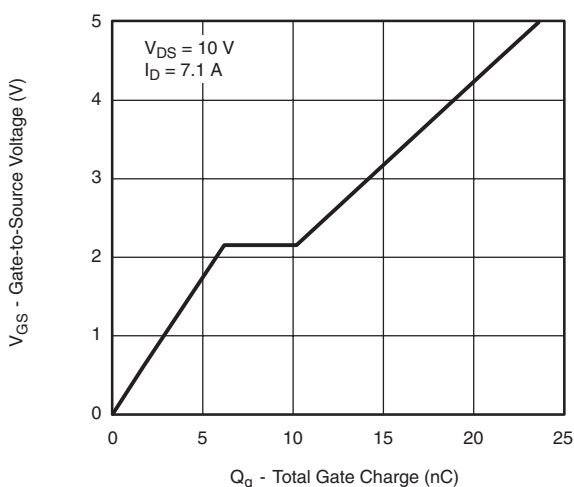
Transfer Characteristics



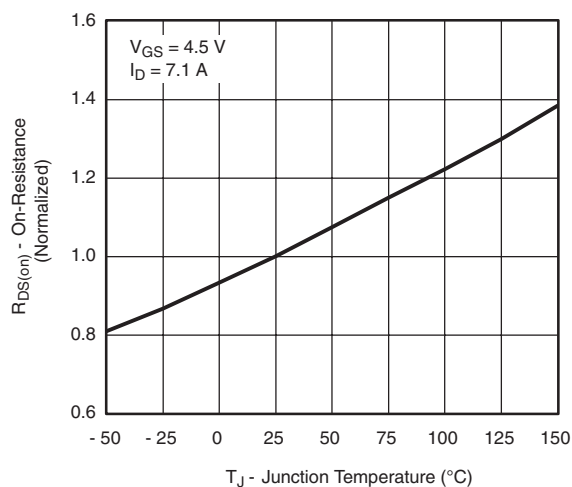
On-Resistance vs. Drain Current



Capacitance



Gate Charge



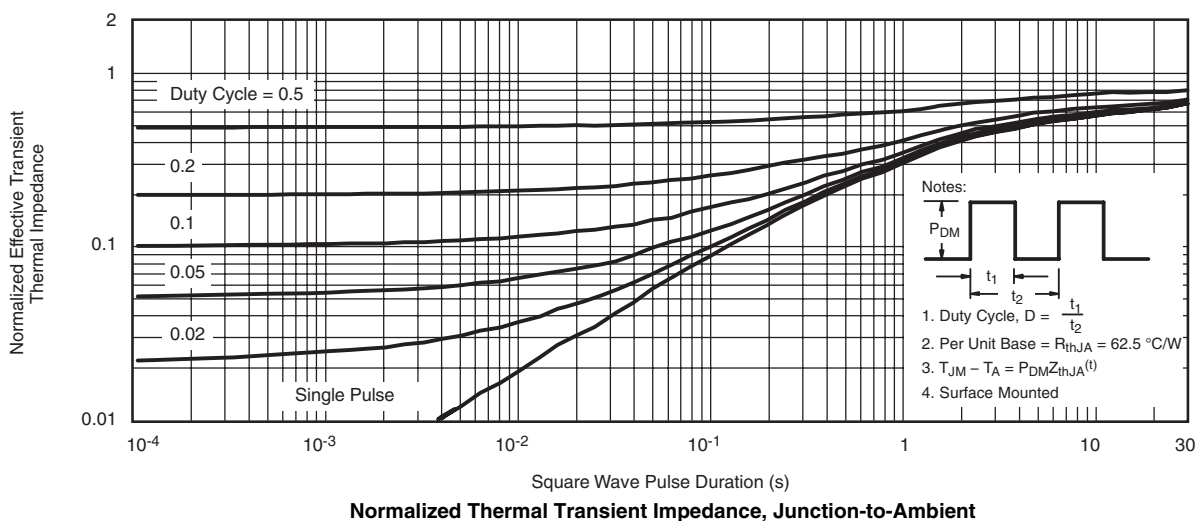
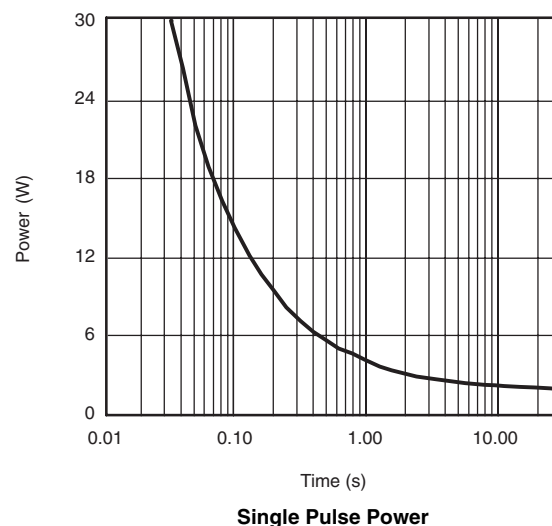
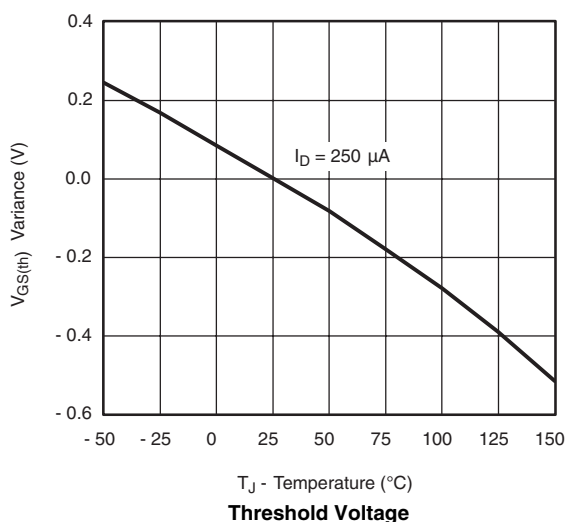
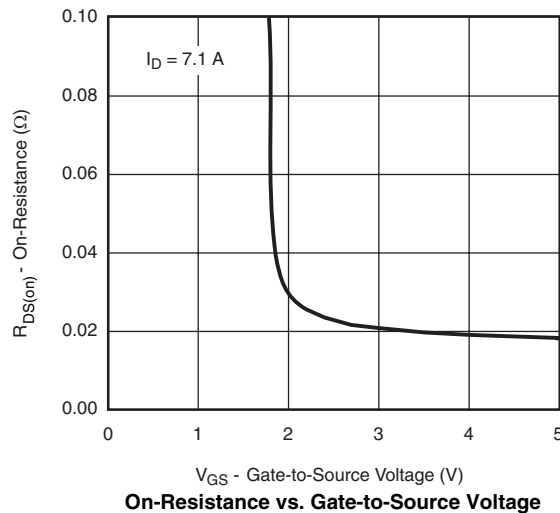
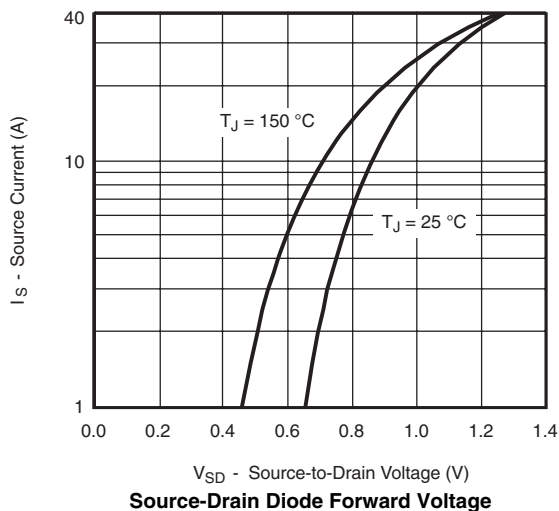
On-Resistance vs. Junction Temperature

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N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

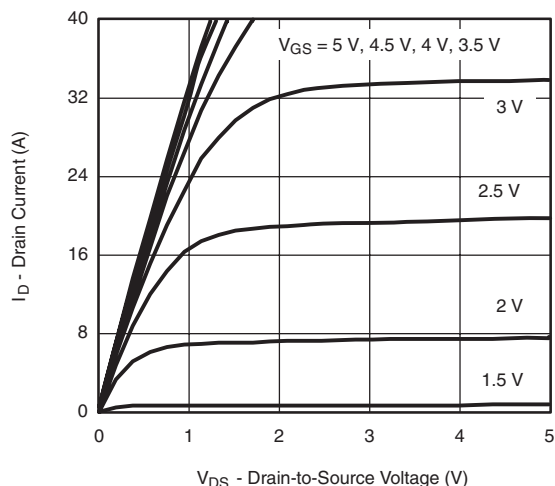




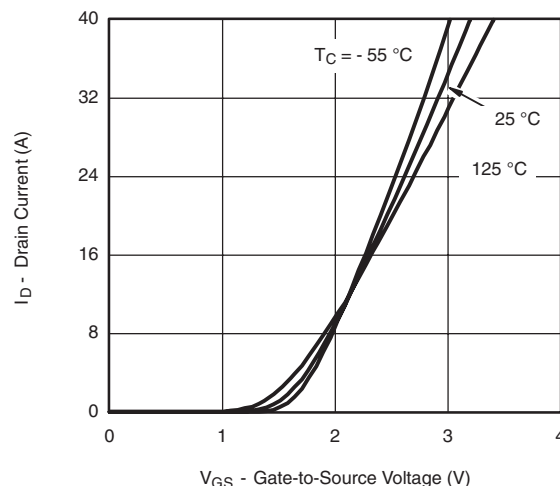
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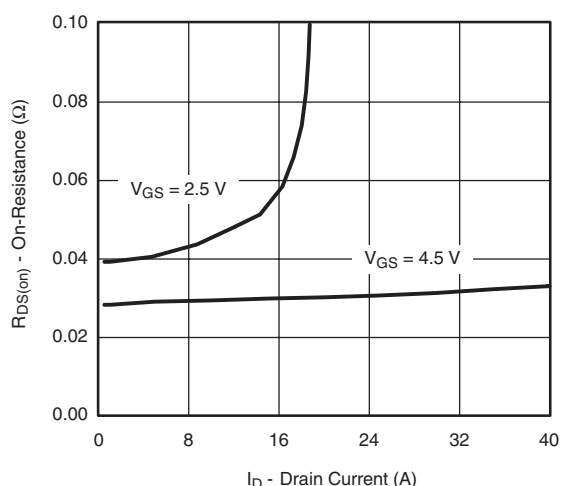
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



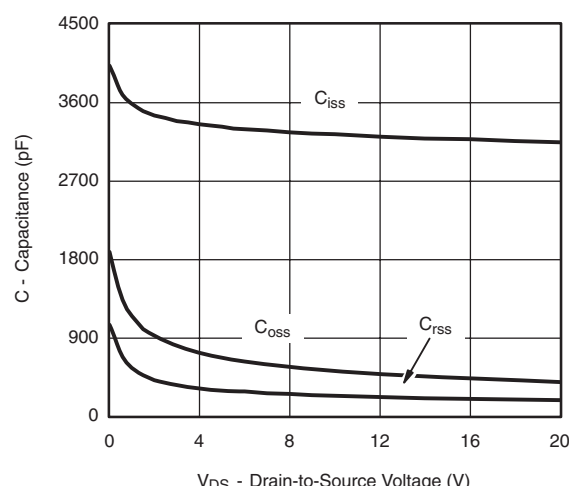
Output Characteristics



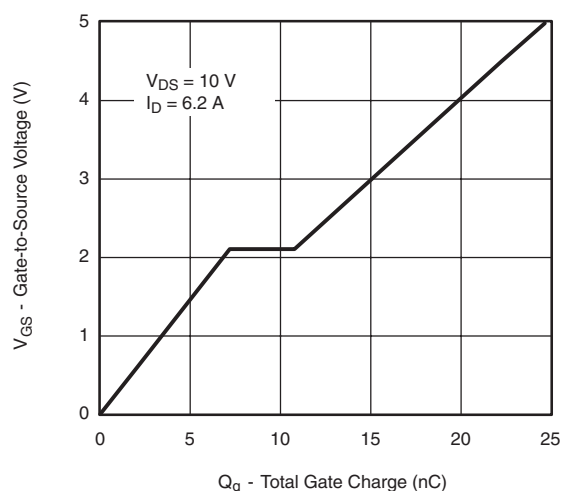
Transfer Characteristics



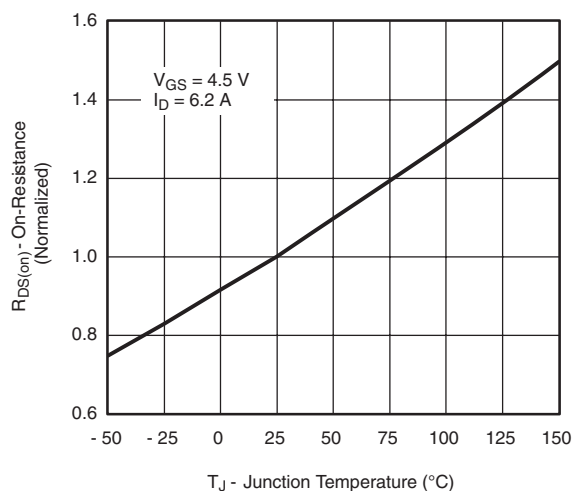
On-Resistance vs. Drain Current



Capacitance



Gate Charge



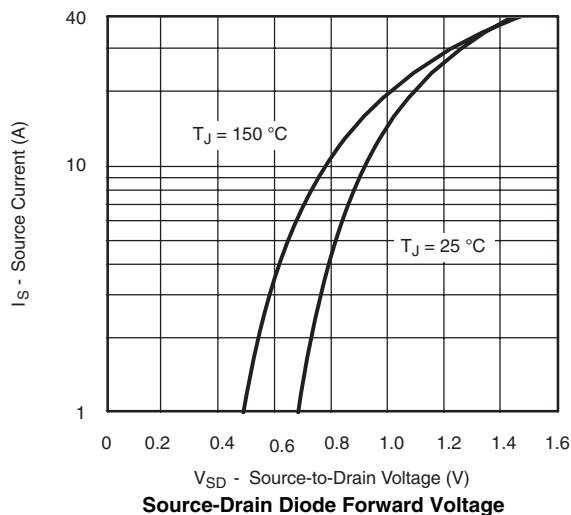
On-Resistance vs. Junction Temperature

Si4562DY

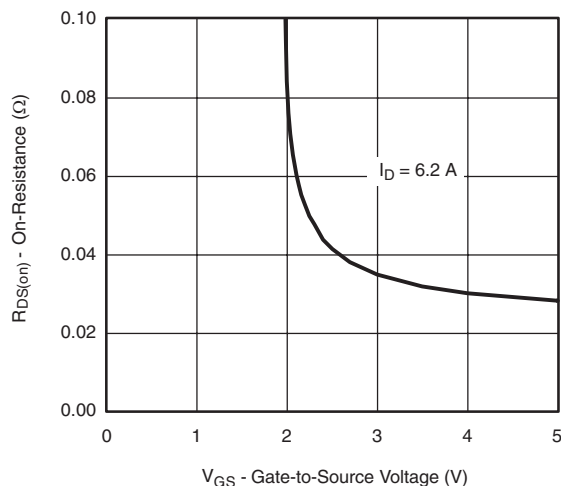
Vishay Siliconix



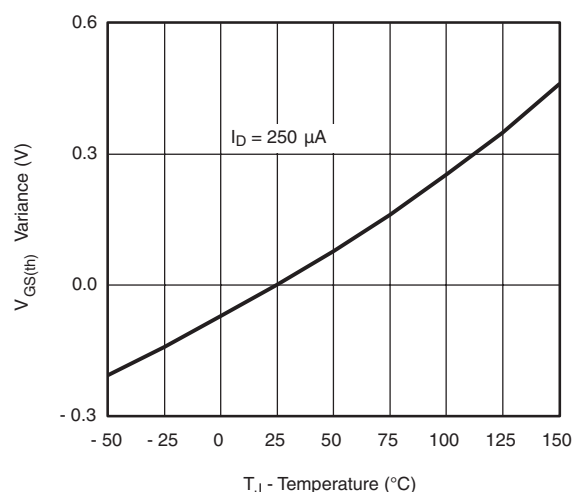
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



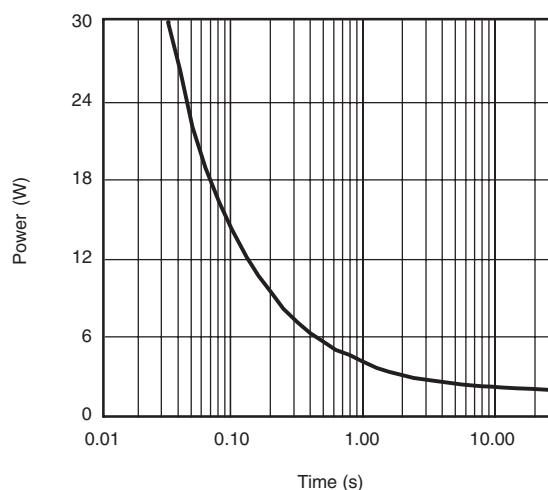
Source-Drain Diode Forward Voltage



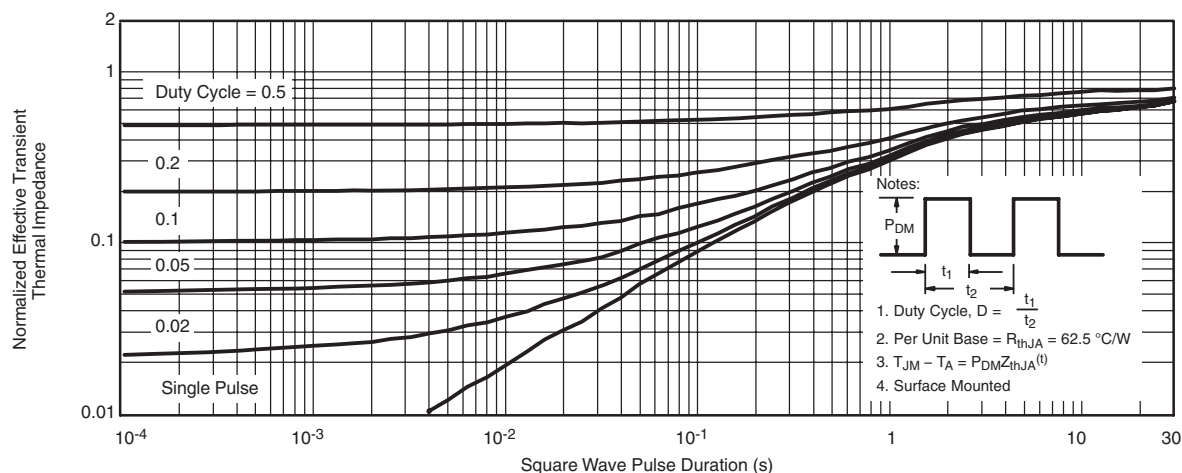
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power vs. Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

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