

# **Excellent Integrated System Limited**

Stocking Distributor

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Vishay/Siliconix SI4923DY-T1-E3

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>





### Si4923DY

Vishay Siliconix

# Dual P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)			
- 30	0.021 at V <sub>GS</sub> = - 10 V	- 8.3			
	0.031 at V <sub>GS</sub> = - 4.5 V	- 6.8			

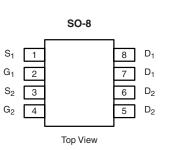
#### FEATURES

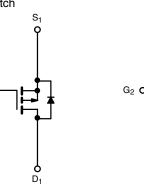
- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

#### APPLICATIONS

- Load Switches
- Notebook PCs
- Desktop PCs
- Game Stations
- Battery Switch

G1 O







RoHS

Ordering Information: Si4923DY-T1-E3 (Lead (Pb)-free) Si4923DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

D<sub>2</sub> P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	Γ <sub>A</sub> = 25 °C, unles	ss otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 30		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 8.3	- 6.2	٨	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 6.6	- 5.0		
Pulsed Drain Current		I <sub>DM</sub>	- 30		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	- 1.7	- 0.9		
	T <sub>A</sub> = 25 °C	– P <sub>D</sub>	2.0	1.1	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.3	0.7		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R <sub>thJA</sub>	45	62.5	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		85	110	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	26	35	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



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<b>SPECIFICATIONS</b> $T_J = 25 \text{ °C}$ , unless otherwise noted							
Parameter	Symbol	Test Conditions	Min. Typ.		Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$ - 1		- 3	V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 25	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -10 V$	- 30			А	
	D	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 8.3 A		0.017	0.021	0	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 6.8 A		0.025	0.031	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 8.3 A		26		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			45.5	70		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 15 V, $V_{GS}$ = - 10 V, $I_{D}$ = - 8.3 A		6.5		nC	
Gate-Drain Charge	Q <sub>gd</sub>			12.6			
Turn-On Delay Time	t <sub>d(on)</sub>			15	25		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 15 $\Omega$		10	15		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_{\text{D}}\cong$ - 1 A, $\text{V}_{\text{GEN}}$ = - 10 V, $\text{R}_{\text{g}}$ = 6 $\Omega$		135	210	ns	
Fall Time	t <sub>f</sub>			80	120		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/μs		70	110		

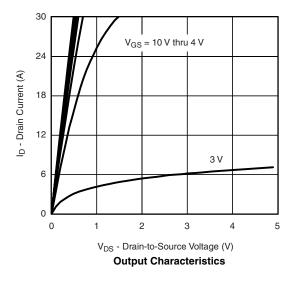
Notes:

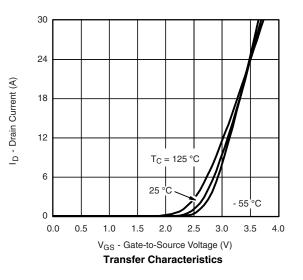
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







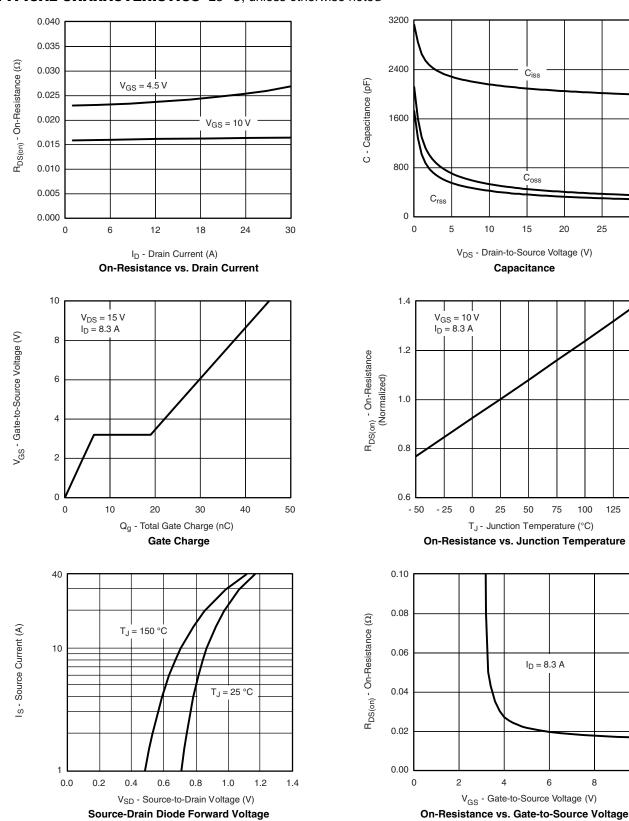


# Si4923DY

30

150

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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Document Number: 72069 S09-0869-Rev. C, 18-May-09 10

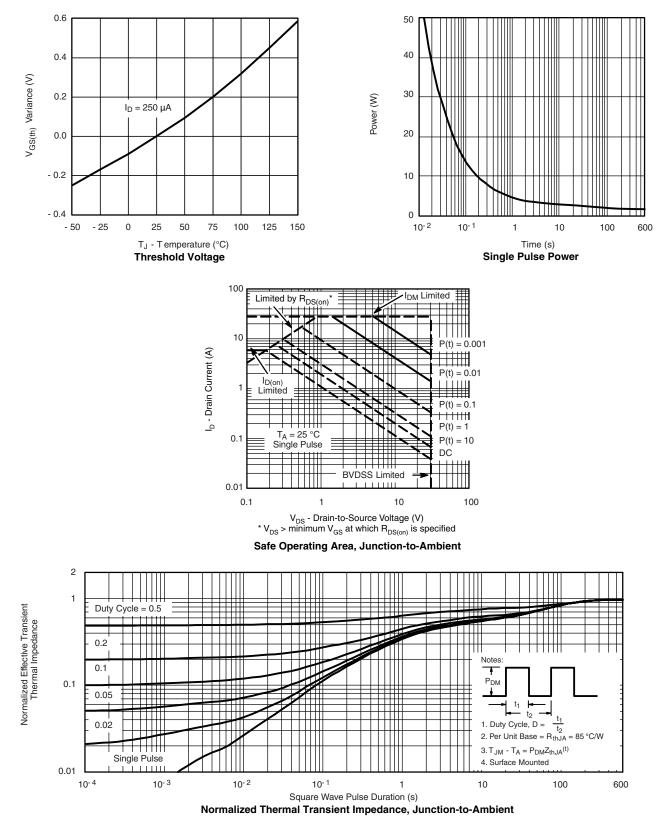


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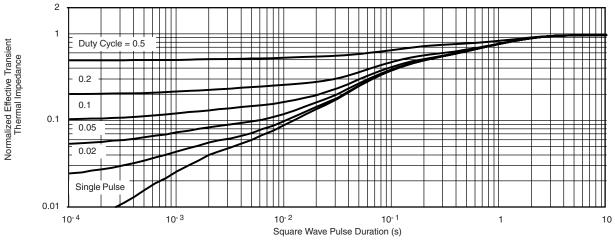




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Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?72069">www.vishay.com/ppg?72069</a>.





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