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[Vishay/Siliconix](#)
[SI4936CDY-T1-GE3](#)

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sales@integrated-circuit.com

New Product



Si4936CDY
Vishay Siliconix

Dual N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^d	Q _g (Typ.)
30	0.040 at V _{GS} = 10 V	5.8	2.8 nC
	0.050 at V _{GS} = 4.5 V	5.5	

FEATURES

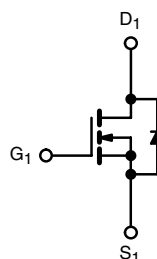
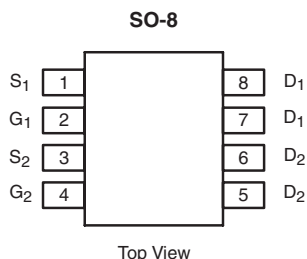
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET

APPLICATIONS

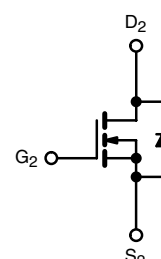
- Low Current DC/DC Conversion
- Notebook System Power



RoHS
COMPLIANT
HALOGEN
FREE



N-Channel MOSFET



N-Channel MOSFET

Ordering Information: Si4936CDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V _{GS}	± 20		
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	5.8	A
		T _C = 70 °C	4.6	
		T _A = 25 °C	5.0 ^{a, b}	
		T _A = 70 °C	4.0 ^{a, b}	
Pulsed Drain Current	I _{DM}	20		
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	1.9	
		T _A = 25 °C	1.4 ^{a, b}	
Maximum Power Dissipation	P _D	T _C = 25 °C	2.3	W
		T _C = 70 °C	1.5	
		T _A = 25 °C	1.7 ^{a, b}	
		T _A = 70 °C	1.1 ^{a, b}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	58	75	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	42	55	

Notes:

- Surface Mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 110 °C/W.
- Based on T_C = 25 °C.

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		32		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 5		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.2		3	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	15			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 5 A		0.033	0.040	Ω
		V _{GS} = 4.5 V, I _D = 4.7 A		0.041	0.050	
Forward Transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 5 A		15		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		325		pF
Output Capacitance	C _{oss}			60		
Reverse Transfer Capacitance	C _{rss}			30		
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 5 A		6	9	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 5 A		2.8	4.2	
Gate-Drain Charge	Q _{gd}			0.8		
Gate Resistance	R _g	f = 1 MHz	0.6	2.8	5.6	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 3.8 Ω I _D ≅ 4 A, V _{GEN} = 4.5 V, R _g = 1 Ω		12	18	ns
Rise Time	t _r			13	20	
Turn-Off Delay Time	t _{d(off)}			16	25	
Fall Time	t _f			11	17	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 3.8 Ω I _D ≅ 4 A, V _{GEN} = 10 V, R _g = 1 Ω		4	8	
Rise Time	t _r			9	18	
Turn-Off Delay Time	t _{d(off)}			11	20	
Fall Time	t _f			8	15	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			1.9	A
Pulse Diode Forward Current	I _{SM}				20	
Body Diode Voltage	V _{SD}	I _S = 4 A, V _{GS} = 0 V		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 4 A, dI/dt = 100 A/μs, T _J = 25 °C		11	20	ns
Body Diode Reverse Recovery Charge	Q _{rr}			4	8	nC
Reverse Recovery Fall Time	t _a			6		ns
Reverse Recovery Rise Time	t _b			5		

Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

b. Guaranteed by design, not subject to production testing.

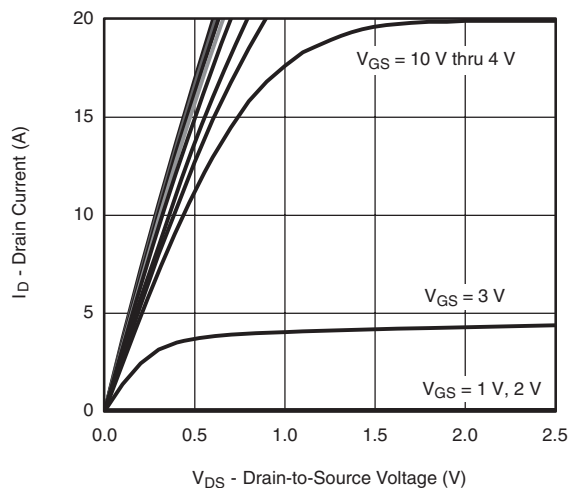
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

New Product

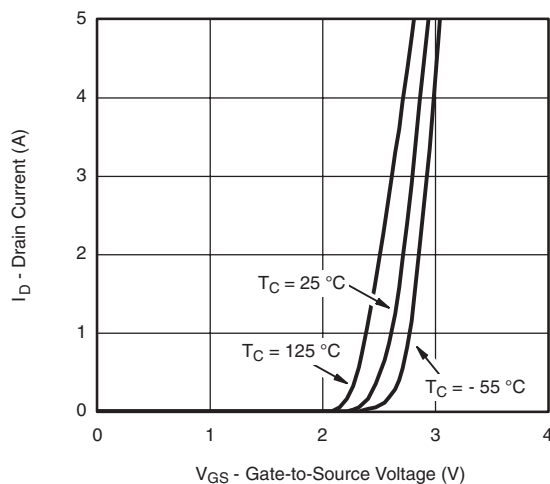


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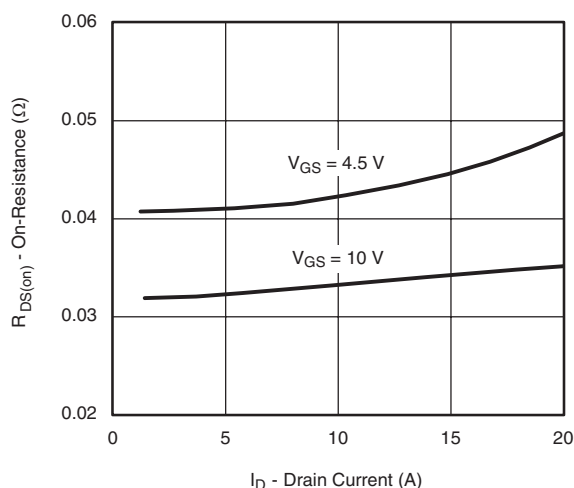
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



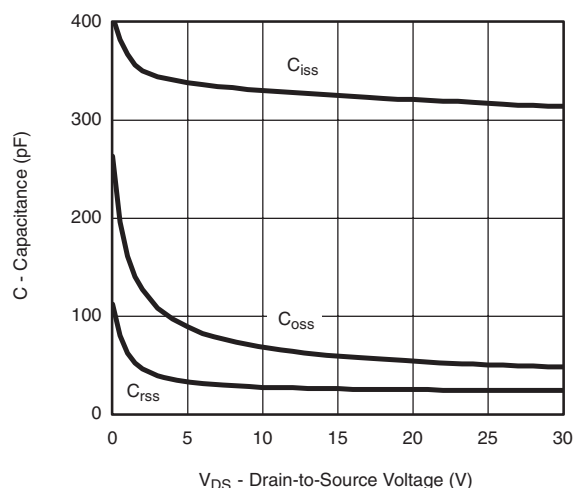
Output Characteristics



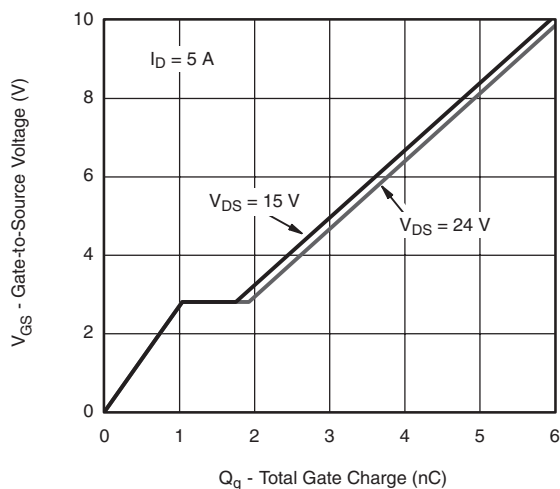
Transfer Characteristics



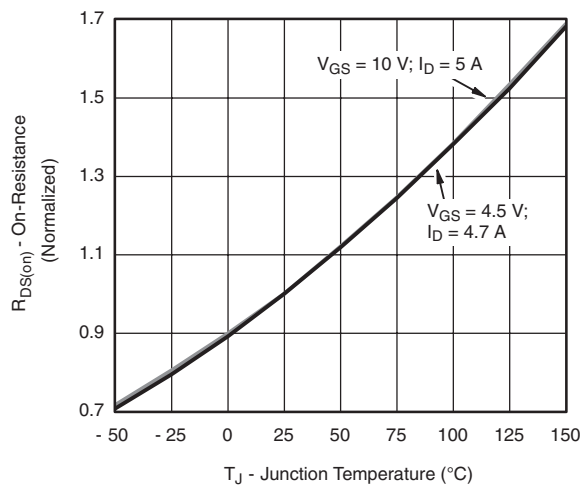
On-Resistance vs. Drain Current



Capacitance



Gate Charge



On-Resistance vs. Junction Temperature

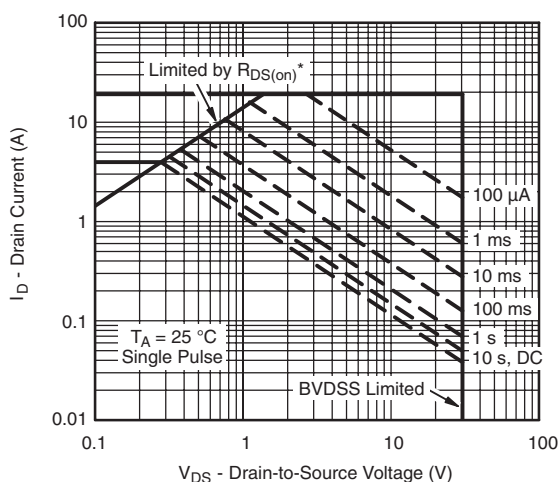
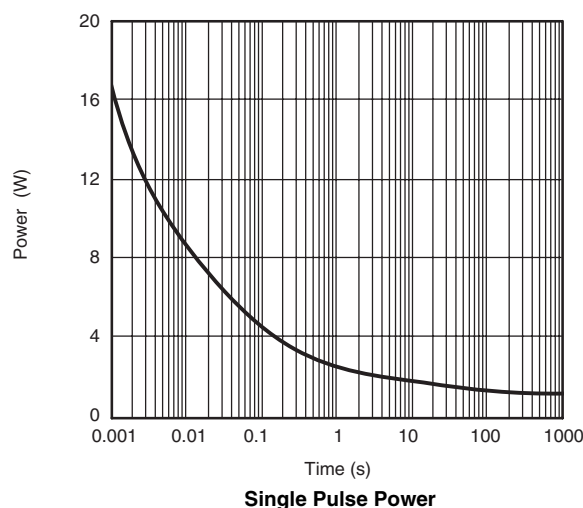
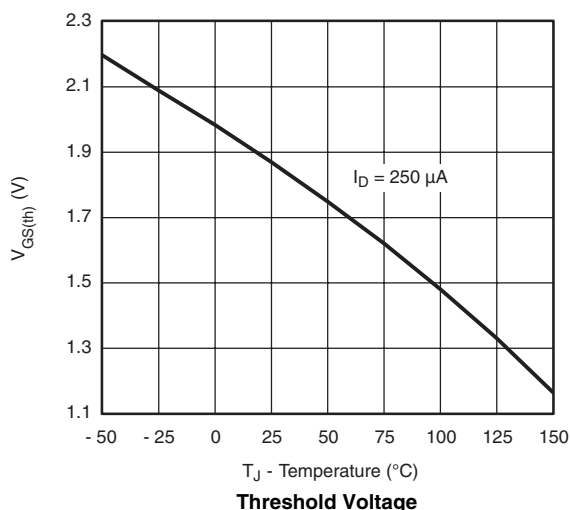
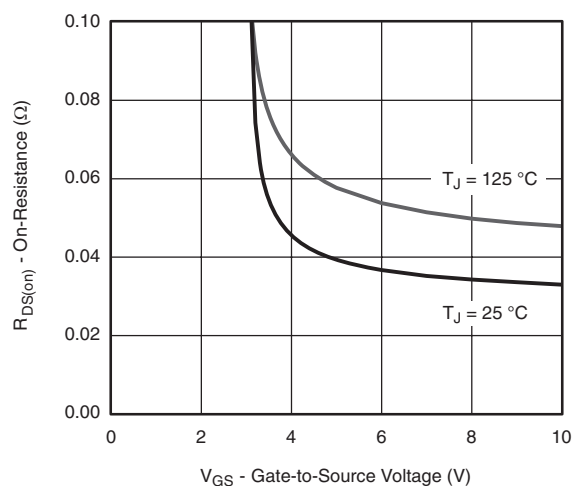
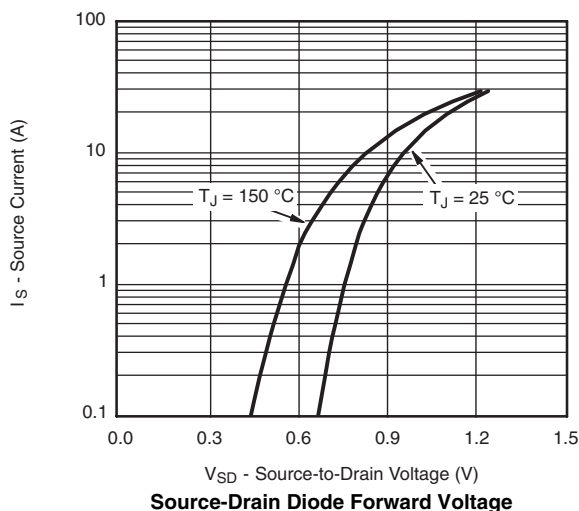
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

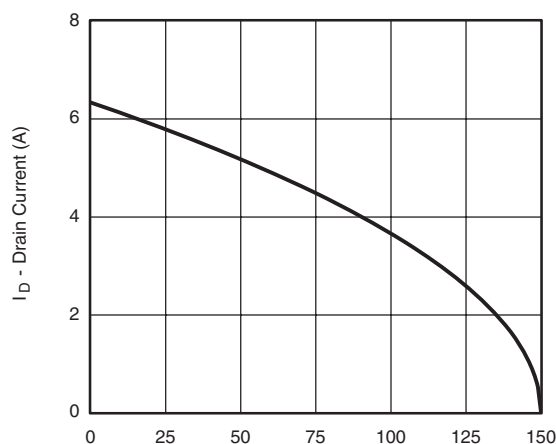
Safe Operating Area, Junction-to-Ambient

New Product



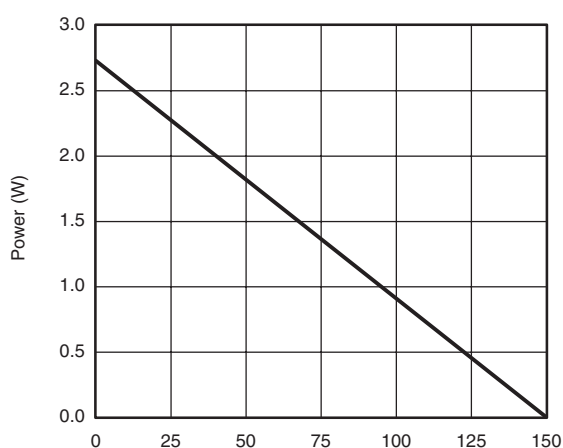
Si4936CDY
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



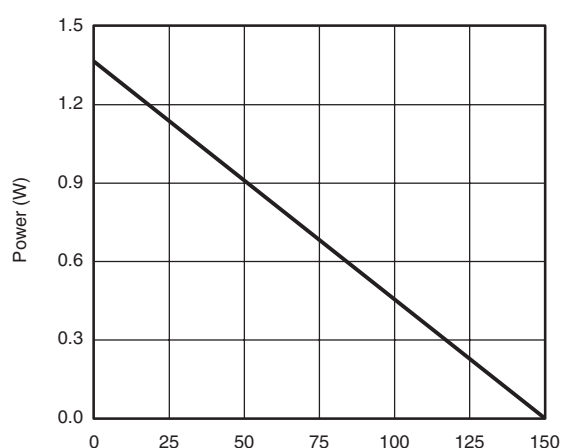
T_C - Case Temperature (°C)

Current Derating*



T_C - Case Temperature (°C)

Power, Junction-to-Case



T_A - Ambient Temperature (°C)

Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

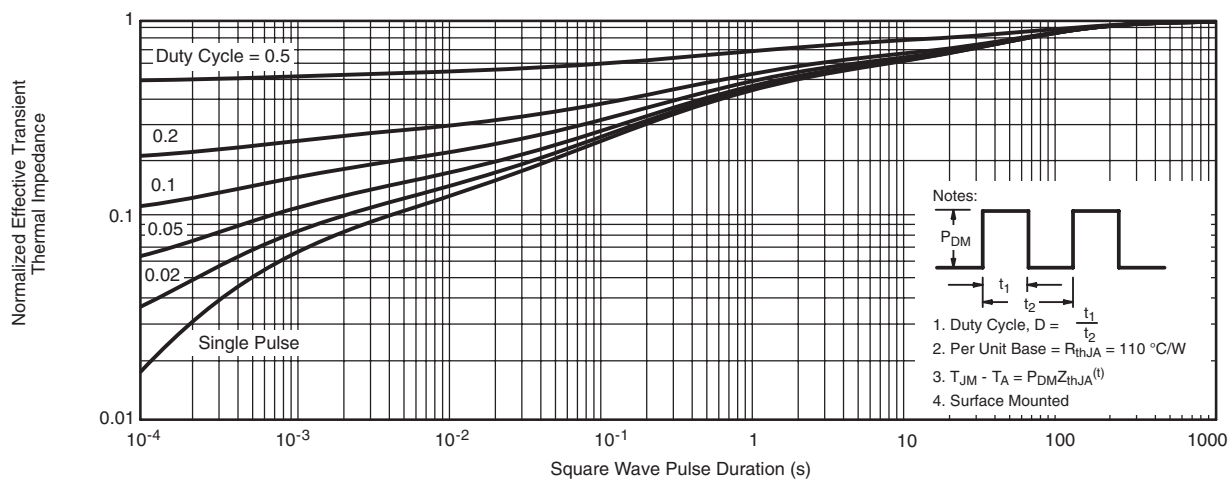
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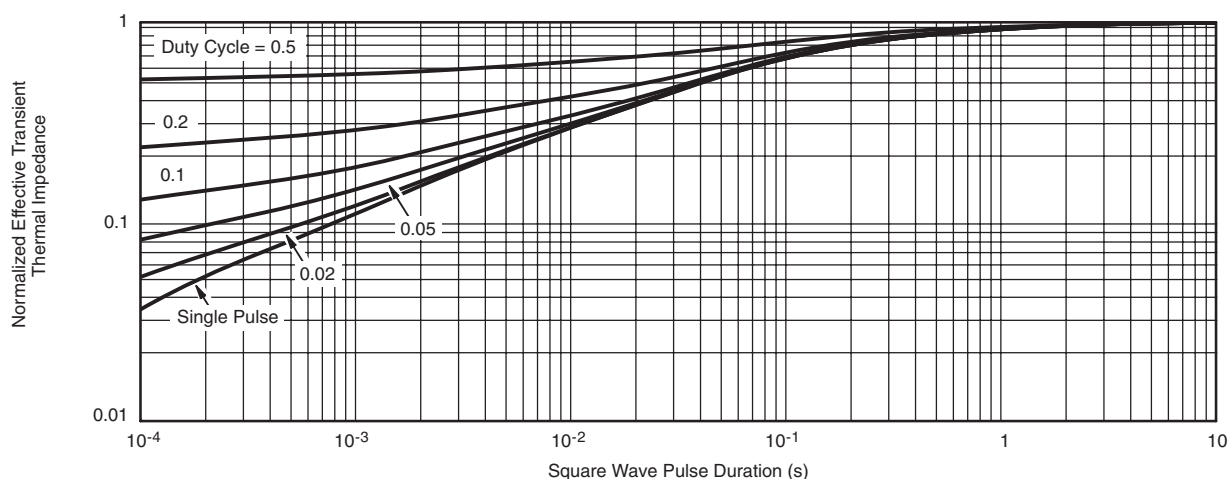
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg269097.

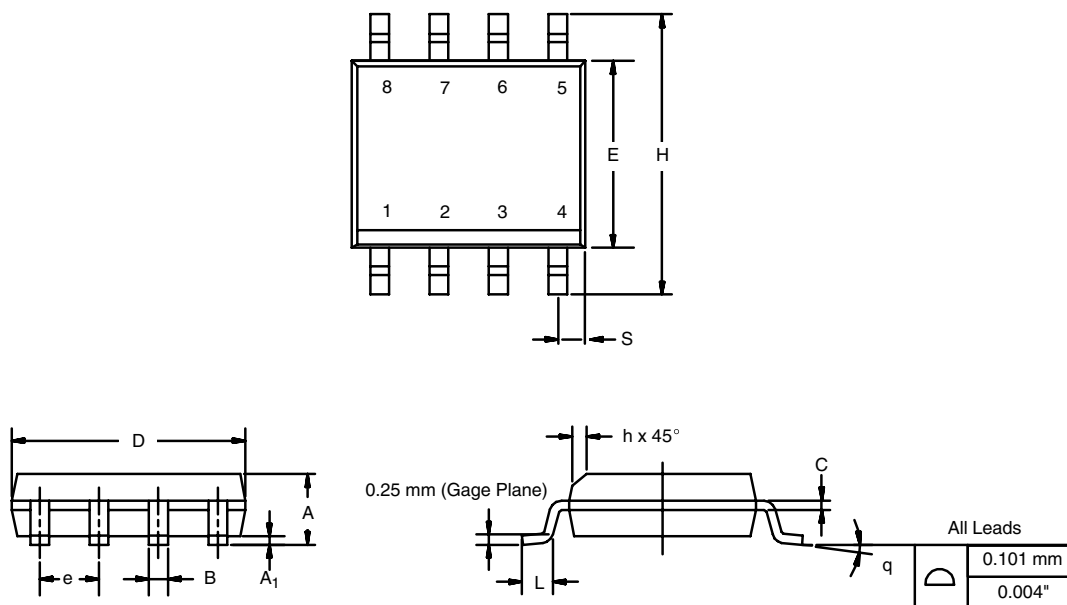


Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026

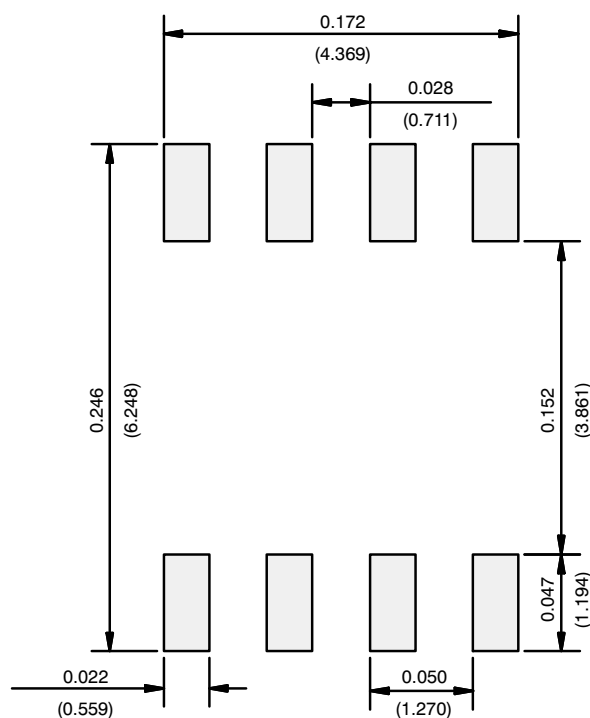
ECN: C-06527-Rev. I, 11-Sep-06
DWG: 5498

Application Note 826

Vishay Siliconix



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

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