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Vishay/Siliconix SI4965DY-T1-E3

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Distributor of Vishay/Siliconix: Excellent Integrated System Limited

Datasheet of SI4965DY-T1-E3 - MOSFET 2P-CH 8V 8SOIC

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Si4965DY

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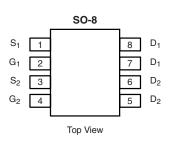
Dual P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	
- 8	0.021 at V _{GS} = - 4.5 V	- 8.0	
	0.027 at V _{GS} = - 2.5 V	- 7.0	
	0.040 at V _{GS} = - 1.8 V	- 5.8	

FEATURES

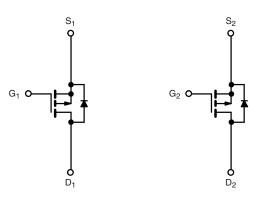
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs: 1.8 V Rated
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si4965DY-T1-E3 (Lead (Pb)-free)

Si4965DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 8	V	
Gate-Source Voltage		V _{GS} ± 8		v	
Continuous Drain Current (T, I = 150 °C) ^{a, b}	T _A = 25 °C	l-	- 8.0		
Continuous Drain Current (1 _J = 150 °C)	T _A = 70 °C	I _D	- 6.4		
Pulsed Drain Current		I _{DM}	- 30	Α	
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	I _S - 1.7		
M. in Branch Branch ab	T _A = 25 °C	P _D 2.0		W	
Maximum Power Dissipation ^{a, b}	T _A = 70 °C	υ υ	1.3]	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 s	- R _{thJA}		62.5	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		93		C/VV	

Notes:

a. Surface Mounted on FR4 board.

 $b.\ t \leq 10\ s.$

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.45			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current		V _{DS} = - 8 V, V _{GS} = 0 V			- 1	μΑ		
	IDSS	V _{DS} = - 8 V, V _{GS} = 0 V, T _J = 70 °C			- 5			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α		
		$V_{GS} = -4.5 \text{ V}, I_D = -8.0 \text{ A}$		0.0175	0.021			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 7.0 A		0.022	0.027	Ω		
		V _{GS} = - 1.8 V, I _D = - 5.8 A		0.031	0.040			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 8.0 A		27		S		
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$			- 1.2	V		
Dynamic ^b								
Total Gate Charge	Q_g			36	55			
Gate-Source Charge	Q_{gs}	$V_{DS} = -4 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -8.0 \text{ A}$		7.5		nC		
Gate-Drain Charge	Q_{gd}			5.0				
Turn-On Delay Time	t _{d(on)}			35	70			
Rise Time	t _r	V_{DD} = - 4 V, R_L = 4 Ω		45	90			
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, $V_{GEN}=$ - 4.5 V, $R_g=$ 6 Ω		170	340	ns		
Fall Time	t _f			90	180			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		60	90			

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

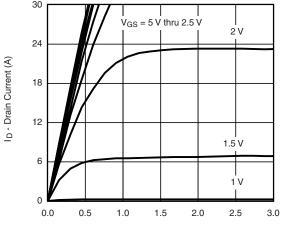




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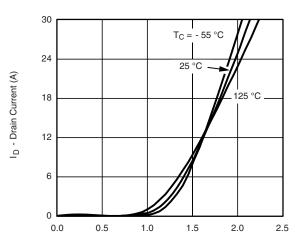
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



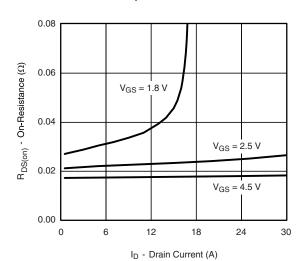
V_{DS} - Drain-to-Source Voltage (V)

Output Characteristics

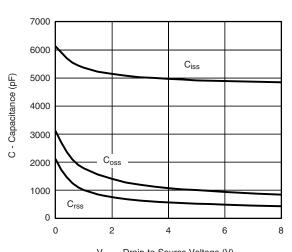


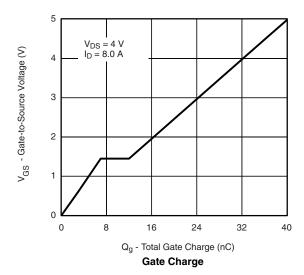


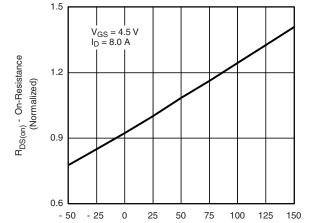
V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**



On-Resistance vs. Drain Current







T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature

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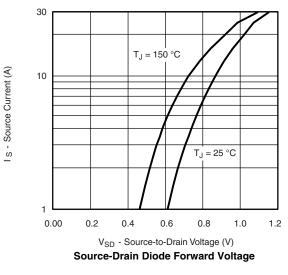


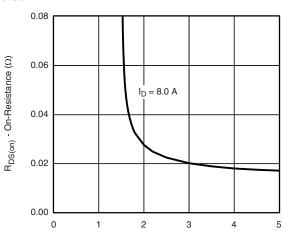
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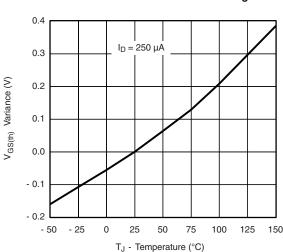
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

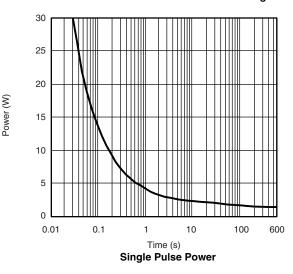


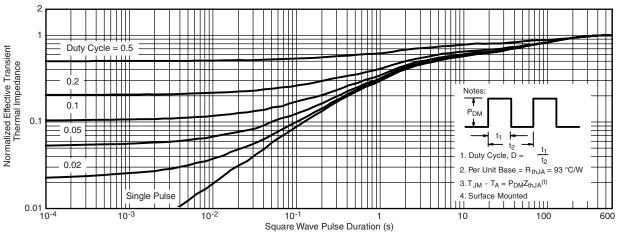




Threshold Voltage

V_{GS} - Gate-to-Source Voltage (V) On-Resistance vs. Gate-to-Source Voltage





Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg270826.



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