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Stocking Distributor

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Vishay/Siliconix SI6413DQ-T1-E3

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>





Si6413DQ

RoHS

COMPLIANT

Vishay Siliconix

P-Channel 1.8-V (G-S) MOSFET

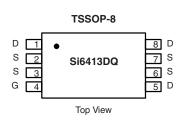
PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 20	0.010 at V _{GS} = - 4.5 V	- 8.8		
	0.013 at V _{GS} = - 2.5 V	- 7.6		
	0.016 at V _{GS} = - 1.8 V	- 6.8		

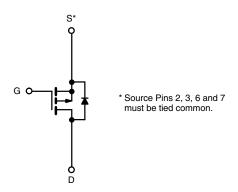
FEATURES

- Halogen-free
- TrenchFET[®] Power MOSFET

APPLICATIONS

- Load Switch
- PA Switch
- Charger Switch





Ordering Information: Si6413DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \degree C$, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 8			
Continuous Drain Current /T 150 °C\a	T _A = 25 °C	- I _D	- 8.8	- 7.2		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 7.0	- 5.7	•	
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	- 30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 1.35	- 0.95		
Maximum Power Dissipation ^a	T _A = 25 °C	PD	1.5	1.05	W	
	T _A = 70 °C	- FD	1.0	0.67		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

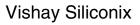
THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum lugation to Amelianta	t ≤ 10 s	- R _{thJA}	60	83	
Maximum Junction-to-Ambient ^a	Steady State		100	120	°C/W
Maximum Junction-to-Foot	Steady State	R _{thJF}	35	45	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



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SPECIFICATIONS $T_J = 25 \ ^\circ C$	C, unless c	therwise noted					
Parameter	Symbol	Test Conditions	itions Min.		Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -400 \ \mu A$	- 0.40		- 0.8	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -16 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		- 1			
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 20			А	
		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -8.8 \text{ A}$		0.008	0.010		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -7.6 \text{ A}$ 0.010		0.010	0.013	Ω	
		V_{GS} = - 1.8 V, I_D = - 6.8 A		0.013	0.016		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -8.8 \text{ A}$		45		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = -1.3 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.58	- 1.1	V	
Dynamic ^b							
Total Gate Charge	Qg			69	105	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 5 V, I_D = - 8.8 A		9.5			
Gate-Drain Charge	Q _{gd}			15.5		1	
Turn-On Delay Time	t _{d(on)}			55	85		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		120	200		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_G = 6 Ω		305	470	ns	
Fall Time	t _f			160	250		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.3 A, di/dt = 100 A/μs		90	150		

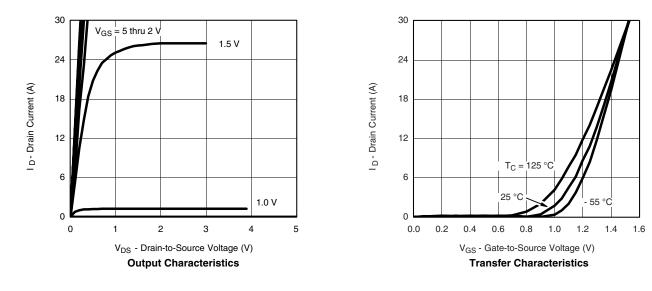
Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

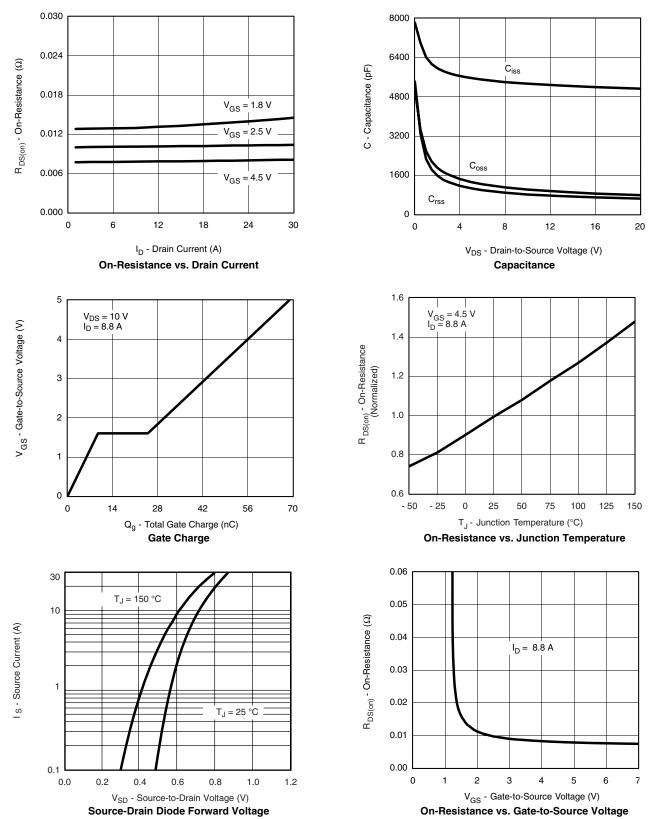






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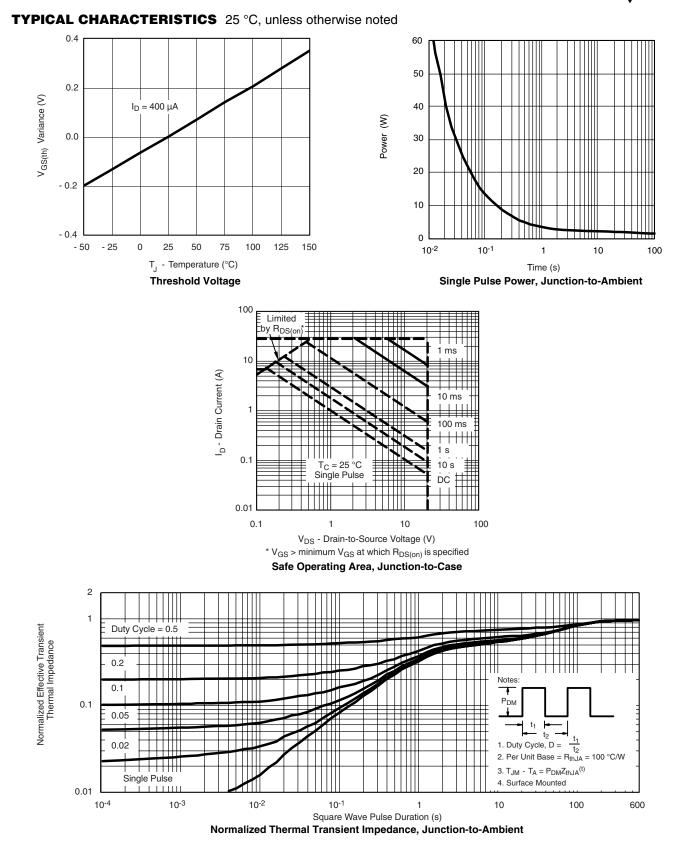
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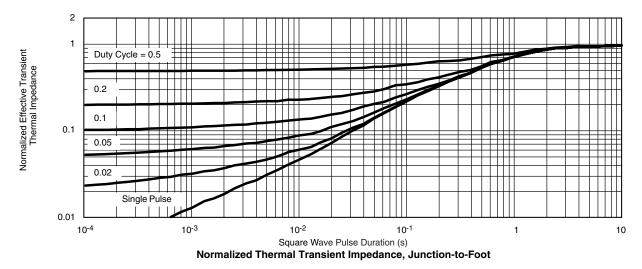




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