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Vishay/Siliconix
SI6966DQ-T1-E3

For any questions, you can email us directly:
sales@integrated-circuit.com

Dual N-Channel 2.5-V (G-S) MOSFET

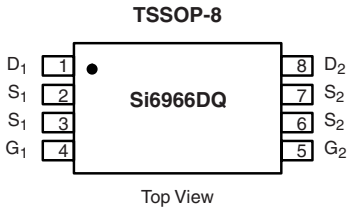
PRODUCT SUMMARY		
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
20	0.030 at V_{GS} = 4.5 V	4.5
	0.040 at V_{GS} = 2.5 V	3.9

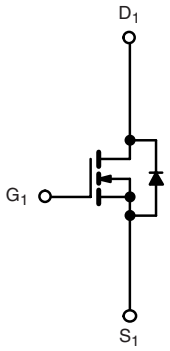
FEATURES

- **Halogen-free Option Available**
- TrenchFET® Power MOSFETs: 2.5 V Rated

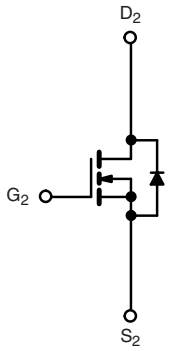
Pb-free Available

RoHS* COMPLIANT





N-Channel MOSFET



N-Channel MOSFET

Ordering Information: Si6966DQ-T1
Si6966DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted					
Parameter		**Symbol**	**10 s**	**Steady State**	**Unit**
Drain-Source Voltage		V_{DS}	20		V
Gate-Source Voltage		V_{GS}	± 12		
Continuous Drain Current (T_J = 150 °C)[a]	T_A = 25 °C	I_D	4.5	4.0	A
	T_A = 70 °C		3.6	3.0	
Pulsed Drain Current		I_{DM}	30		
Continuous Source Current (Diode Conduction)[a]		I_S	1.25	0.75	
Maximum Power Dissipation[a]	T_A = 25 °C	P_D	1.14	0.83	W
	T_A = 70 °C		0.73	0.53	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		**Symbol**	**Typical**	**Maximum**	**Unit**
Maximum Junction-to-Ambient[a]	t ≤ 10 s	R_{thJA}	86	110	°C/W
	Steady State		124	150	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	52	65	

Notes:
a. Surface Mounted on FR4 board.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm.

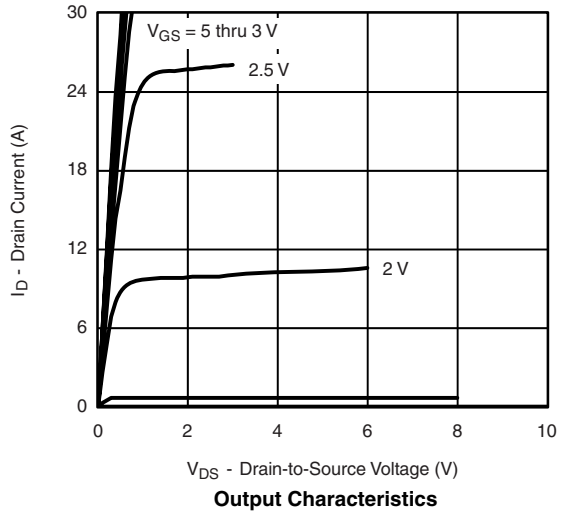
* Pb containing terminations are not RoHS compliant, exemptions may apply.

SPECIFICATIONS T_J = 25 °C, unless otherwise noted						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	0.6		1.4	V
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 12 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 20 V, V_{GS} = 0 V			1	µA
		V_{DS} = 20 V, V_{GS} = 0 V, T_J = 55 °C			25	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} = 4.5 V	30			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 4.5 V, I_D = 4.5 A		0.021	0.030	Ω
		V_{GS} = 2.5 V, I_D = 3.9 A		0.030	0.040	
Forward Transconductance[a]	g_{fs}	V_{DS} = 10 V, I_D = 4.5 A		20		S
Diode Forward Voltage[a]	V_{SD}	I_S = 1.25 A, V_{GS} = 0 V		0.65	1.2	V
Dynamic[b]						
Total Gate Charge	Q_g	V_{DS} = 10 V, V_{GS} = 4.5 V, I_D = 4.5 A		11.5	20	nC
Gate-Source Charge	Q_{gs}			1.9		
Gate-Drain Charge	Q_{gd}			3.6		
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 10 V, R_L = 10 Ω I_D ≅ 1 A, V_{GEN} = 10 V, R_G = 6 Ω		11	20	ns
Rise Time	t_r			9	15	
Turn-Off Delay Time	$t_{d(off)}$			36	55	
Fall Time	t_f			11	20	
Source-Drain Reverse Recovery Time	t_{rr}	I_F = 1.25 A, dI/dt = 100 A/µs		30	60	

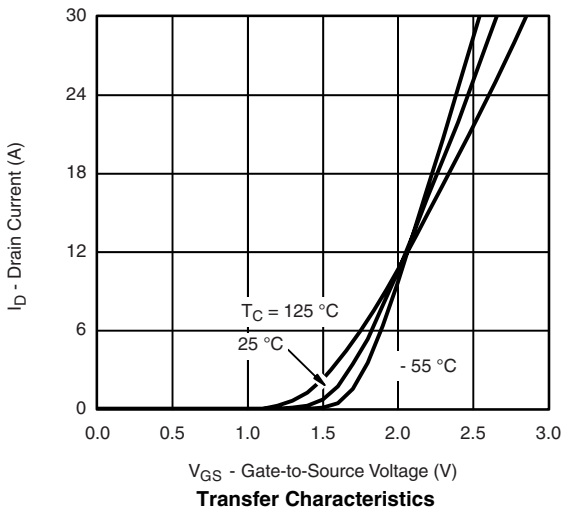
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

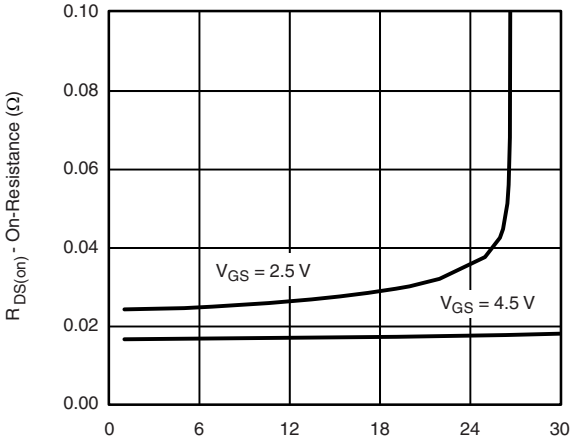


Output Characteristics

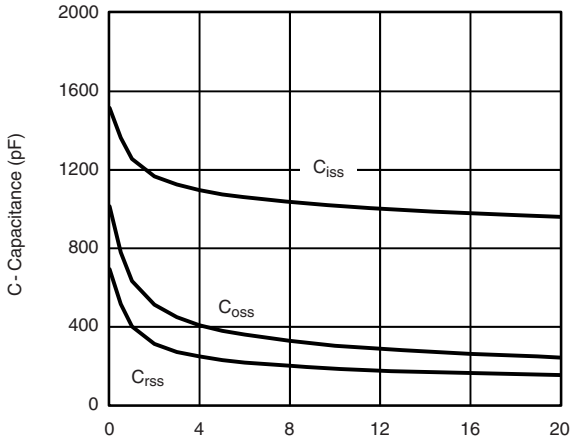


Transfer Characteristics

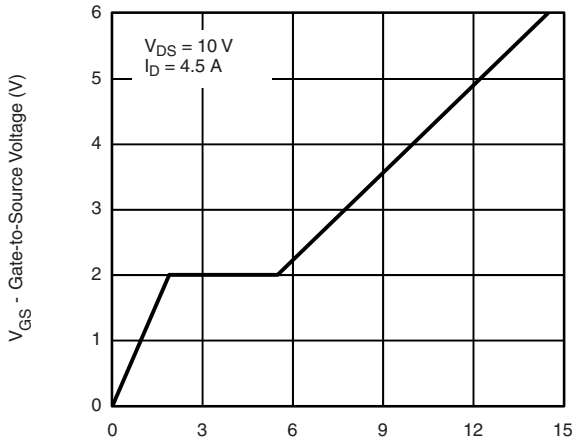
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



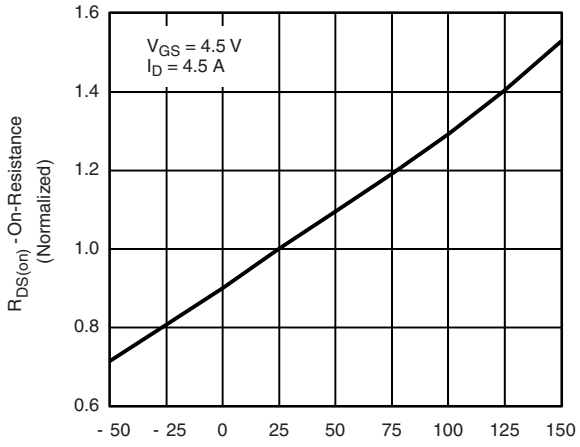
On-Resistance vs. Drain Current



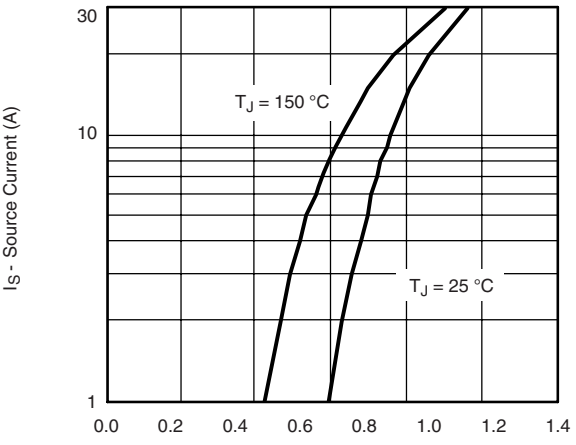
Capacitance



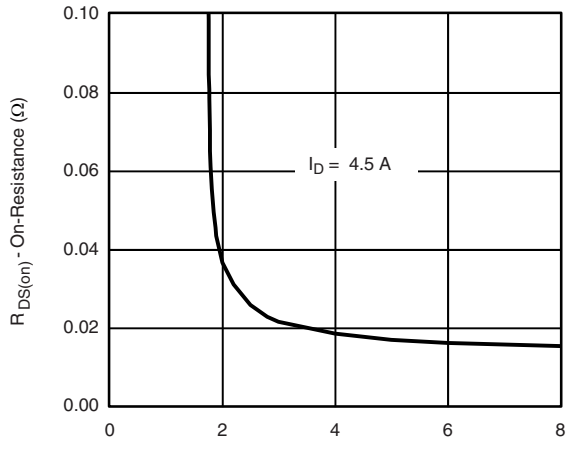
Gate Charge



On-Resistance vs. Junction Temperature

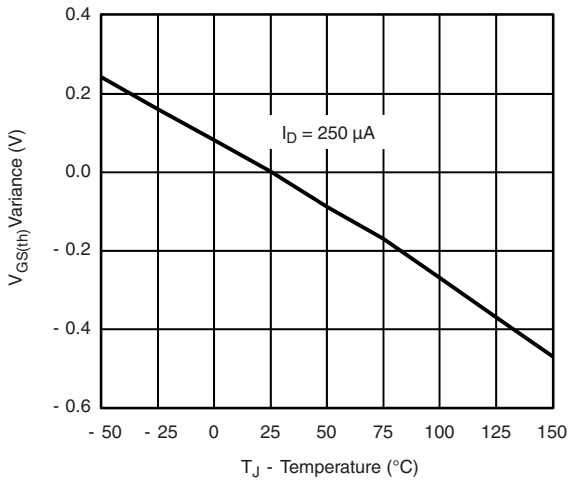


Source-Drain Diode Forward Voltage

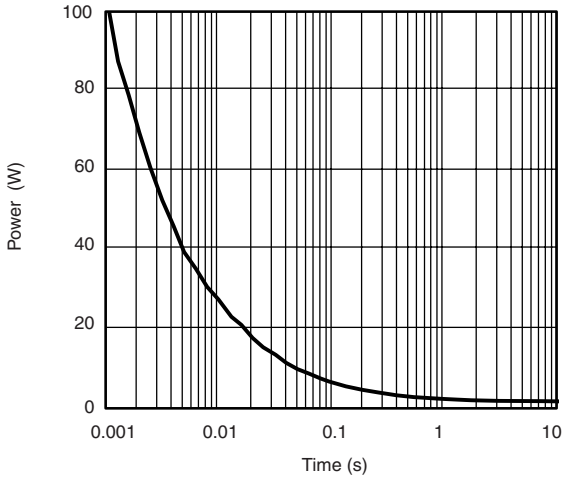


On-Resistance vs. Gate-to-Source Voltage

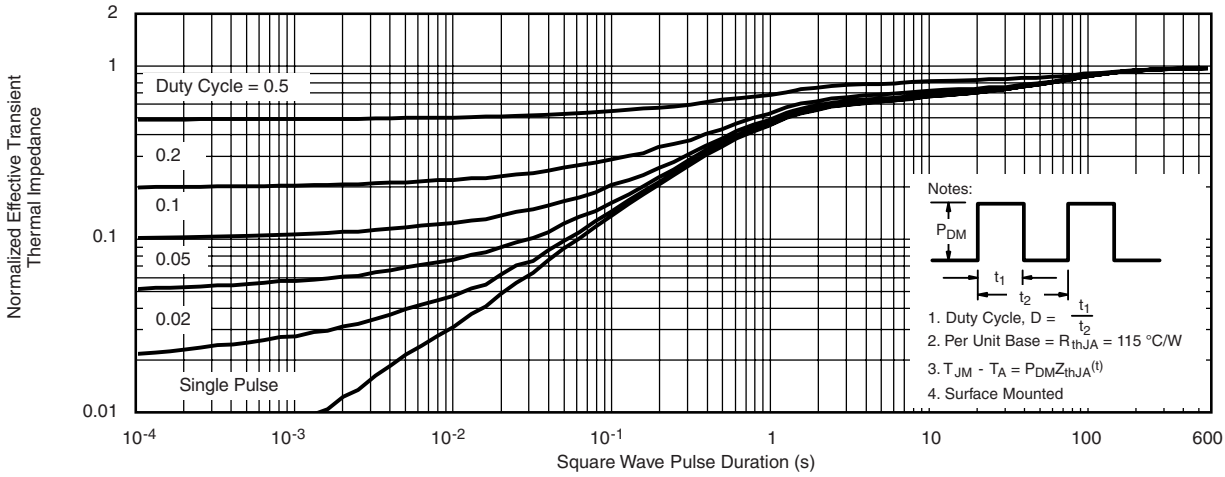
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



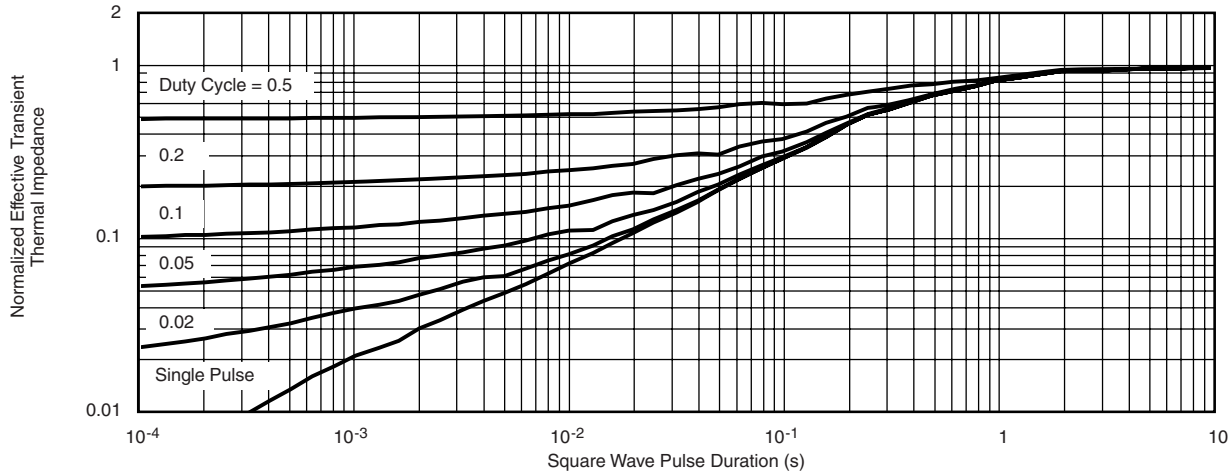
Threshold Voltage



Single Pulse Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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