

## **Excellent Integrated System Limited**

Stocking Distributor

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[Vishay/Siliconix](#)  
[SI7901EDN-T1-E3](#)

For any questions, you can email us directly:

[sales@integrated-circuit.com](mailto:sales@integrated-circuit.com)



## Dual P-Channel 20-V (D-S) MOSFET

<b>PRODUCT SUMMARY</b>		
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
- 20	0.048 at $V_{GS} = - 4.5$ V	- 6.3
	0.068 at $V_{GS} = - 2.5$ V	- 5.3
	0.090 at $V_{GS} = - 1.8$ V	- 4.6

### FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFETs: 1.8 V Rated
- ESD Protected: 4500 V
- Ultra Low Thermal Resistance PowerPAK® Package with Low 1.07 mm Profile

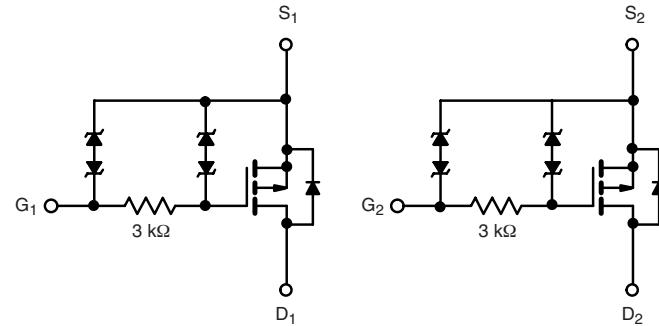
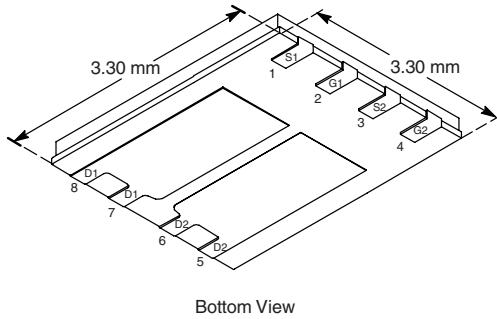


**RoHS**  
COMPLIANT  
**HALOGEN**  
**FREE**  
Available

### APPLICATIONS

- Bidirectional Switch

PowerPAK 1212-8



**Ordering Information:** Si7901EDN-T1-E3 (Lead (Pb)-free)  
Si7901EDN-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25$ °C, unless otherwise noted				
Parameter	Symbol	10 s	Steady State	Unit
Drain-Source Voltage	$V_{DS}$	- 20		V
Gate-Source Voltage	$V_{GS}$			
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$T_A = 25$ °C	$I_D$	- 6.3	A
	$T_A = 85$ °C		- 4.5	
Pulsed Drain Current	$I_{DM}$	- 20		A
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$		- 2.3	
Maximum Power Dissipation <sup>a</sup>	$T_A = 25$ °C	$P_D$	2.8	W
	$T_A = 85$ °C		1.5	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150		°C
Soldering Recommendations <sup>b,c</sup>		260		

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ s	$R_{thJA}$	35	°C/W
	Steady State		75	
Maximum Junction-to-Case (Drain)	Steady State	$R_{thJC}$	4	5

Notes:

- a. Surface Mounted on 1" x 1" FR4 board.
- b. See Solder Profile ([www.vishay.com/ppa?73257](http://www.vishay.com/ppa?73257)). The PowerPAK 1212-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

# Si7901EDN

Vishay Siliconix



## SPECIFICATIONS $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Test Conditions		Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -800 \mu\text{A}$	-0.45		-1.0	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			$\pm 1.5$	$\text{vA}$
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			$\pm 10$	$\text{mA}$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$			-1	$\mu\text{A}$
		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 85^\circ\text{C}$			-5	
On-State Drain Current <sup>a</sup>	$I_{D(\text{on})}$	$V_{DS} \leq -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-20			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(\text{on})}$	$V_{GS} = -4.5 \text{ V}, I_D = -6.3 \text{ A}$		0.041	0.048	
		$V_{GS} = -2.5 \text{ V}, I_D = -5.3 \text{ A}$		0.057	0.068	$\Omega$
		$V_{GS} = -1.8 \text{ V}, I_D = -1 \text{ A}$		0.072	0.090	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -15 \text{ V}, I_D = -6.3 \text{ A}$		14		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -2.3 \text{ A}, V_{GS} = 0 \text{ V}$		-0.8	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -6.3 \text{ A}$		12	18	nC
Gate-Source Charge	$Q_{gs}$			2.5		
Gate-Drain Charge	$Q_{gd}$			2.9		
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -10 \text{ V}, R_L = 10 \Omega$ $I_D \approx -1 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_G = 6 \Omega$		2.5	4	$\mu\text{s}$
Rise Time	$t_r$			4	6	
Turn-Off Delay Time	$t_{d(\text{off})}$			15	23	
Fall Time	$t_f$			12	18	

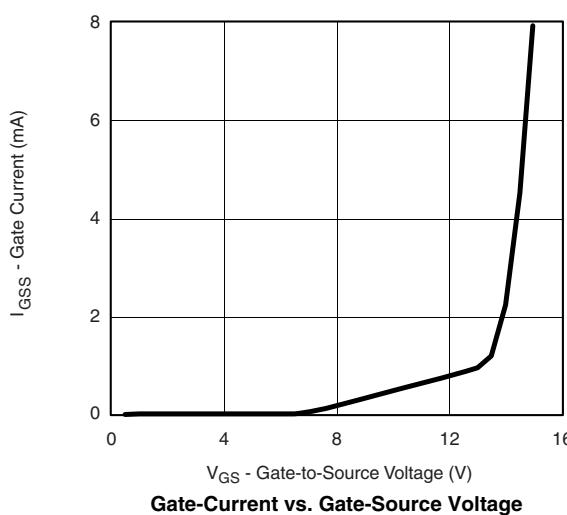
### Notes

a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .

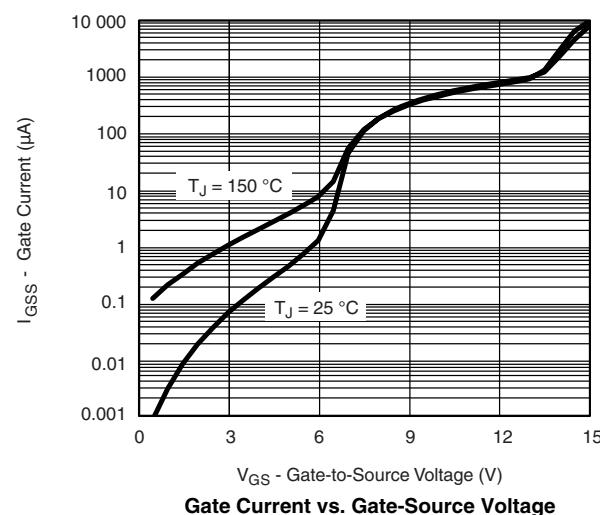
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## TYPICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$ , unless otherwise noted

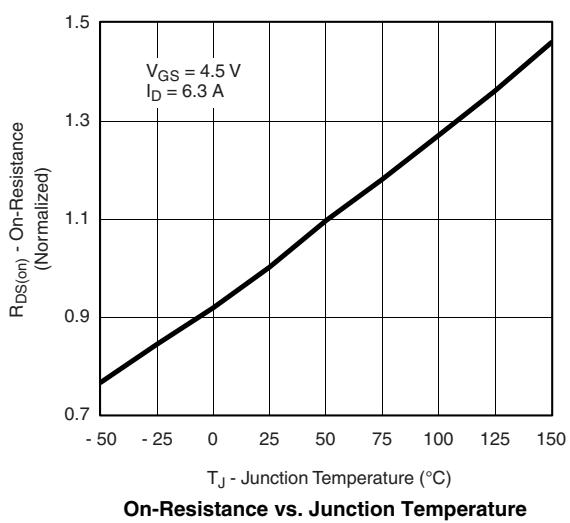
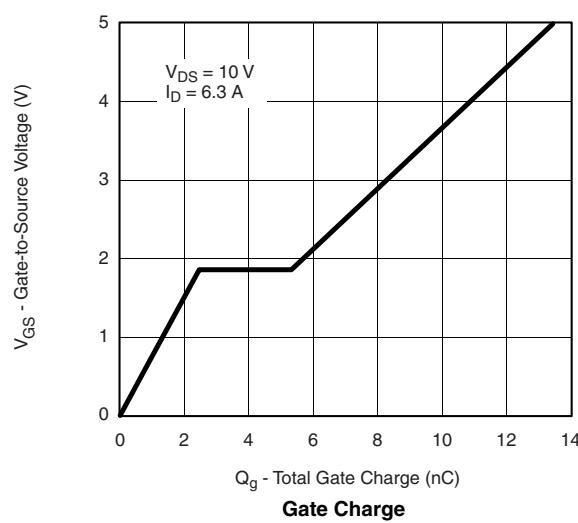
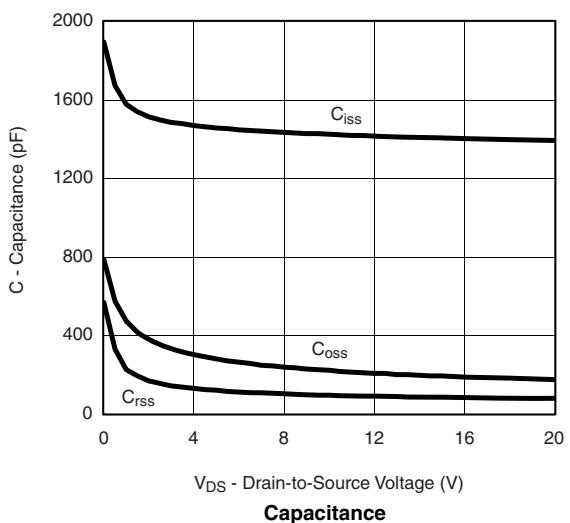
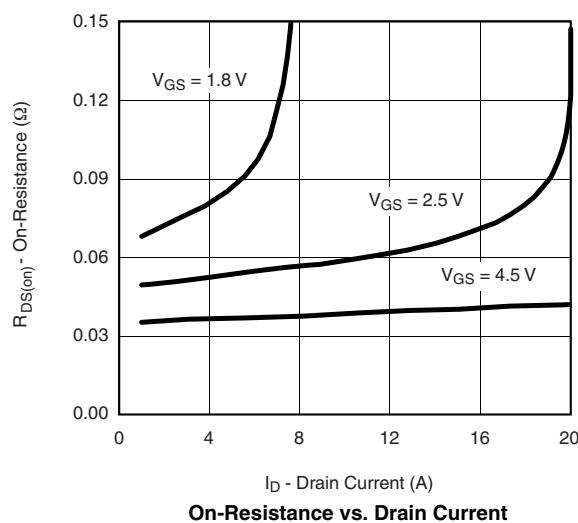
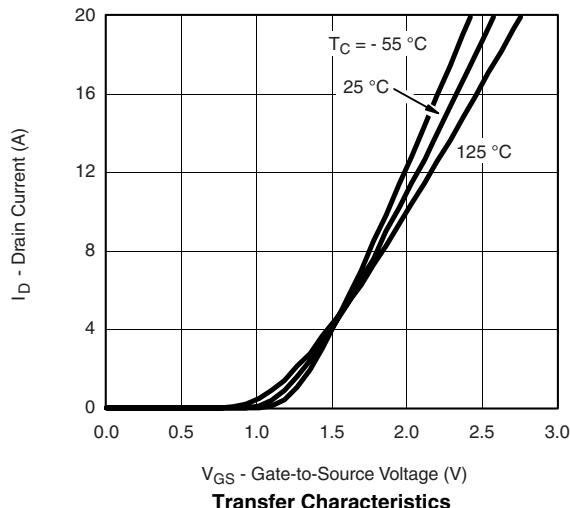
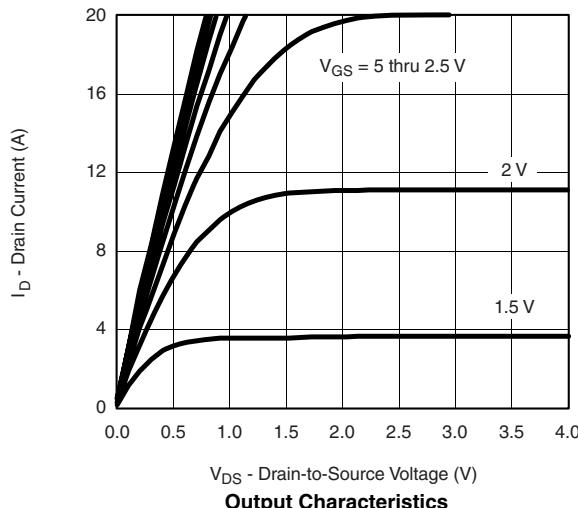


Gate-Current vs. Gate-Source Voltage





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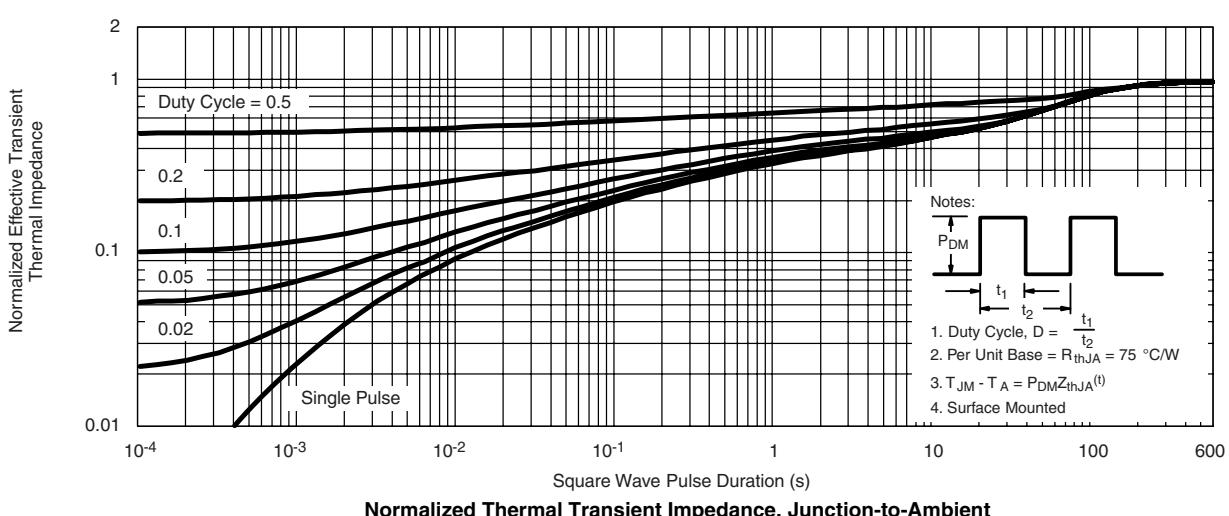
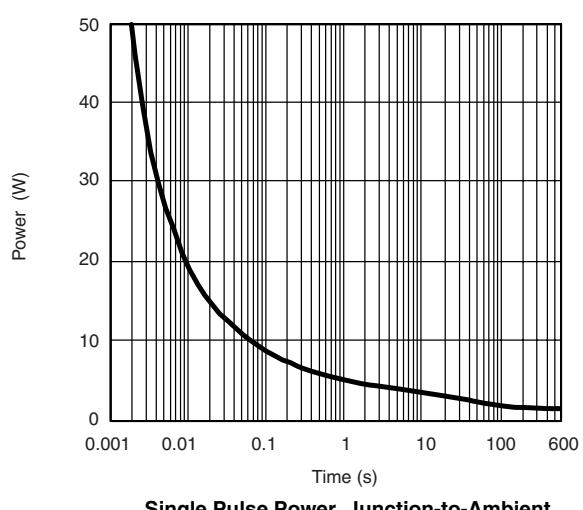
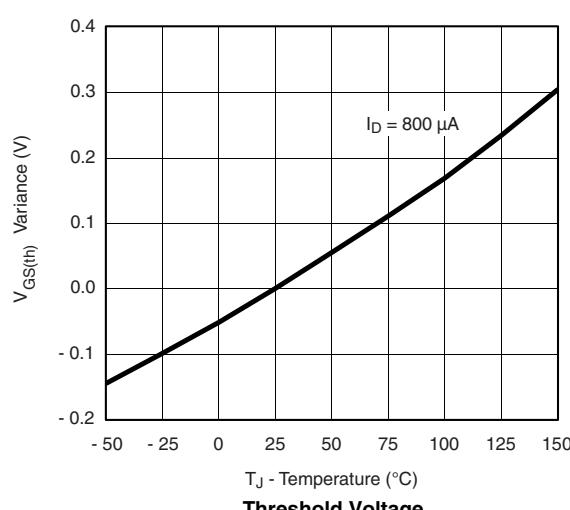
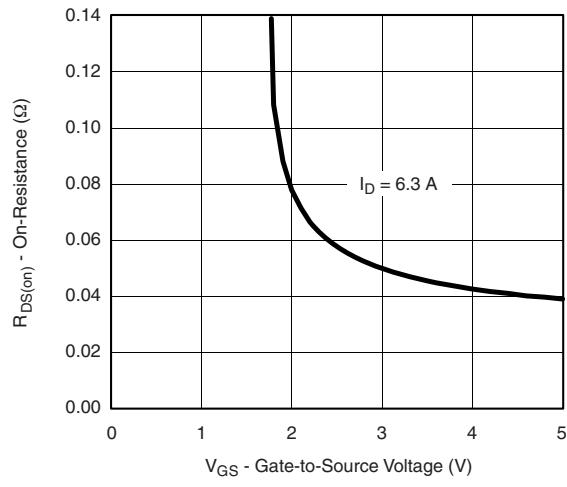
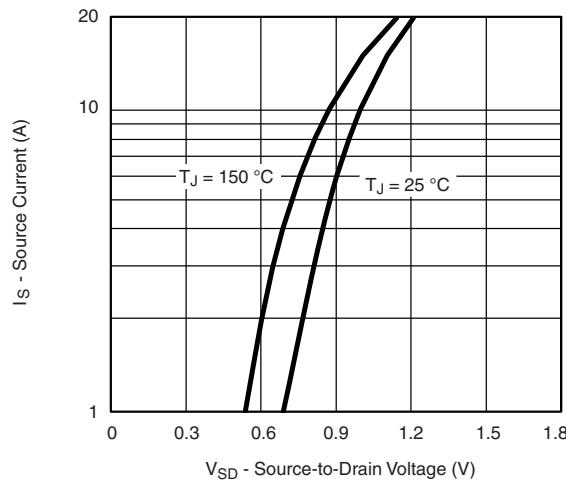


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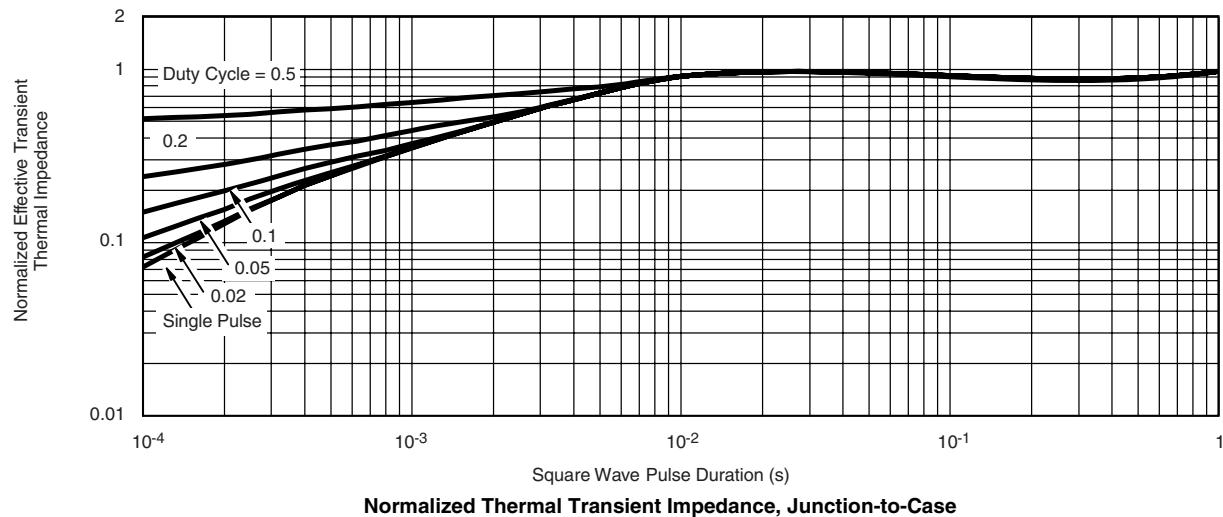


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