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Stocking Distributor

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[Vishay/Siliconix](#)
[SI9410BDY-T1-E3](#)

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N-Channel 30-V (D-S) MOSFET

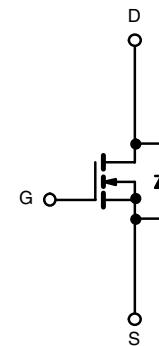
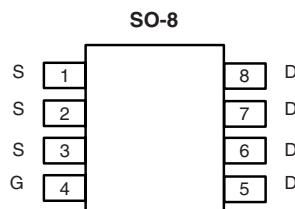
PRODUCT SUMMARY		
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
30	0.024 at $V_{GS} = 10$ V	8.1
	0.033 at $V_{GS} = 4.5$ V	6.9

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN FREE
Available



Ordering Information: Si9410BDY-T1-E3 (Lead (Pb)-free)
Si9410BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V_{DS}	30		V	
Gate-Source Voltage		V_{GS}	± 20			
Continuous Drain Current ($T_J = 150$ °C) ^a	$T_A = 25$ °C	I_D	8.1	6.2	A	
	$T_A = 70$ °C		6.5	5.0		
Pulsed Drain Current (10 μ s Pulse Width)		I_{DM}	30			
Continuous Source Current (Diode Conduction) ^a		I_S	2.1	1.2		
Maximum Power Dissipation ^a	$T_A = 25$ °C	P_D	2.5	1.5	W	
	$T_A = 70$ °C		1.6	0.9		
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	$t \leq 10$ s	R_{thJA}	40	50	°C/W
	Steady State		70	85	
Maximum Junction-to-Foot	R_{thJF}	20	24		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

Si9410BDY

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SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$	1.0		3.0	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30 \text{ V}$, $V_{GS} = 0 \text{ V}$		1		μA
		$V_{DS} = 30 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_J = 55^\circ\text{C}$		5		
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \geq 5 \text{ V}$, $V_{GS} = 10 \text{ V}$	30			A
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$, $I_D = 8.1 \text{ A}$		0.019	0.024	Ω
		$V_{GS} = 4.5 \text{ V}$, $I_D = 6.9 \text{ A}$		0.026	0.033	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15 \text{ V}$, $I_D = 8.1 \text{ A}$		20		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 2.1 \text{ A}$, $V_{GS} = 0 \text{ V}$		0.8	1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = 15 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 8.1 \text{ A}$		15	23	nC
Gate-Source Charge	Q_{gs}			3.2		
Gate-Drain Charge	Q_{gd}			2.5		
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = 15 \text{ V}$, $R_L = 15 \Omega$ $I_D \geq 1 \text{ A}$, $V_{GEN} = 10 \text{ V}$, $R_g = 6 \Omega$		10	15	ns
Rise Time	t_r			15	25	
Turn-Off Delay Time	$t_{d(\text{off})}$			30	45	
Fall Time	t_f			11	20	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 2.1 \text{ A}$, $dI/dt = 100 \text{ A}/\mu\text{s}$		25	50	

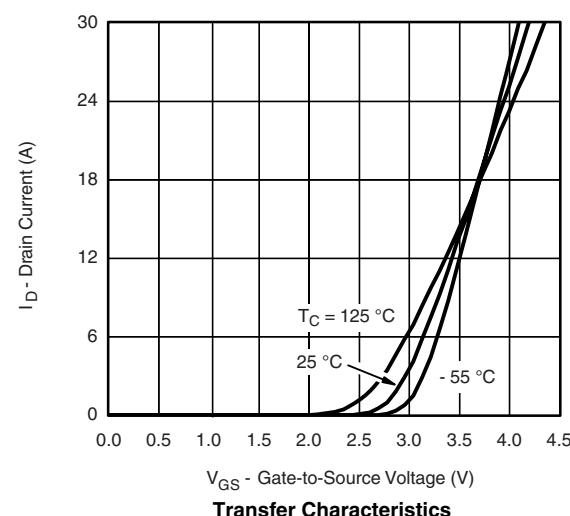
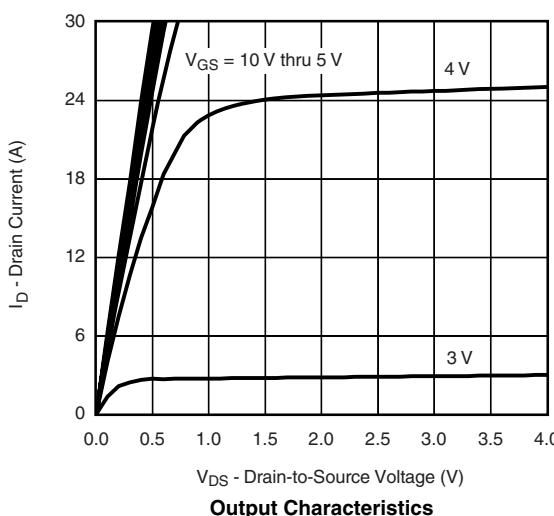
Notes:

a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25°C , unless otherwise noted

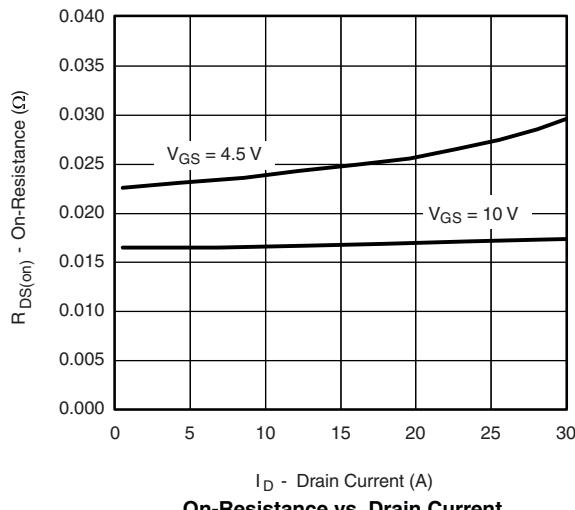




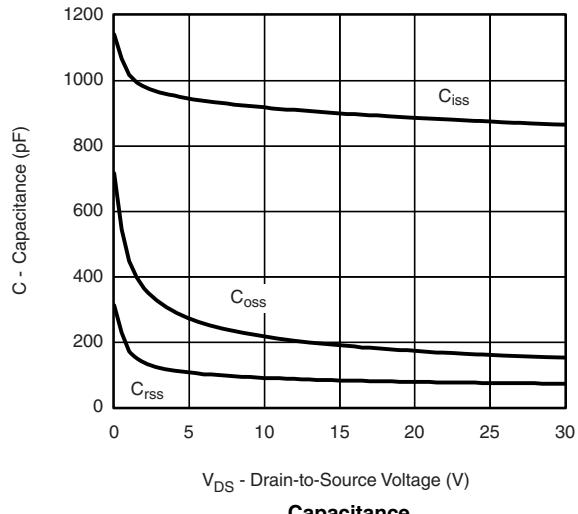
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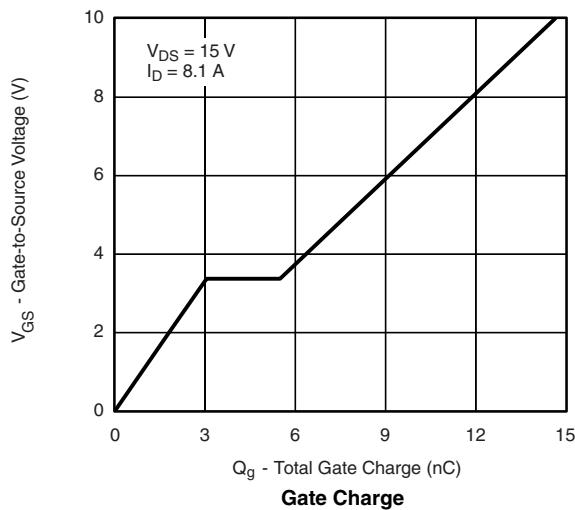
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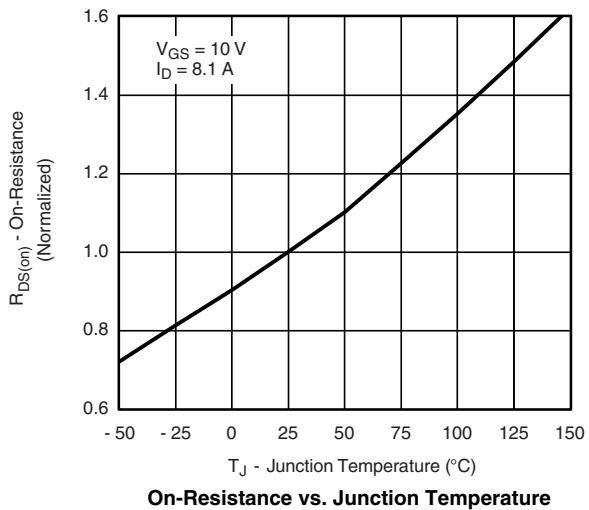
On-Resistance vs. Drain Current



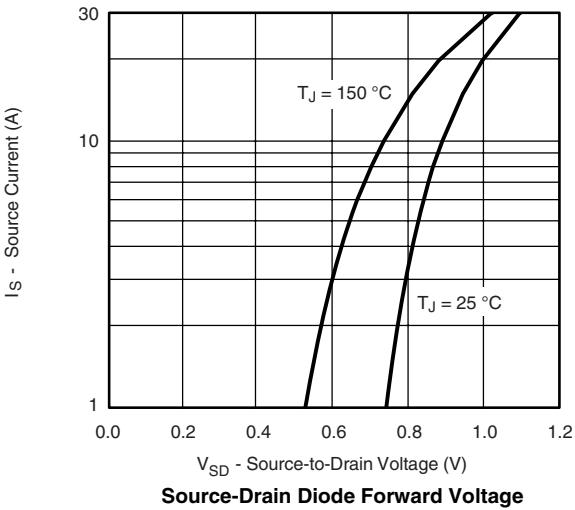
Capacitance



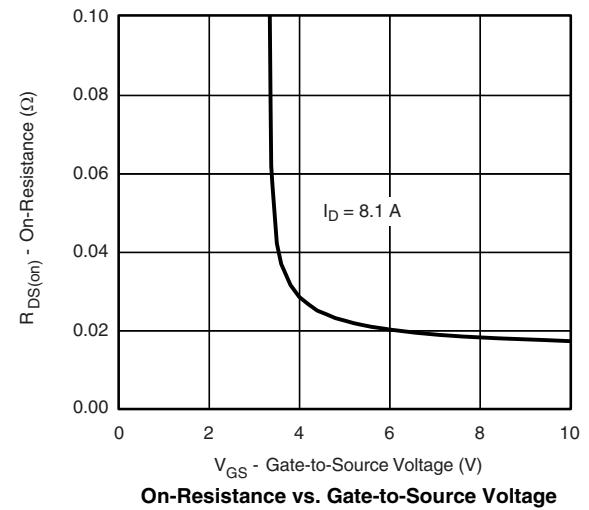
Gate Charge



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



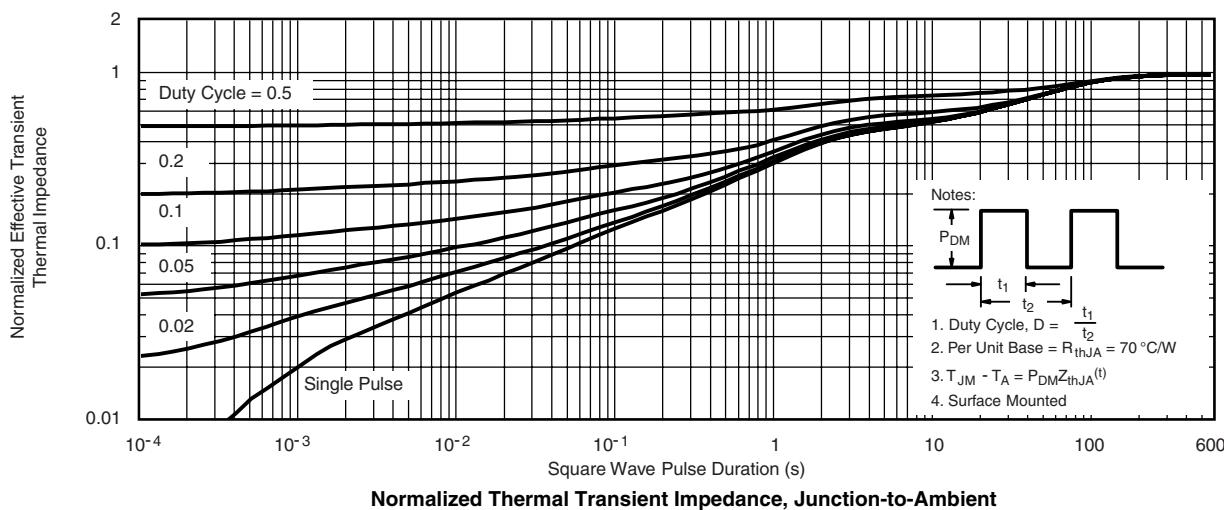
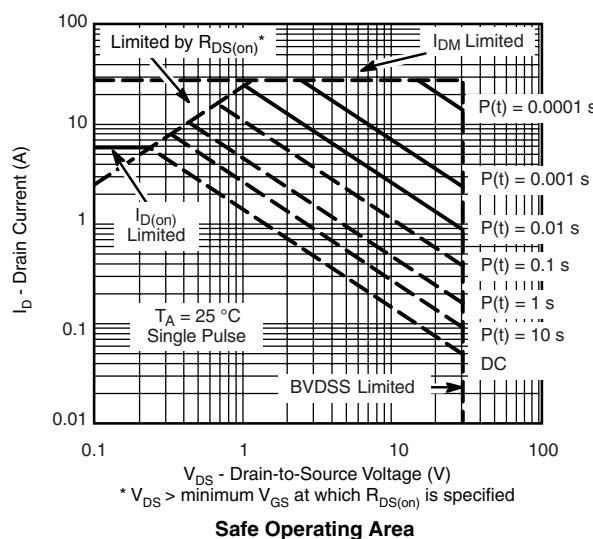
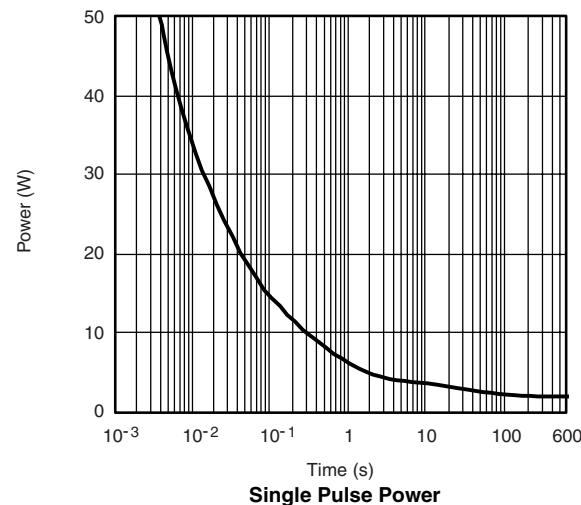
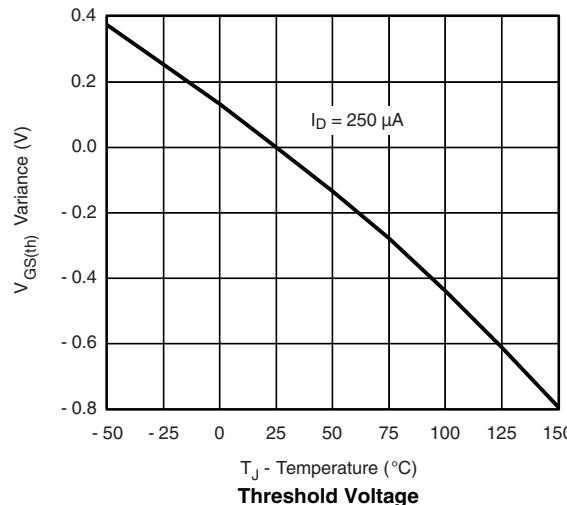
On-Resistance vs. Gate-to-Source Voltage

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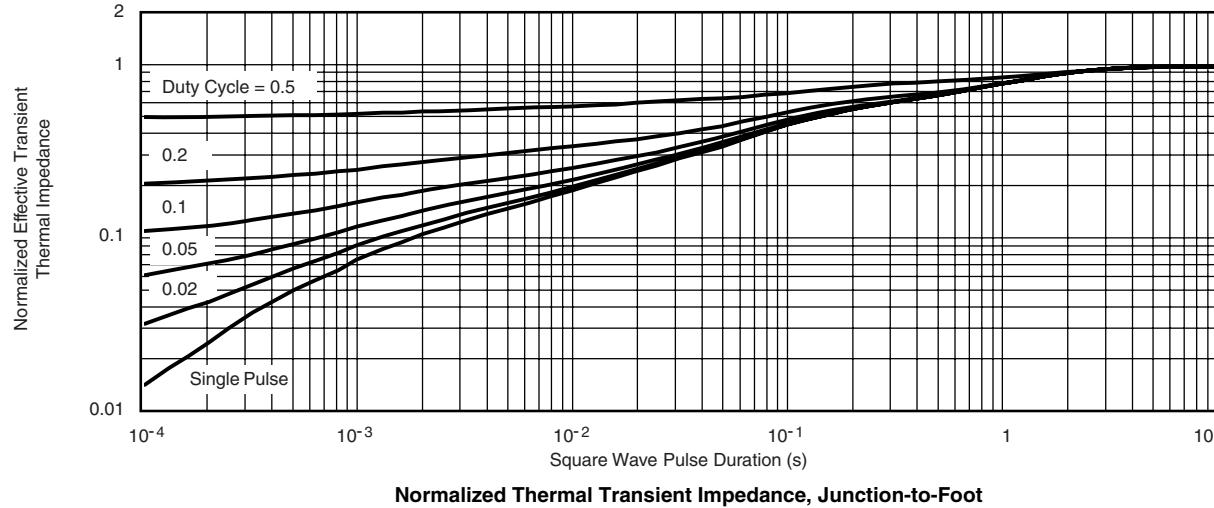
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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