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Vishay Semiconductor/Diodes Division VS-10ETS08FPPBF

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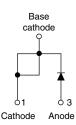
VS-10ETS..FPPbF Series, VS-10ETS..FP-M3 Series

www.vishay.com

Vishay Semiconductors

High Voltage, Input Rectifier Diode, 10 A





TO-220 FULL-PAK

PRODUCT SUMMARY				
Package	TO-220FP			
I _{F(AV)}	10 A			
V _R	800 V to 1200 V			
V _F at I _F	1.1 V			
I _{FSM}	160 A			
T _J max.	150 °C			
Diode variation	Single die			

FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature
- Designed and qualified according to JEDEC-JESD47
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL E78996 approved
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS			
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	12.0	16.0	А	

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Sinusoidal waveform	10	A	
V _{RRM}	Range	800/1200	V	
I _{FSM}		160	А	
V _F	10 A, T _J = 25 °C	1.1	V	
TJ		- 40 to 150	°C	

VOLTAGE RATINGS					
PART NUMBER V _{RRM} , MAXIMUM PART NUMBER PEAK REVERSE VOLTAGE V		V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA		
VS-10ETS08FPPbF, VS-10ETS08FP-M3	800	900	0.5		
VS-10ETS12FPPbF, VS-10ETS12FP-M3	1200	1300	0.5		

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1



RoHS

COMPLIANT HALOGEN

FREE





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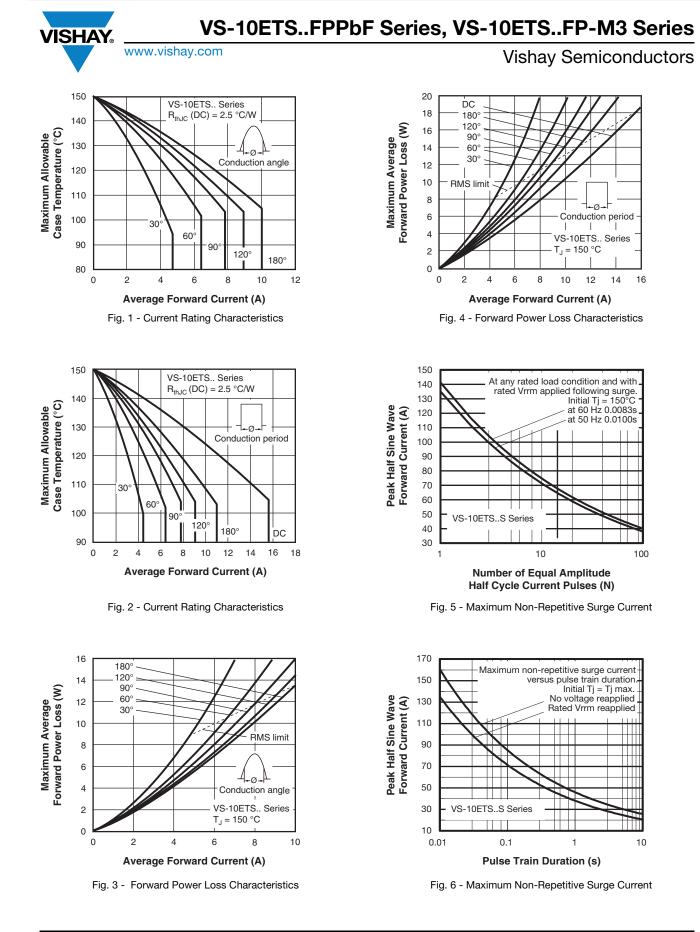
ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	$T_C = 105 \text{ °C}$, 180° conduction half sine wave	10	
Maximum peak one cycle		10 ms sine pulse, rated V_{RRM} applied	135	A
non-repetitive surge current	10 ms sine pulse, no voltage reapplied	160		
Maximum I ² t for fusing I ² t		10 ms sine pulse, rated V_{RRM} applied	91	A ² s
		10 ms sine pulse, no voltage reapplied	130	A-2
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied 1300 $A^2\sqrt{s}$		A²√s

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS V		VALUES	UNITS
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C 1.		1.1	V
Forward slope resistance	r _t	- T _J = 150 °C		20	mΩ
Threshold voltage	V _{F(TO)}			0.82	V
Maximum reverse leakage current	1	T _J = 25 °C	V - Potod V	0.05	mA
Maximum reverse leakage current	I _{RM}	T _J = 150 °C	V _R = Rated V _{RRM}	0.50	ША

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and stor temperature range	rage	T _J , T _{Stg}		- 40 to 150	°C	
Maximum thermal resistan junction to case	ce,	R _{thJC}	DC operation	2.5		
Maximum thermal resistan junction to ambient	ce,	R _{thJA}		62	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
minimum				6 (5)	kgf ⋅ cm	
Mounting torque maximum			12 (10)	(lbf · in)		
Marking device				10ETS08FP		
			Case style TO-220 FULL-PAK (94/V0)	10ETS12FP		

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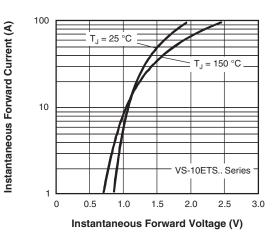


Fig. 7 - Forward Voltage Drop Characteristics

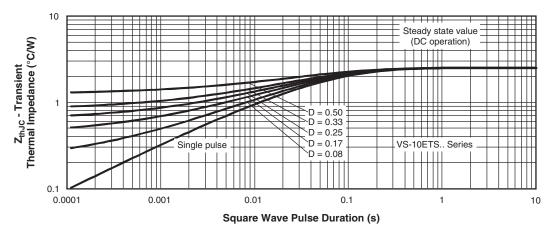


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

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4





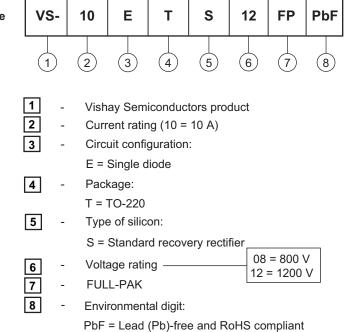
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ORDERING INFORMATION TABLE

Device code



-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-10ETS08FPPbF	50	1000	Antistatic plastic tubes		
VS-10ETS08FP-M3	50	1000	Antistatic plastic tubes		
VS-10ETS12FPPbF	50	1000	Antistatic plastic tubes		
VS-10ETS12FP-M3	50	1000	Antistatic plastic tubes		

LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95005			
Deut meuling information	TO-220FP PbF	www.vishay.com/doc?95009	
Part marking information	TO-220FP -M3	www.vishay.com/doc?95440	

5



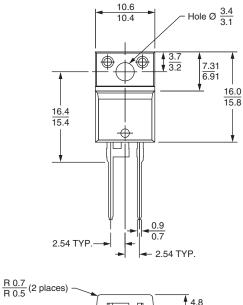


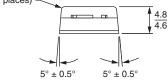
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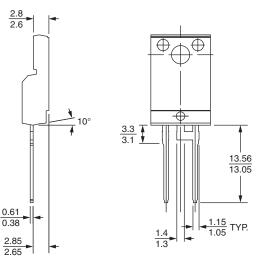
Outline Dimensions

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DIMENSIONS in millimeters







Lead assignments Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220 FULL-PAK

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1





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