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Avago Technologies US, Inc. ASMT-CR00

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Datasheet of ASMT-CR00 - LED RED CLEAR 2SMD

AVAGO ASMT-CR00

AllnGaP Red 0.4mm Low Profile Right Angle Surface Mount ChipLED **Datasheet**

Description

The ASMT-CR00 of red color chip-type LEDs is designed with the smallest footprint to achieve high density of components on board. They have the industry standard footprint 1.6 mm x 1.0 mm and a height of only 0.4 mm. This makes them very suitable for cellular phone and mobile equipment backlighting and indication application where space is a constraint. In order to facilitate automated pick and place operation, these ChipLEDs are shipped in conductive tape and reel, with 4000 units per reel. These part are compatible with IR soldering.

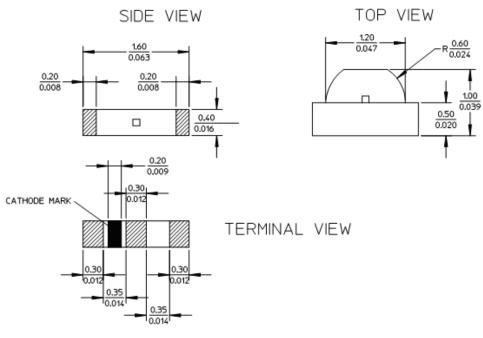
Features

- Small size right angle mount
- 0603 industry standard footprint
- 0.4 mm low profile type
- Operating temperature range of -40°C to +85°C
- Compatible with IR reflow soldering process
- Available in 8mm tape on 178mm (7') diameter reels
- Reel sealed in zip locked moisture barrier bags

Applications

- LCD Backlighting
- Keypad Side / Backlighting
- **Pushbutton backlighting**
- **Symbol Indicator**

Package Dimension



NOTES:

- 1. ALL DIMENSIONS IN MILLIMETERS (INCHES).
 2. TOLERANCE IS ± 0.1 mm (± 0.004 IN.) UNLESS OTHERWISE SPECIFIED.

Device Selection Guide

Package Dimension (mm)	Parts per Reel	Package Description
1.6 (L) x 1.0 (W) x 0.4 (H)	4000	Untinted, Non-diffused

CAUTION: ASMT-CR00 LEDs are Class 1A ESD sensitive per JESD22-A114C.01. Please observe appropriate precautions during handling and processing. Refer to Application Note AN-1142 for additional details.



Absolute Maximum Ratings at T_A = 25°C

Parameter	ASMT-CR00	Unit
DC Forward Current [1]	25	mA
Power Dissipation	60	mW
Reverse Voltage (I _R = 100μA)	5	V
LED Junction Temperature	95	°C
Operating Temperature Range	-40 to +85	°C
Storage Temperature Range	-40 to +85	°C
Soldering Temperature	See reflow soldering profile (Figure 6 & 7)	

Note:

Electrical Characteristics at T_A = 25°C

	Forward V	oltage	Reverse Breakdov	vn
	V _F (Volts) [1]	V _R (Volts)	Thermal Resistance
	@ I _F = 20m	ıΑ	@ $I_R = 100 \mu A$	R _{0J-PIN} (°C/W)
Part Number	Тур.	Max.	Min.	Тур.
ASMT-CR00	1.9	2.4	5	400

Notes:

1.Vf tolerance: ±0.1V

Optical Characteristics at T_A = 25°C

	Luminous Intensity lv [1] (mcd)		tensity Peak Wavelength	Dominant Wavelength	Viewing Angle 2 θ _{1/2} [3]
	@ 20mA		λ_{peak} (nm)	λ_d [2] (nm)	(Degrees)
Part Number	Min.	Тур.	Тур.	Тур.	Тур.
ASMT-CR00	28.5	90.0	637.0	626.0	150

Notes:

- 1. The luminous intensity Iv is measured at the peak of the spatial radiation pattern which may not be aligned with the mechanical axis of the LED package.
- 2. The dominant wavelength, λ_d , is derived from the CIE Chromaticity Diagram and represents the perceived color of the device.
- 3. $\theta_{1/2}$ is the off-axis angle where the luminous intensity is $\frac{1}{2}$ the peak intensity.

^{1.} Derate linearly as shown in Figure 4.

Light Intensity (I_V) Bin Limits

	Intensity (mcd)		
Bin ID	Minimum	Minimum	
N	28.50	45.00	
Р	45.00	71.50	
Q	71.50	112.50	
R	112.50	180.0	

Tolerance: ±15%

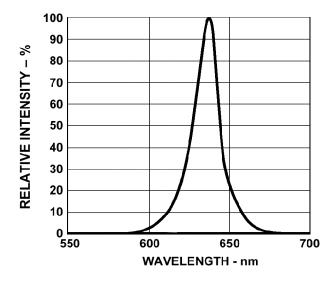
Notes:

- Bin categories are established for classification of products. Products may not be available in all categories. Please contact your Avago representative for information on current available bins.
- The lv binning specification set-up is for lowest allowable lv binning only. There is no upper lv bin limits.

Color Bin Limits

	Dominant Wavelength (nm)		
Bin ID	Minimum	Maximum	
-	620.0	635.0	

Tolerance: ±1nm



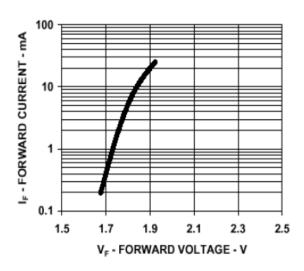


Figure 1. Relative intensity vs. wavelength

Figure 2. Forward current vs. forward voltage

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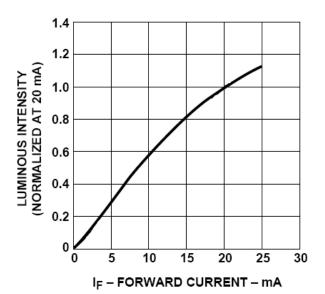


Figure 3. Luminous intensity vs. forward current

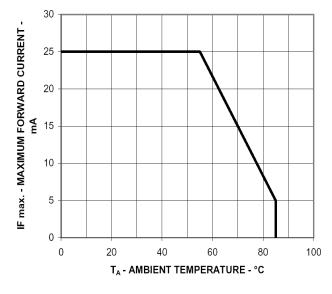


Figure 4. Maximum forward current vs. ambient temperature

EMITTING DIRECTION

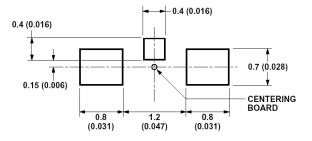


Figure 5. Recommended soldering land pattern.

Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ± 0.1 mm (± 0.004 in.) unless otherwise specified.

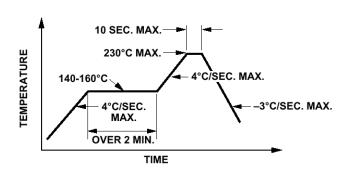
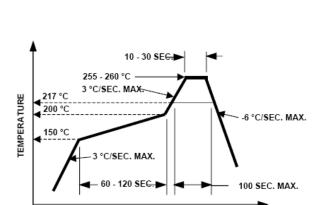


Figure 6. Recommended reflow soldering profile.



(Acc. to J-STD-020C)

Figure 7. Recommended Pb-free reflow soldering profile.

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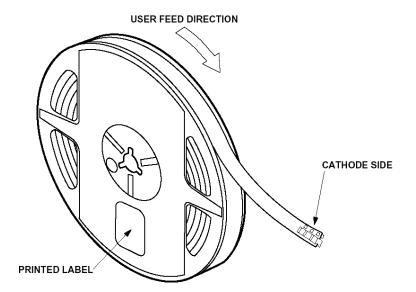


Figure 9. Reeling orientation.

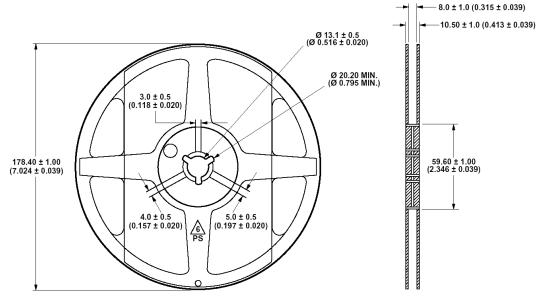


Figure 10. Reel dimensions.

Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ± 0.1 mm (± 0.004 in.) unless otherwise specified.

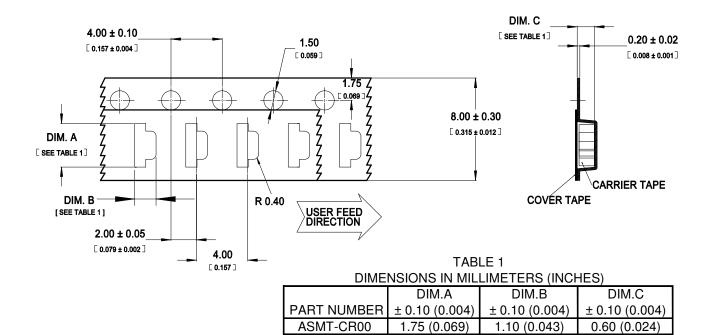


Figure 11. Tape dimensions.

Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ±0.1mm (±0.004in.) unless otherwise specified.

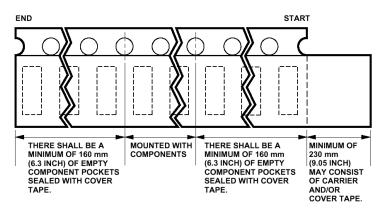


Figure 12. Tape leader and trailer dimensions.

Reflow Soldering:

For more information on reflow soldering, refer to Application Note AN-1060, *Surface Mounting SMT LED Indicator Components*.

Storage Condition:

5 to 30°C @ 60%RH max.

Baking is required before mounting, if:

- 1. Humidity Indicator Card is > 10% when read at 23 ± 5°C.
- 2. Device expose to factory conditions <30°C/60%RH more than 672 hours.

Recommended baking condition: 60±5°C for 20 hours.

Handling Precaution

Customer are advised to implement proper handling precaution as the thin package of 0.4mm will have high potential of crack epoxy during application. Stacking the PCBA is strictly prohibited.