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Stocking Distributor

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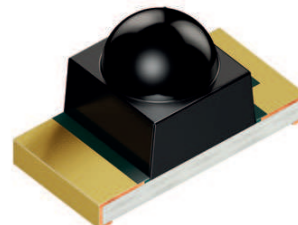
[OSRAM Opto Semiconductors, Inc.](#)
[SFH 4059-Z](#)

For any questions, you can email us directly:

sales@integrated-circuit.com

High Power Infrared Emitter (850 nm) Version 1.4

SFH 4059



Features:

- High optical power
- Very small package: (LxWxH) 3.2 mm x 1.6 mm x 1.85 mm
- UL version available (ordering code & test conditions on request)

Applications

- Miniature photointerrupters
- Industrial electronics
- For control and drive circuits
- Mobile devices

Notes

Depending on the mode of operation, these devices emit highly concentrated non visible infrared light which can be hazardous to the human eye. Products which incorporate these devices have to follow the safety precautions given in IEC 60825-1 and IEC 62471.

Ordering Information

Type:	Irradiance E_e [mW/cm ²] $I_F = 70 \text{ mA}$, $t_p = 20 \text{ ms}$	Radiant intensity $I_{e, \text{typ}}$ [mW/sr] $I_F = 70 \text{ mA}$, $t_p = 20 \text{ ms}$	Ordering Code
SFH 4059-QS	15 (≥ 6.3)	100	Q65111A2991

Note: I_e measured with a detector (11.3 mm diameter) in 100 mm distance ($\Omega = 0.01 \text{ sr}$) to the device surface
 E_e measured in the near field with a detector (7.2 mm diameter) in 20 mm distance ($\Omega = 0.1 \text{ sr}$) to the device surface

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Maximum Ratings ($T_A = 25\text{ }^{\circ}\text{C}$)

Parameter	Symbol	Values	Unit
Operation and storage temperature range	$T_{op}; T_{stg}$	-40 ... 85	$^{\circ}\text{C}$
Reverse voltage	V_R	5	V
Forward current	I_F	70	mA
Surge current ($t_p \leq 300\text{ }\mu\text{s}$, $D = 0$)	I_{FSM}	0.7	A
Power consumption	P_{tot}	140	mW
ESD withstand voltage (acc. to ANSI/ ESDA/ JEDEC JS-001 - HBM)	V_{ESD}	2	kV
Thermal resistance junction - ambient ^{1) page 13}	R_{thJA}	540	K / W
Thermal resistance junction - soldering point ^{2) page 13}	R_{thJS}	360	K / W

Characteristics ($T_A = 25\text{ }^{\circ}\text{C}$)

Parameter		Symbol	Values	Unit
Peak wavelength ($I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$)	(typ)	λ_{peak}	860	nm
Centroid wavelength ($I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$)	(typ)	$\lambda_{centroid}$	850	nm
Spectral bandwidth at 50% of I_{max} ($I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$)	(typ)	$\Delta\lambda$	30	nm
Half angle	(typ)	ϕ	± 10	$^{\circ}$
Dimensions of active chip area	(typ)	L x W	0.2 x 0.2	mm x mm
Rise and fall time of I_e (10% and 90% of $I_{e\text{ max}}$) ($I_F = 70\text{ mA}$, $R_L = 50\text{ }\Omega$)	(typ)	t_r, t_f	12	ns
Forward voltage ($I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$)	(typ (max))	V_F	1.6 (≤ 2)	V
Forward voltage ($I_F = 500\text{ mA}$, $t_p = 100\text{ }\mu\text{s}$)	(typ (max))	V_F	2.4	V
Reverse current ($V_R = 5\text{ V}$)		I_R	not designed for reverse operation	μA
Total radiant flux ($I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$)	(typ)	Φ_e	40	mW
Radiant intensity ($I_F = 70\text{ mA}$, $t_p = 20\text{ ms}$)		$I_{e, typ}$	100	mW/sr

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Parameter	Symbol	Values	Unit
Temperature coefficient of I_e or Φ_e ($I_F = 70 \text{ mA}$, $t_p = 20 \text{ ms}$)	TC_I	-0.5	% / K
Temperature coefficient of V_F ($I_F = 70 \text{ mA}$, $t_p = 20 \text{ ms}$)	TC_V	-0.7	mV / K
Temperature coefficient of wavelength ($I_F = 70 \text{ mA}$, $t_p = 20 \text{ ms}$)	TC_λ	0.3	nm / K

Grouping ($T_A = 25^\circ\text{C}$)

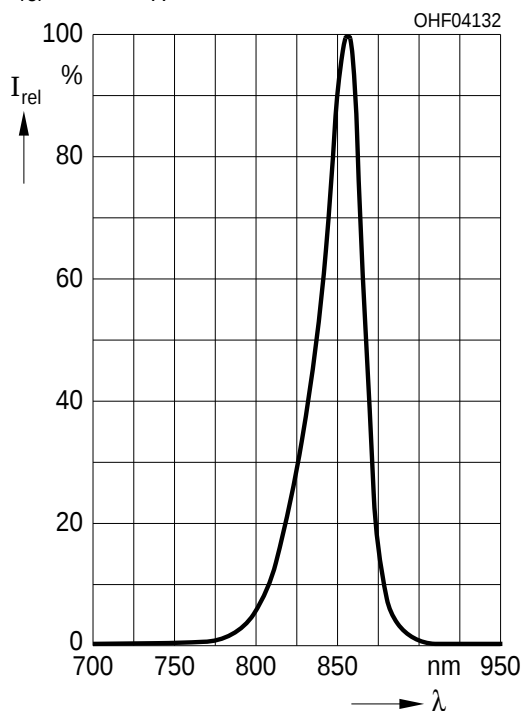
Parameter	Min Irradiance $I_F = 70 \text{ mA}$, $t_p = 20 \text{ ms}$ $E_{e, \min} [\text{mW}/\text{cm}^2]$	Max Irradiance $I_F = 70 \text{ mA}$, $t_p = 20 \text{ ms}$ $E_{e, \max} [\text{mW}/\text{cm}^2]$
SFH 4059 -Q	6.3	12.5
SFH 4059 -R	10	20
SFH 4059 -S	16	32

Note: E_e measured in the near field with a detector (7.2 mm diameter) in 20 mm distance ($\Omega = 0.1 \text{ sr}$) to the device surface

Only one group in one packing unit (variation lower 2:1).

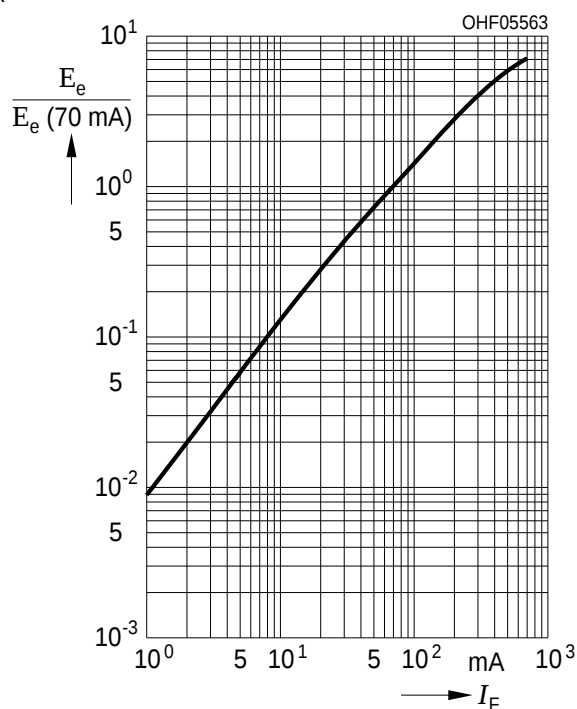
Relative Spectral Emission ^{3) page 13}

(typ) $I_{\text{rel}} = f(\lambda)$, $T_A = 25^\circ\text{C}$



Irradiance ^{3) page 13}

$E_e/E_e(70\text{mA}) = f(I_F)$, Single pulse, $t_p = 25 \mu\text{s}$, $T_A = 25^\circ\text{C}$

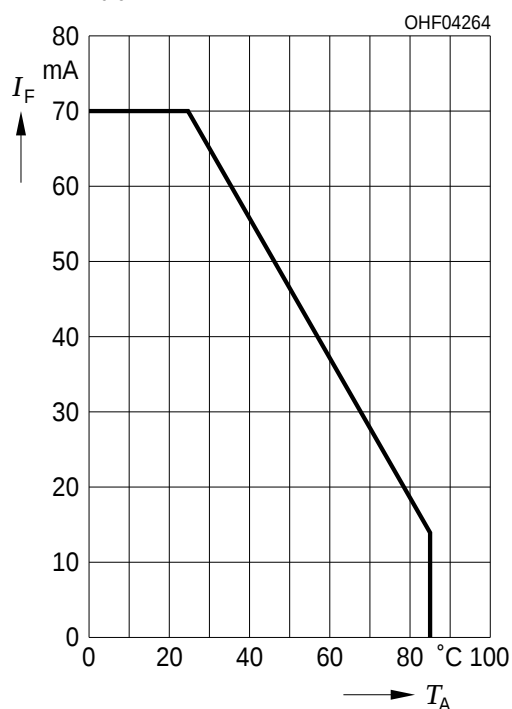


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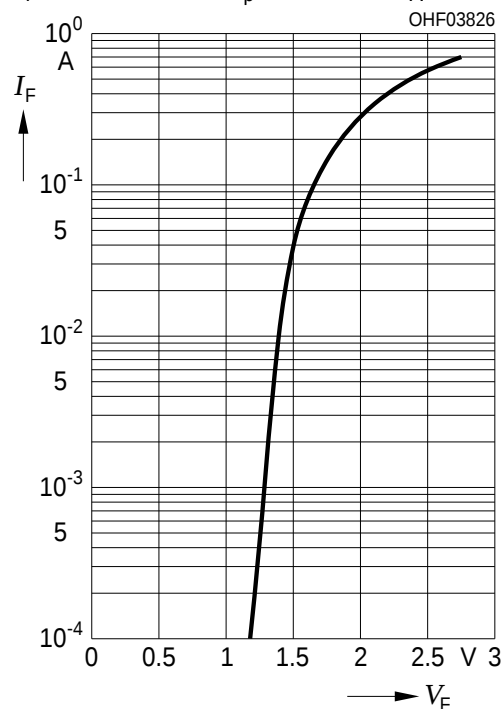
Max. Permissible Forward Current

$$I_F = f(T_A), R_{thJA} = 540K/W$$



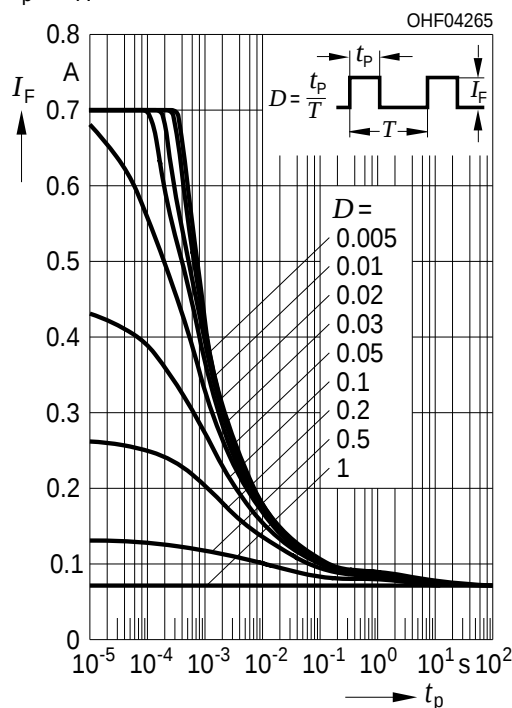
Forward Current ^{3) page 13}

$$I_F = f(V_F), \text{ single pulse, } t_p = 100 \mu s, T_A = 25^\circ C$$



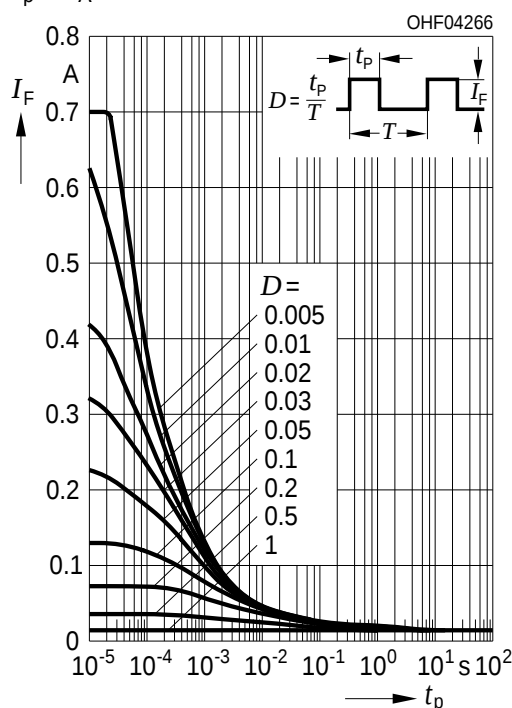
Permissible Pulse Handling Capability

$$I_F = f(t_p), T_A = 25^\circ C, \text{ duty cycle } D = \text{parameter}$$



Permissible Pulse Handling Capability

$$I_F = f(t_p), T_A = 85^\circ C, \text{ duty cycle } D = \text{parameter}$$

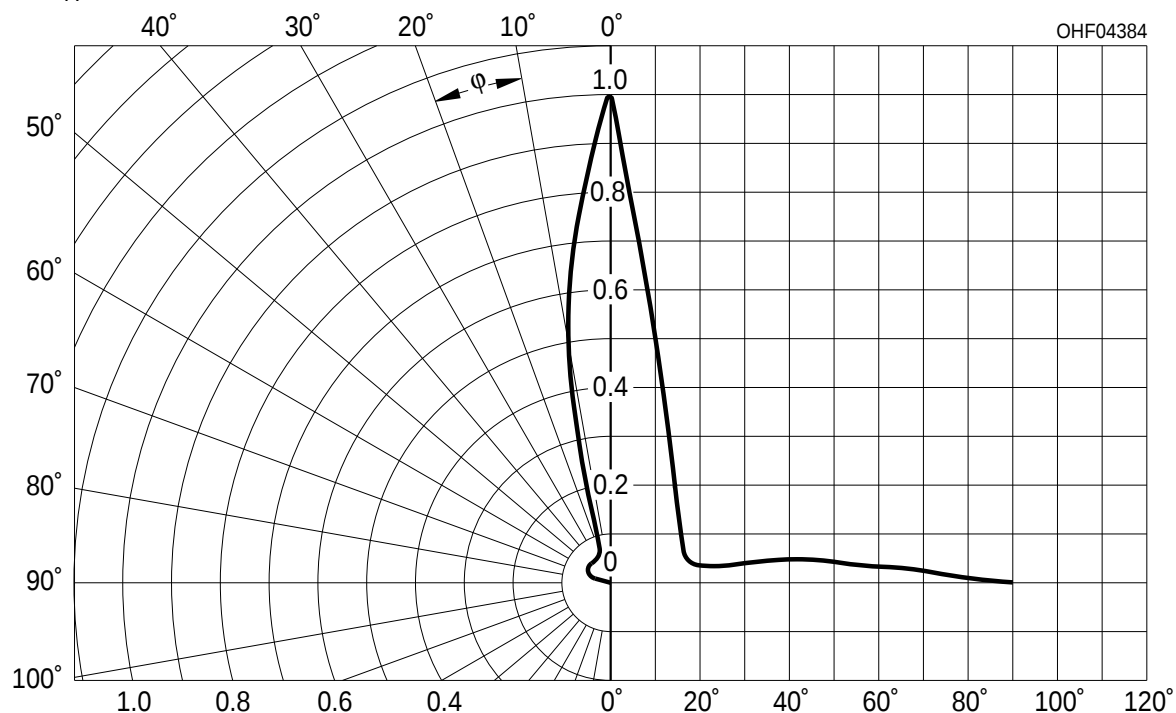


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Radiation Characteristics ^{3) page 13}

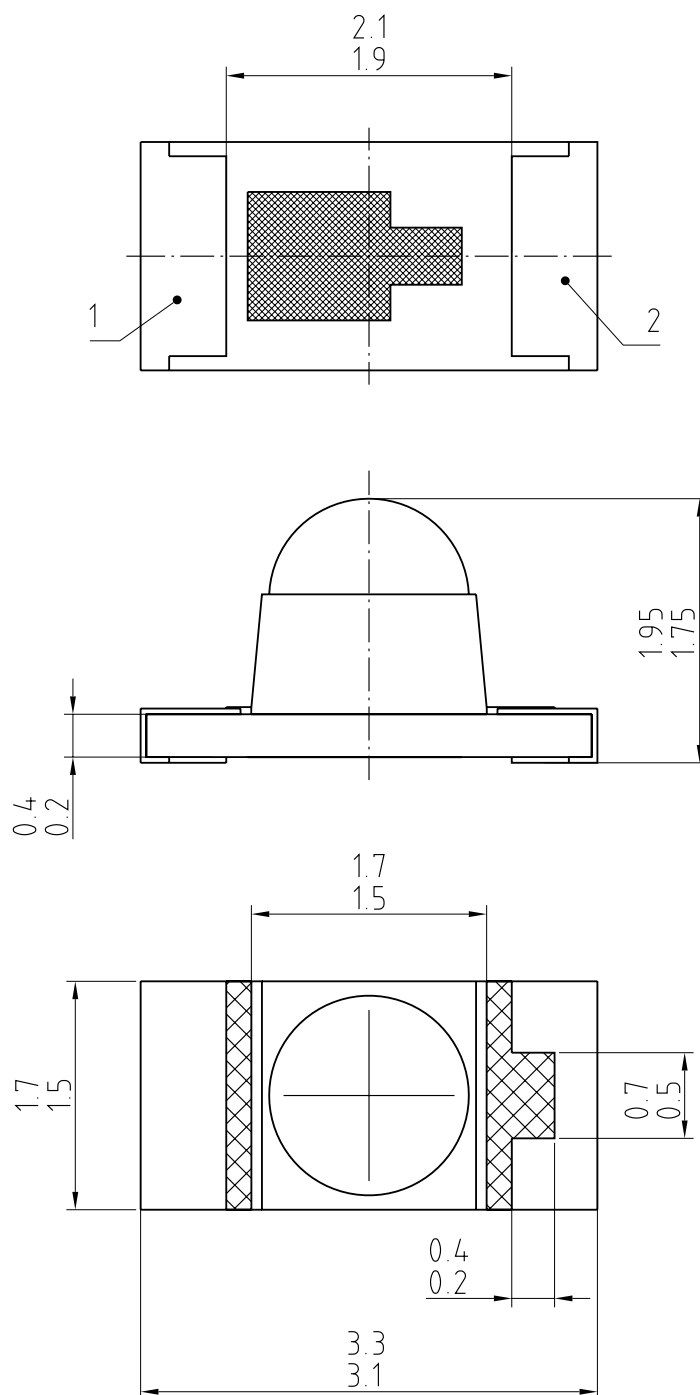
$I_{\text{rel}} = f(\phi)$, $T_A = 25^\circ\text{C}$



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Package Outline



C63062-A4074-A1...-05

Dimensions in mm.

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Pinning

Pin	Description
1	cathode
2	anode

Package

Chipped

Approximate Weight:

5.4 mg

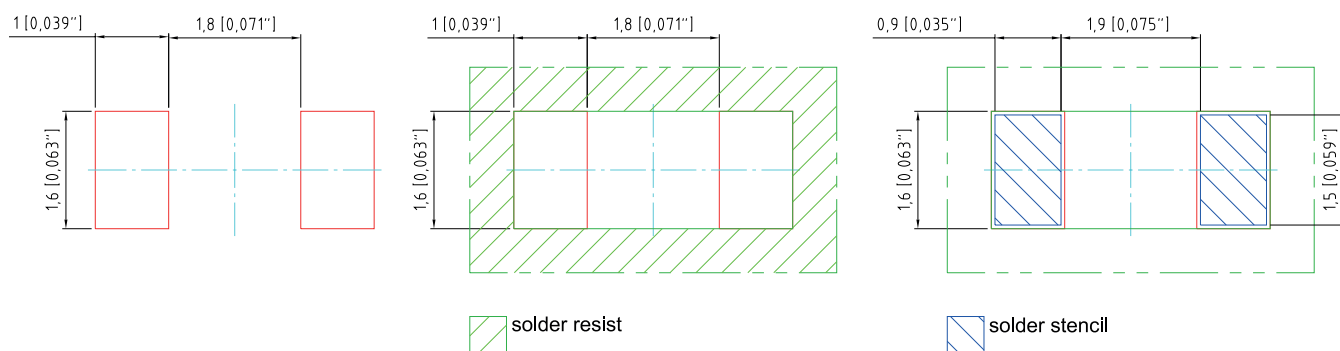
Note:

Package: Epoxy

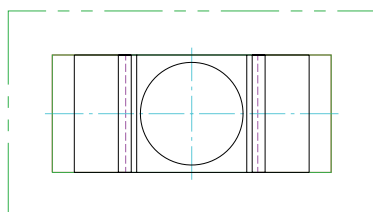
Note:

Colour: black

Recommended Solder Pad



Component Location on Pad



Dimensions in mm (inch).

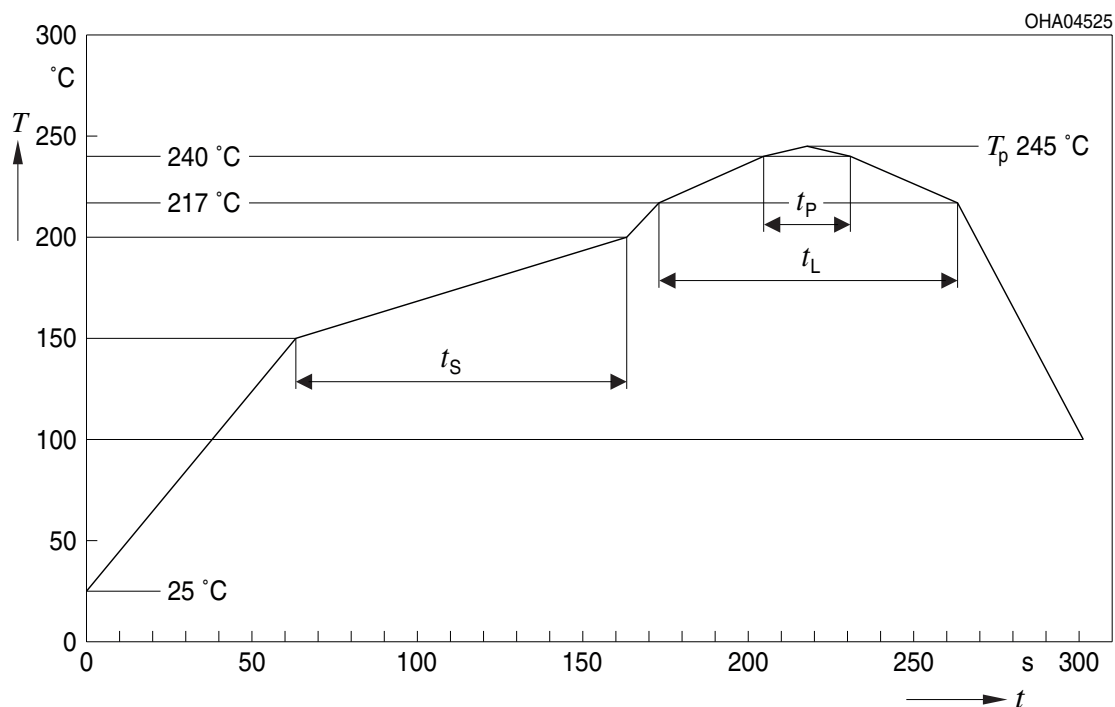
E062.3010.78-04

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Reflow Soldering Profile

Product complies to MSL Level 3 acc. to JEDEC J-STD-020D.01



OHA04612

Profile Feature Profil-Charakteristik	Symbol Symbol	Pb-Free (SnAgCu) Assembly			Unit Einheit
		Minimum	Recommendation	Maximum	
Ramp-up rate to preheat*) 25 °C to 150 °C			2	3	K/s
Time t _S T _{Smin} to T _{Smax}	t _S	60	100	120	s
Ramp-up rate to peak*) T _{Smax} to T _P			2	3	K/s
Liquidus temperature	T _L	217			°C
Time above liquidus temperature	t _L		80	100	s
Peak temperature	T _P		245	260	°C
Time within 5 °C of the specified peak temperature T _P - 5 K	t _P	10	20	30	s
Ramp-down rate* T _P to 100 °C			3	6	K/s
Time 25 °C to T _P				480	s

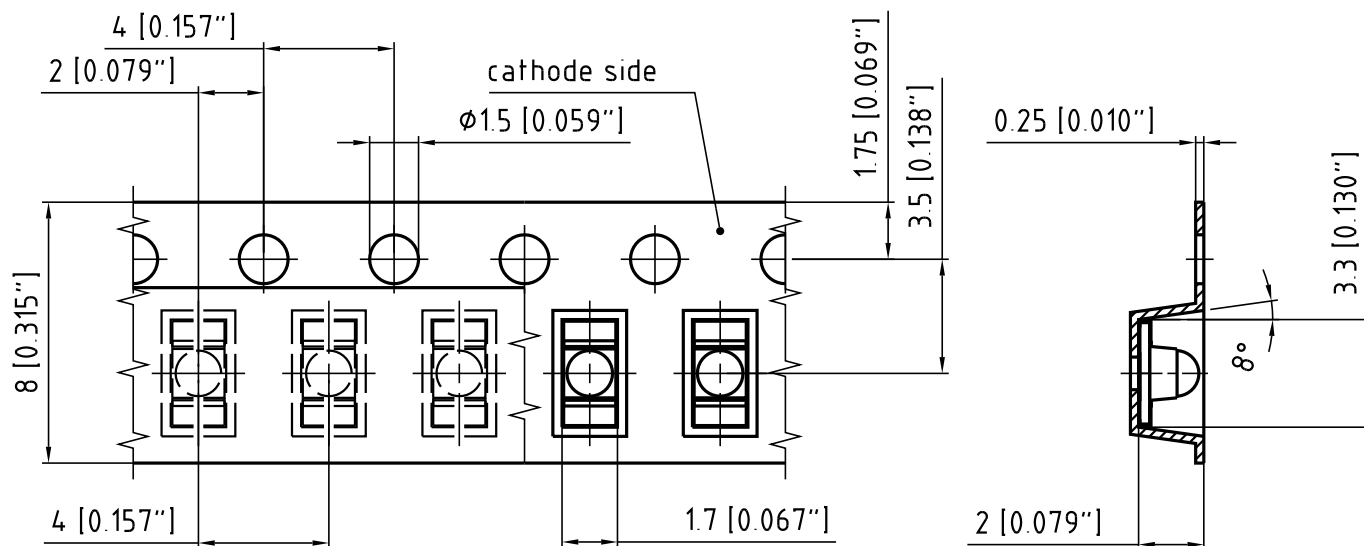
All temperatures refer to the center of the package, measured on the top of the component

* slope calculation DT/Dt: Dt max. 5 s; fulfillment for the whole T-range

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Taping

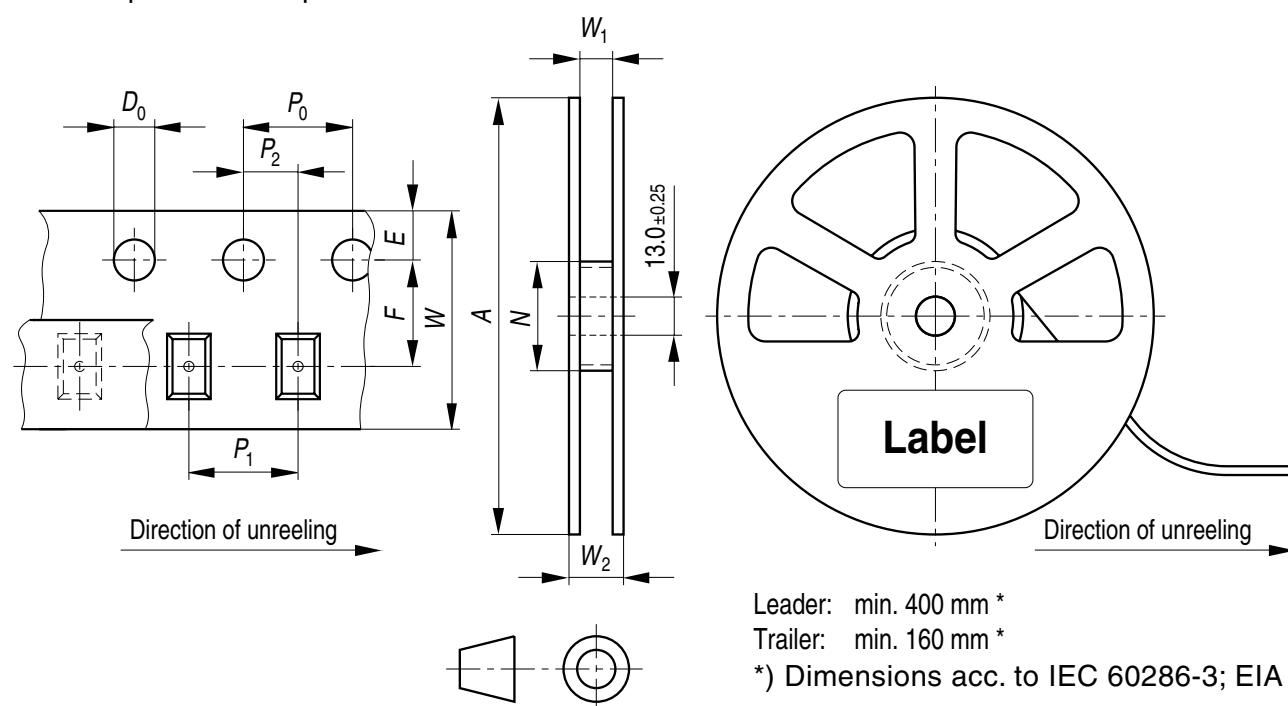


C63062-A4074-B1-01

Dimensions in mm (inch).

Tape and Reel

8 mm tape with 2000 pcs. on Ø 180 mm reel



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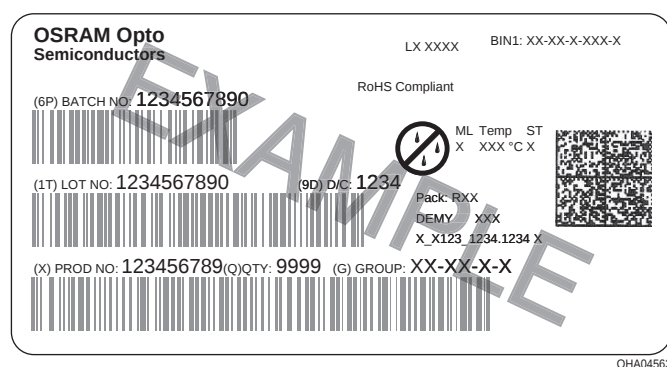
Tape dimensions [mm]

W	P ₀	P ₁	P ₂	D ₀	E	F
8 + 0.3 / -0.1	4 ± 0.1	2 ± 0.05 or 4 ± 0.1	2 ± 0.05	1.5 ± 0.1	1.75 ± 0.1	3.5 ± 0.05

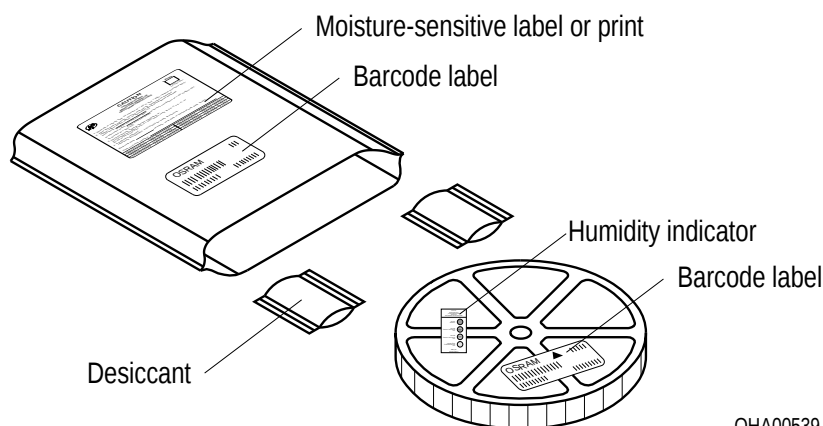
Reel dimensions [mm]

A	W	N _{min}	W ₁	W _{2max}
180	8	60	8.4 + 2	14.4

Barcode-Product-Label (BPL)



Dry Packing Process and Materials



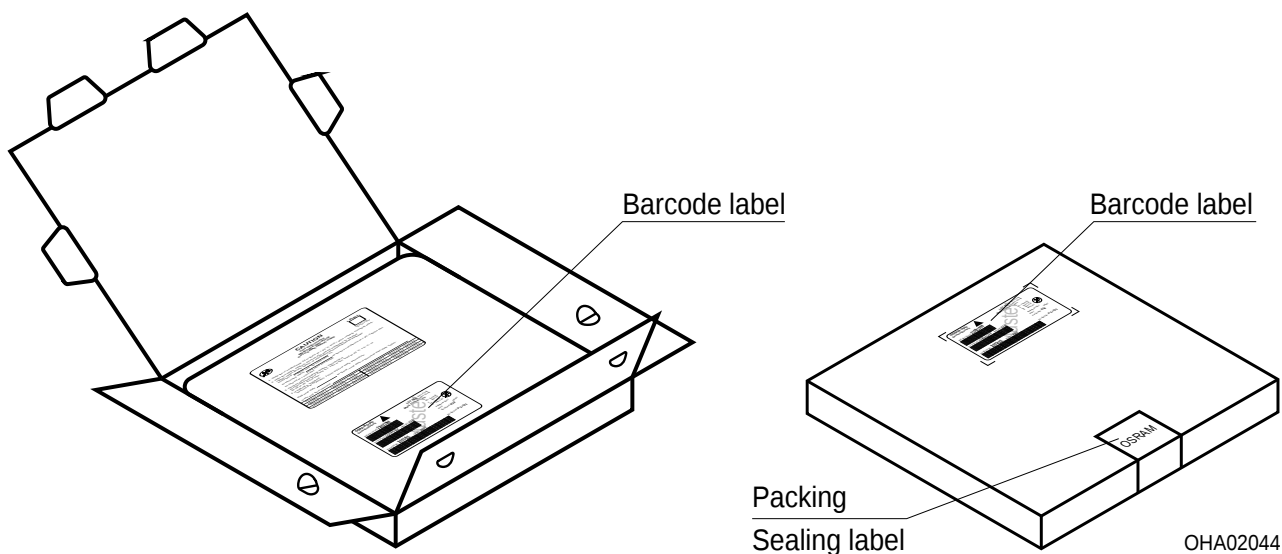
Note:

Moisture-sensitive product is packed in a dry bag containing desiccant and a humidity card. Regarding dry pack you will find further information in the internet. Here you will also find the normative references like JEDEC.

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Transportation Packing and Materials



Dimensions of transportation box in mm

Width	Length	Height
200 ± 5	195 ± 5	30 ± 5

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Disclaimer

Language english will prevail in case of any discrepancies or deviations between the two language wordings.

Attention please!

The information describes the type of component and shall not be considered as assured characteristics.

Terms of delivery and rights to change design reserved. Due to technical requirements components may contain dangerous substances.

For information on the types in question please contact our Sales Organization.

If printed or downloaded, please find the latest version in the Internet.

Packing

Please use the recycling operators known to you. We can also help you – get in touch with your nearest sales office.

By agreement we will take packing material back, if it is sorted. You must bear the costs of transport. For packing material that is returned to us unsorted or which we are not obliged to accept, we shall have to invoice you for any costs incurred.

Components used in life-support devices or systems must be expressly authorized for such purpose!

Critical components* may only be used in life-support devices** or systems with the express written approval of OSRAM OS.

*) A critical component is a component used in a life-support device or system whose failure can reasonably be expected to cause the failure of that life-support device or system, or to affect its safety or the effectiveness of that device or system.

**) Life support devices or systems are intended (a) to be implanted in the human body, or (b) to support and/or maintain and sustain human life. If they fail, it is reasonable to assume that the health and the life of the user may be endangered.

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Glossary

- ¹⁾ **Thermal resistance:** junction -ambient, mounted on PC-board (FR4), padsize 5 mm² each
- ²⁾ **Thermal resistance:** junction - soldering point, of the device only, mounted on an ideal heatsink (e.g. metal block)
- ³⁾ **Typical Values:** Due to the special conditions of the manufacturing processes of LED, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.

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