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STMicroelectronics STPS1545CGY-TR

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# **STPS1545C-Y**

# Automotive power Schottky rectifier

#### **Features**

- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Avalanche capability specified
- AEC-Q101 qualified

### **Description**

Dual center tap Schottky rectifier suited for high frequency DC to DC converters.

Packaged in D<sup>2</sup>PAK, this device is especially intended for use in low voltage, high frequency inverters, free wheeling and polarity protection applications.

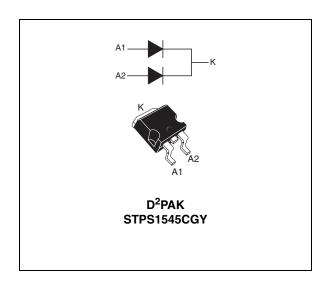


Table 1. Device summary

I <sub>F(AV)</sub>	2 x 7.5 A
V <sub>RRM</sub>	45 V
T <sub>j (max)</sub>	175 °C
V <sub>F(max)</sub>	0.57 V

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### 1 Characteristics

Table 2. Absolute Ratings (limiting values)

Symbol	Parameter			Value	Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage			45	V
I <sub>F(RMS)</sub>	RMS forward voltage			20	Α
I <sub>F(AV)</sub>	Average forward current $\delta = 0.5$	T <sub>c</sub> = 157 °C	T <sub>c</sub> = 157 °C Per diode		Α
I <sub>FSM</sub>	Surge non repetitive forward current	t <sub>p</sub> = 10 ms Sinusoidal		150	Α
I <sub>RRM</sub>	Peak repetitive reverse current	t <sub>p</sub> = 2 μs square F = 1 kHz		1	Α
I <sub>RSM</sub>	Non repetitive peak reverse current	t <sub>p</sub> = 100 μs square		2	Α
P <sub>ARM</sub>	Repetitive peak avalanche power $t_p = 1 \mu s T_j = 25 °C$		2700	W	
T <sub>stg</sub>	Storage temperature range			-65 to +175	°C
T <sub>j</sub>	Maximum operating junction temperature (1)			-40 to +175	°C
dV/dt	Critical rate of rise of reverse voltage			10000	V/µs

<sup>1.</sup>  $\frac{dPtot}{dT_i} < \frac{1}{Rth(i-a)}$  condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistances

Symbol	Parameter	Value	Unit	
R <sub>th(j-c)</sub>	Llunction to case	Per diode Total	3.0 1.7	°C/W
R <sub>th(c)</sub>	Coupling	0.35		

When the diodes 1 and 2 are used simultaneously :  $\Delta$ Tj(diode 1) = P(diode1) x R<sub>th(j-c)</sub>(Per diode) + P(diode 2) x R<sub>th(c)</sub>

Table 4. Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
ı (1)	I <sub>R</sub> <sup>(1)</sup> Reverse leakage current	T <sub>j</sub> = 25 °C	V <sub>R</sub> =V <sub>RRM</sub>	-	-	100	μΑ
'R`		T <sub>j</sub> = 125 °C		-	5	15	mA
V <sub>F</sub> <sup>(1)</sup> Forward		T <sub>j</sub> = 125 °C	I <sub>F</sub> = 7.5A	-	0.5	0.57	
	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 15 A	-	-	0.84	V
		T <sub>j</sub> = 125 °C	I <sub>F</sub> = 15 A	-	0.65	0.72	

<sup>1.</sup> Pulse test: tp = 380  $\mu$ s,  $\delta$  < 2%

To evaluate the conduction losses use the following equation:

$$P = 0.42 \times I_{F(AV)} + 0.020 I_{F}^{2}_{(RMS)}$$

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Figure 1. Average forward power dissipation Figure 2. versus average forward current (per diode)

Average forward current versus ambient temperature ( $\delta$  = 0.5, per diode)

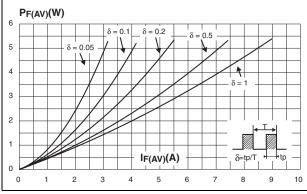
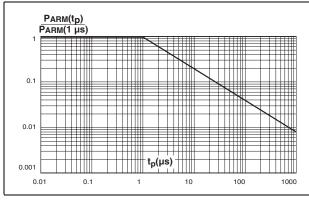


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature



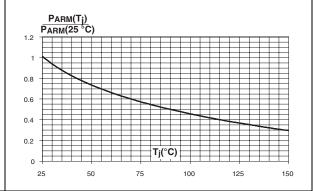
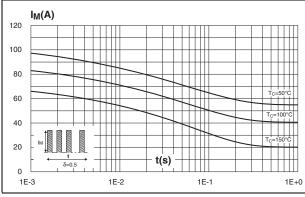
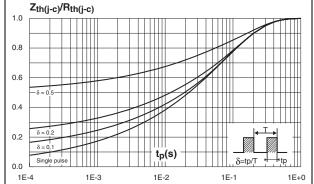


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

Figure 6. Relative variation of thermal impedance junction to case versus pulse duration





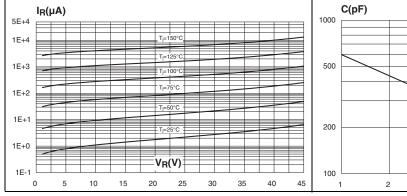
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Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)

Figure 8. Junction capacitance versus reverse voltage applied (typical values, per diode)



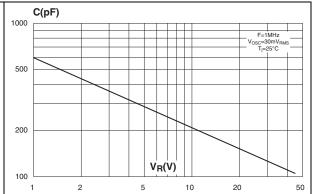
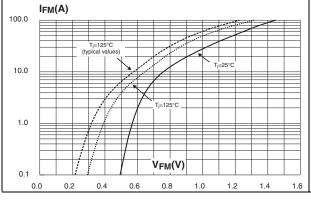
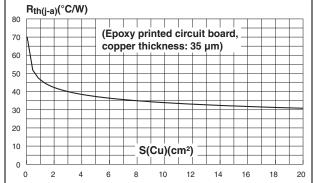


Figure 9. Forward voltage drop versus forward current (maximum values, per diode)

Figure 10. Thermal resistance junction to ambient versus copper surface under tab





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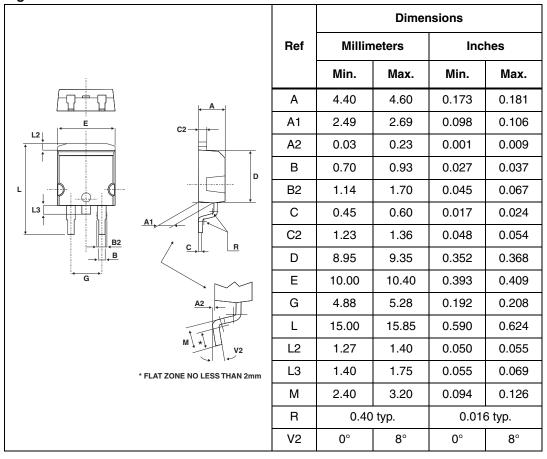
STPS1545C-Y Package Information

### 2 Package Information

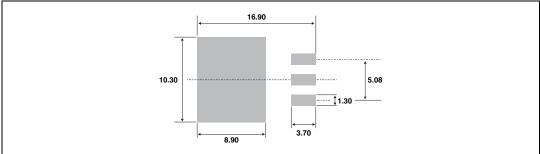
- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Figure 11. D<sup>2</sup>PAK dimensions











**Ordering information** 

STPS1545C-Y

# 3 Ordering information

Table 5. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS1545CGY-TR	STPS1545CGY	D <sup>2</sup> PAK	1.48 g	1000	Tape and reel

# 4 Revision history

Table 6. Document revision history

Date	Revision	Changes
23-May-2011	1	Initial release.

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# Distributor of STMicroelectronics: Excellent Integrated System Limited Datasheet of STPS1545CGY-TR - DIODE ARRAY SCHOTTKY 45V D2PAK

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#### STPS1545C-Y

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