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STMicroelectronics STT13005FP

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## STT13005FP

## High voltage fast-switching NPN power transistor

#### **Features**

- High voltage capability
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed

### **Applications**

- Electronic ballast for fluorescent lighting
- Flyback and forward single transistor low power converters



The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and medium voltage capability.

It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.

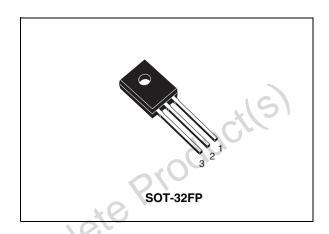


Figure 1. Internal schematic diagram

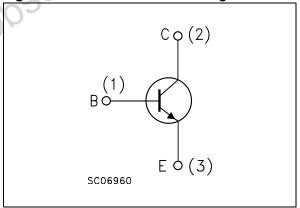


Table 1. Device summary

Order code	Marking	Package	Packaging
STT13005FP	T13005FP	SOT-32FP	Bag

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Electrical ratings STT13005FP

## 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-emitter voltage (V <sub>BE</sub> = 0)	700	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	400	V
V <sub>EBO</sub>	Emitter-base voltage $(I_C = 0)$	9	٧
I <sub>C</sub>	Collector current	2	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	4	Α
I <sub>B</sub>	Base current	1 ,(9	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5 ms)	2	Α
P <sub>tot</sub>	Total dissipation at $T_c = 25$ °C	30	W
T <sub>stg</sub>	Storage temperature	-65 to 150	°C
T <sub>J</sub>	Max. operating junction temperature	150	°C

Table 3. Thermal data

Unit	Value	Parameter	Symbol
°C/W	4.2	Thermal resistance junction-case max	R <sub>thJC</sub>
		ducile	ie Pro
		ducilsi	ie Pro



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**Electrical characteristics** 

#### **Electrical characteristics** 2

 $T_{case} = 25$  °C unless otherwise specified.

**Electrical characteristics** Table 4.

Symbol	Parameter	Test condition	ns	Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector cut-off current (V <sub>BE</sub> = 0)	V <sub>CE</sub> = 700 V V <sub>CE</sub> = 700 V T <sub>C</sub> = 7	125 °C			100 500	μ <b>Α</b> μ <b>Α</b>
I <sub>CEO</sub>	Collector cut-off current (I <sub>B</sub> = 0)	V <sub>CE</sub> = 400 V				250	μA
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)	I <sub>E</sub> = 10 mA		9		116	V
V <sub>CEO(sus)</sub> (1)	Collector-emitter sustaining voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA		400	90,	)	٧
(4)	Collector-emitter	~ _	125 mA			0.5	V
V <sub>CE(sat)</sub> (1)	saturation voltage		0.2 A 0.4 A			1 1.5	V V
	Base-emitter saturation	I <sub>C</sub> = 0.5 A I <sub>B</sub> =	125 mA			1	V
V <sub>BE(sat)</sub> (1)	voltage	9	0.2 A			1.3	V
			0.4 A			1.5	V
h <sub>FE</sub> <sup>(1)</sup>	DC current gain	~ ~ ~	= 5 V = 5 V	10 8		50	
	Resistive load						
t <sub>r</sub>	Rise time	I <sub>C</sub> = 1 A V <sub>CC</sub>	= 125 V		0.4	0.7	μs
t <sub>s</sub>	Storage time	$I_{B1} = -I_{B2} = 0.2 \text{ A}$			3.2	4.5	μs
t <sub>f</sub>	Fall time				0.25	0.4	μs
01	Inductive load	I <sub>C</sub> = 1 A I <sub>B1</sub>	<sub>1</sub> = 0.2 A				
t <sub>s</sub>	Storage time	-	= 50 mH		0.8		μs
	Fall time	$V_{Clamp} = 300 \text{ V}$			0.16		μs

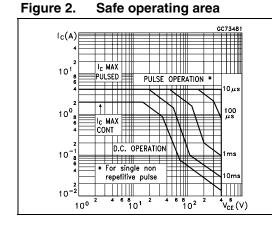


**Electrical characteristics** 

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## 2.1 Electrical characteristics (curves)

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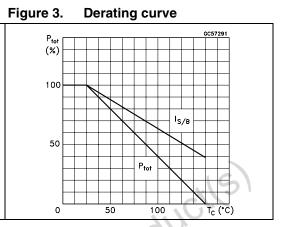
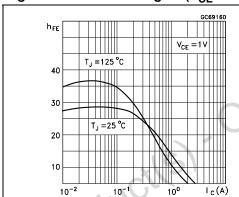


Figure 4. DC current gain ( $V_{CE} = 1 \text{ V}$ ) Figure 5. DC current gain ( $V_{CE} = 5 \text{ V}$ )



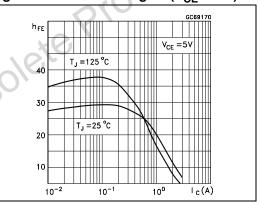
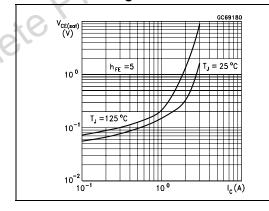
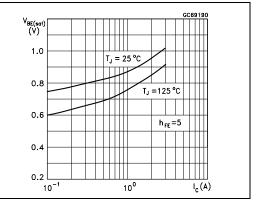


Figure 6. Collector-emitter saturation voltage

Figure 7. Base-emitter saturation voltage





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**Electrical characteristics** 

Figure 8. Inductive load fall time

 $t_f(ns)$  $V_{Clamp} = 300 V$   $h_{FE} = 5$  $R_{BB} = 0 \Omega$  $V_{BE(off)} = -5V$  $T_J = 125$  °C 300 30 0.5 1.5 I<sub>C</sub>(A)

Figure 9. Inductive load storage time

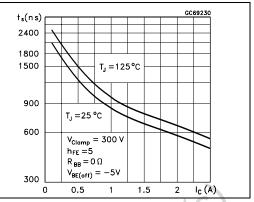
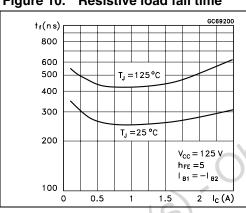


Figure 10. Resistive load fall time



Resistive load storage time Figure 11.

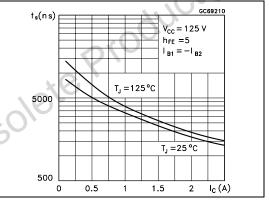
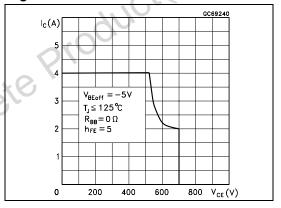


Figure 12. **Reverse biased SOA** 



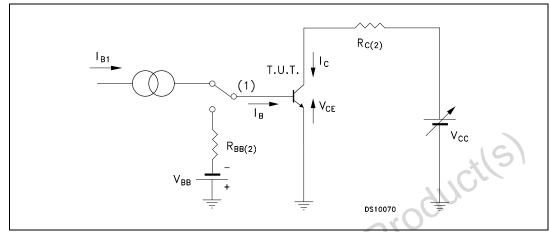


**Electrical characteristics** 

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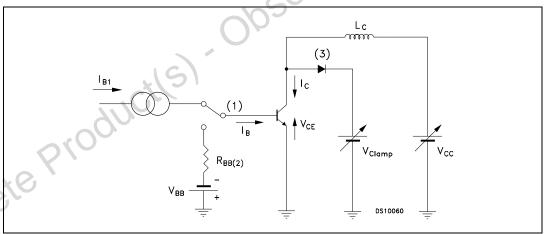
### 2.2 Test circuits

Figure 13. Resistive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor

Figure 14. Inductive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor
- 3. Fast recovery rectifier



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STT13005FP

Package mechanical data

## 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.



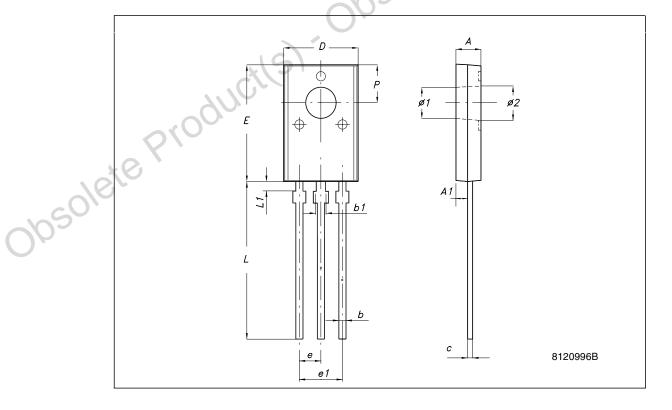


#### Package mechanical data

STT13005FP

SOT-32FP	mechan	nical	data

DIM.		mm.	
DIW.	MIN.	TYP	MAX.
Α	3.00		3.40
A1	1.80		2.20
b	0.66		0.86
b1	1.17		1.37
С	0.45		0.60
D	7.80		8.20
E	10.80		11.20
е		2.28	0
e1	4.46		4.66
L	15.30		15.70
L1	1.30	V	1.50
Р	4.04	20,	4.24
ø1	2.90	101	3.10
ø2	3.10	20/2	3.30







STT13005FP Revision history

# 4 Revision history

Table 5. Document revision history

Date	Revision	Changes
06-May-2009	1	Initial release
10-Sep-2009	2	Document status promoted from preliminary data to datasheet





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