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sales@integrated-circuit.com

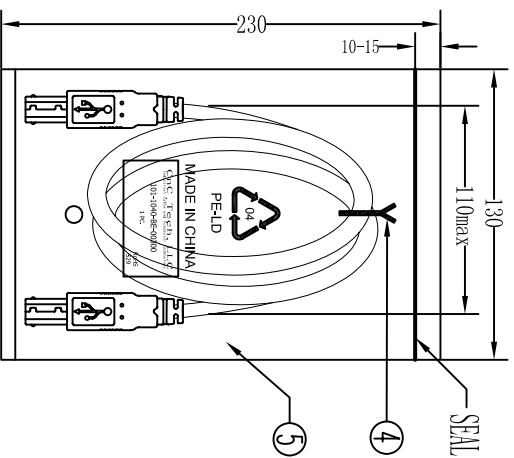


2000±50mm

CABLE BLACK PRINT
USB CABLE E189529 **PU** AWM 2725 28AWG/1PR AND
24AWG/2C 80°C 30V VW-1

Diagram illustrating a quantum circuit with two qubits, P1 and P2. The circuit consists of several gates and a final measurement stage labeled SHELL.

- Qubit P1:**
 - Gate 1: A single-qubit gate.
 - Gate 2: A CNOT gate with control on P1 and target on P2.
 - Gate 3: A CNOT gate with control on P2 and target on P1.
 - Gate 4: A single-qubit gate.
- Qubit P2:**
 - Gate 1: A single-qubit gate.
 - Gate 2: A single-qubit gate.
 - Gate 3: A single-qubit gate.
 - Gate 4: A single-qubit gate.
- Measurement:** The outputs of both qubits are measured, labeled SHELL.



IPC/PE BAG

1. 100% short, open, miswire test
2. Conduct resistance: $2\ \Omega$ (max)
3. Insulation resistance: $5\ \text{M}\Omega$ (min)
4. dielectric test: DC 300V

⑥	PE BAG	1/10	CLEAR SIZE:280*330*0.05mm PRINT BLACK"MADE IN CHINA"+ "10PCS".
⑤	PE BAG	1	CLEAR SIZE:130*230*0.05mm PRINT BLACK"MADE IN CHINA"+ "1PC".
④	TIE	1	WHITE L=100mm
③	OUT JACKET	12g	BEIGE(V-29#) PVC-40P (MOULD NO:WM-65)
②	CABLE	1	BEIGE(V-29#) USB2.0 UL2725 28#/1P+24#/2C+AEB OD:4.7mm L=2000±10MM
①	CONN	2	USB2.0 B MALE NICKEL PIN G/F 3PCS TYPE INSULATOR COLOR:WHITE
NO.	NAME	Q'TY	SPECIFICATION

CnC Tech, LLC			
Industrial Cable and Connector Technology			
DRAWN	ZHONGHS	DRAWING NO	
CHECKED		CUSTOMER	
APPROVAL		ITEM NO.	101-1040-BE-0036
DECIDED		DATE	2012-02-10
VERSIONS/EDITION	A/0	PROJECTION	