

Excellent Integrated System Limited

Stocking Distributor

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<u>Vishay Semiconductor/Diodes Division</u> 1N4148WS-E3-08

For any questions, you can email us directly: sales@integrated-circuit.com

Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of 1N4148WS-E3-08 - DIODE GEN PURP 75V 150MA SOD323

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1N4148WS

Vishay Semiconductors

Small Signal Fast Switching Diode



FEATURES

· Silicon epitaxial planar diode



- Fast switching diodes
- AEC-Q101 qualified
- Base P/N-E3 RoHS-compliant, commercial grade



- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912

MARKING (example only)



22610

Bar = cathode marking XY = type code

MECHANICAL DATA

Case: SOD-323

Weight: approx. 4.3 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	INTERNAL CONSTRUCTION	TYPE MARKING	REMARKS	
1N4148WS	1N4148WS-E3-08 or 1N4148WS-E3-18	Single diode	A2	Tape and reel	
	1N4148WS-HE3-08 or 1N4148WS-HE3-18	Sirigle diode	AZ		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	75	V	
Repetitive peak reverse voltage		V_{RRM}	100		
Average rectified current half wave rectification with resistive load (1)	f ≥ 50 Hz	I _{F(AV)}	150	mA	
Surge forward current	t < 1 s and T _j = 25 °C	I _{FSM}	350]	
Power dissipation (1)		P _{tot}	200	mW	

Note

⁽¹⁾ Valid provided that electrodes are kept at ambient temperature.

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R _{thJA}	650	K/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	- 65 to + 150	°C	
Operating temperature range		T _{op}	- 55 to + 150	°C	

Note

(1) Valid provided that electrodes are kept at ambient temperature





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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Converse voltage	I _F = 10 mA	V _F			1	V
Forward voltage	I _F = 100 mA	V _F			1.2	V
	V _R = 20 V	I _R			25	nA
Laglaga aumant	V _R = 75 V	I _R			5	μΑ
Leakage current	V _R = 100 V	I _R			100	
	$V_R = 20 \text{ V}, T_j = 150 ^{\circ}\text{C}$	I _R			50	
Diode capacitance	$V_F = V_R = 0 V$	C _D			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \mu s$, rise time < 30 ns, $f_p = (5 \text{ to } 100) \text{ kHz}$	V_{fr}			2.5	V
Reverse recovery time	I_F = 10 mA, i_R = 1 mA, V_R = 6 V, R_L = 100 Ω	t _{rr}			4	ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

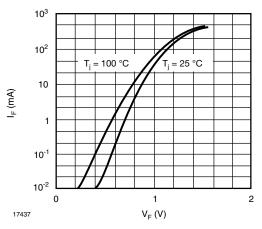


Fig. 1 - Forward Characteristics

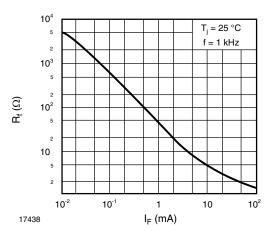


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

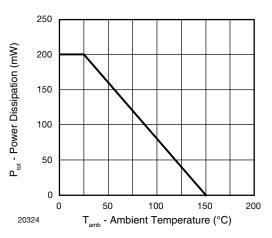


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

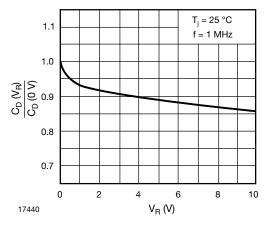
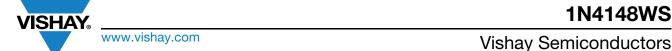


Fig. 4 - Relative Capacitance vs. Reverse Voltage

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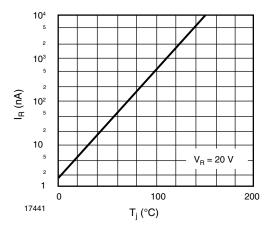


Fig. 5 - Leakage Current vs. Junction Temperature

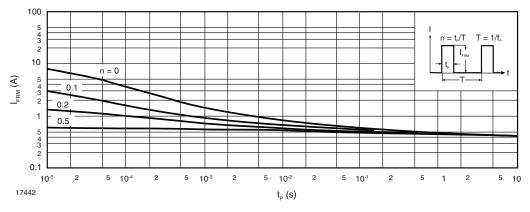


Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

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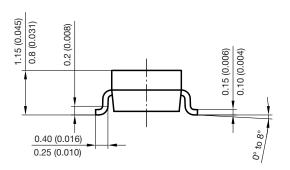


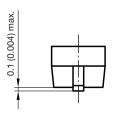
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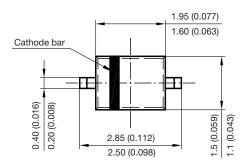
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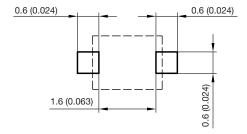
PACKAGE DIMENSIONS in millimeters (inches): SOD-323







Foot print recommendation:



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