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Fairchild Semiconductor FGA40N65SMD

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Distributor of Fairchild Semiconductor: Excellent Integrated System Limited Datasheet of FGA40N65SMD - IGBT 650V 80A 349W TO3P Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

General Description

losses are essential.



FGA40N65SMD 650 V, 40 A Field Stop IGBT

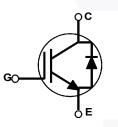
Features

- Maximum Junction Temperature : T_J = 175°C
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)}$ = 1.9 V(Typ.) @ I_C = 40 A
- Fast Switching : E_{OFF} = 6.5 uJ/A
- Tighten Parameter Distribution
- RoHS Compliant

Applications

- · Solar Inverter, UPS, Welder, PFC, Induction Heating
- Telecom, ESS





Using novel field stop IGBT technology, Fairchild's new series of field stop 2nd generation IGBTs offer the optimum performance

for solar inverter, UPS, welder, induction heating, telecom, ESS

and PFC applications where low conduction and switching

Absolute Maximum Ratings

Symbol	Description		Ratings	Unit
V _{CES}	Collector to Emitter Voltage		650	V
V _{GES}	Gate to Emitter Voltage		± 20	V
GES	Transient Gate to Emitter Voltage		± 30	V
Ic	Collector Current	@ T _C = 25°C	80	А
·C	Collector Current	@ T _C = 100°C	40	А
I _{CM (1)}	Pulsed Collector Current		120	А
I _F	Diode Forward Current	@ T _C = 25°C	40	А
	Diode Forward Current	@ T _C = 100°C	20	А
I _{FM (1)}	Pulsed Diode Maximum Forward Curren	it	120	А
P _D	Maximum Power Dissipation	@ T _C = 25°C	349	W
. D	Maximum Power Dissipation	@ T _C = 100°C	174	W
TJ	Operating Junction Temperature		-55 to +175	°C
T _{stg}	Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Notes:

1: Repetitive rating: Pulse width limited by max. junction temperature



Symbol Paramete			r Typ.			Max.		Unit			
R _{0JC} (IGBT) Thermal Resistance, Junction to Ca			ise		-			0.43		°C/W	
$ \begin{array}{c c} \hline & & \\ \hline \\ \hline$			ise			-		1.5 40		°C/W °C/W	
			nbier								
Packag	e Mar	king and Orde	rina In	foi	mation						
Part Nu		Top Mark	Packa		Packing Metho	bd	Reel	Size	Tape V	Nidth	Quantity
FGA40N65SMD FGA40N65SMD		TO-3PN		Tube		N/A		N/A		30	
Electric	al Ch	aracteristics o	f the IC	GB	$T_{c} = 25^{\circ}C$ unless other	rwise r	noted				
Symbol		Parameter			Test Conditio			Min.	Тур.	Max.	Unit
Off Charac	teristics										
BV _{CES}		or to Emitter Breakdow	n Voltage	VG	₌ = 0 V, I _C = 250 μA			650	-	-	V
ΔBV_{CES} ΔT_J		ature Coefficient of Br		-	$V_{GE} = 0 V, I_C = 250 \mu A$			-	0.6	-	V/ºC
I _{CES}		or Cut-Off Current		V _{CI}	= V _{CES} , V _{GE} = 0 V			-	-	250	μA
I _{GES}	G-E Leakage Current		$V_{GE} = V_{GES}, V_{CE} = 0 V$			-	-	±400	nA		
On Charac	teristics										
V _{GE(th)}	1	reshold Voltage	-	I _C =	= 250 μΑ, V _{CE} = V _{GE}			3.5	4.5	6.0	V
. ,	Collector to Emitter Saturation Voltage		I _C = 40 A, V _{GE} = 15 V			-	1.9	2.5	V		
V _{CE(sat)}			$I_{\rm C}$ = 40 A, V _{GE} = 15 V, T _C = 175°C			-	2.1	-	v		
Dynamic C	haracte	ristics									
C _{ies}	1	apacitance						-	1880	-	pF
C _{oes}	Output	Capacitance		V _{CE} = 30 V, V _{GE} = 0 V, f = 1 MHz			-	180	-	pF	
C _{res}	Revers	e Transfer Capacitance	e				-	50	-	pF	
Switching	Charact	eristics									
t _{d(on)}	1	n Delay Time						-	12	16	ns
t _r	Rise Ti	me		V _{CC} = 400 V, I _C = 40 A, R _G = 6 Ω, V _{GE} = 15 V, Inductive Load, T _C = 25°C			-	20	28	ns	
t _{d(off)}	Turn-O	ff Delay Time					-	92	120	ns	
t _f	Fall Tim	ne						-	13	17	ns
Eon	Turn-O	n Switching Loss				°C		-	0.82	1.23	mJ
E _{off}	Turn-O	ff Switching Loss						-	0.26	0.34	mJ
E _{ts}	Total Sv	witching Loss						-	1.08	1.57	mJ
t _{d(on)}	Turn-O	n Delay Time						-	15	-	ns
t _r	Rise Ti	me						-	22	-	ns
t _{d(off)}	Turn-O	ff Delay Time		Vc	_C = 400 V, I _C = 40 A,			-	116	-	ns
t _f	Fall Tim	ne		$R_G = 6 \Omega$, $V_{GE} = 15 V$, Inductive Load, $T_C = 175^{\circ}C$				-	16	-	ns
E _{on}	Turn-O	n Switching Loss				5°C		-	1.08	-	mJ
E _{off}	Turn-O	ff Switching Loss						-	0.60	-	mJ
E _{ts}	Total S	witching Loss						-	1.68	-	mJ



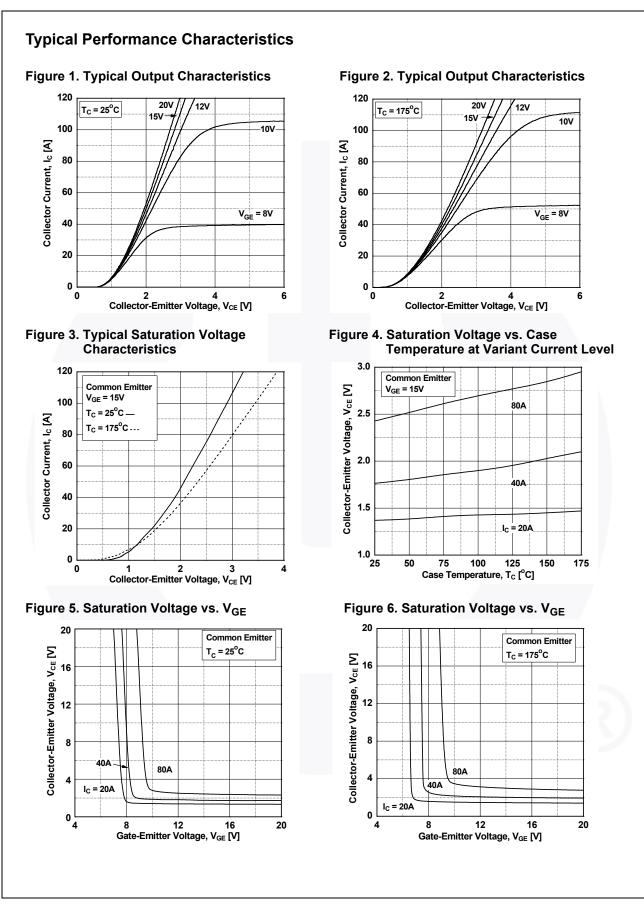
Electrical Characteristics of the IGBT (Continued)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Unit
Qg	Total Gate Charge		-	119	180	nC
Q _{ge}	Gate to Emitter Charge	V _{CE} = 400 V, I _C = 40 A, V _{GE} = 15 V	-	13	20	nC
Q _{gc}	Gate to Collector Charge	VGE - 13 V	-	58	90	nC

Electrical Characteristics of the Diode $T_C = 25^{\circ}C$ unless otherwise noted

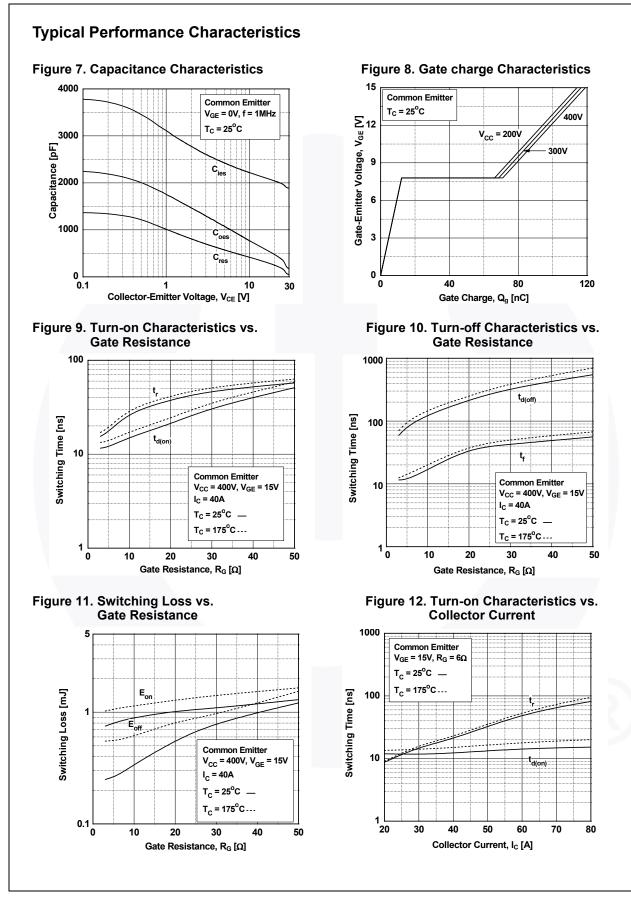
Symbol	Parameter	Test Condi	Test Conditions			Max	Unit	
V _{FM} Diode Forward Voltage	inde Forward Voltage	I _F = 20 A	T _C = 25°C	-	2.1	2.6	V	
	blode i orward voltage		T _C = 175 ^o C	-	1.7	-		
E _{rec}	Reverse Recovery Energy		T _C = 175 ^o C	-	96	-	uJ	
t _{rr}	Diode Reverse Recovery Time	I _F =20 A, di _F /dt = 200 A/μs	T _C = 25°C	-	42	-	ns	
			T _C = 175 ^o C	-	200	-		
1	Diode Peak Reverse Recovery Current		T _C = 25 ^o C	-	3.6	-	А	
'n			T _C = 175 ^o C	-	8.0	-		
Q _{rr} Diode	Diode Reverse Recovery Charge		T _C = 25°C	-	76	-	nC	
αn			T _C = 175 ^o C	-	800	-	110	





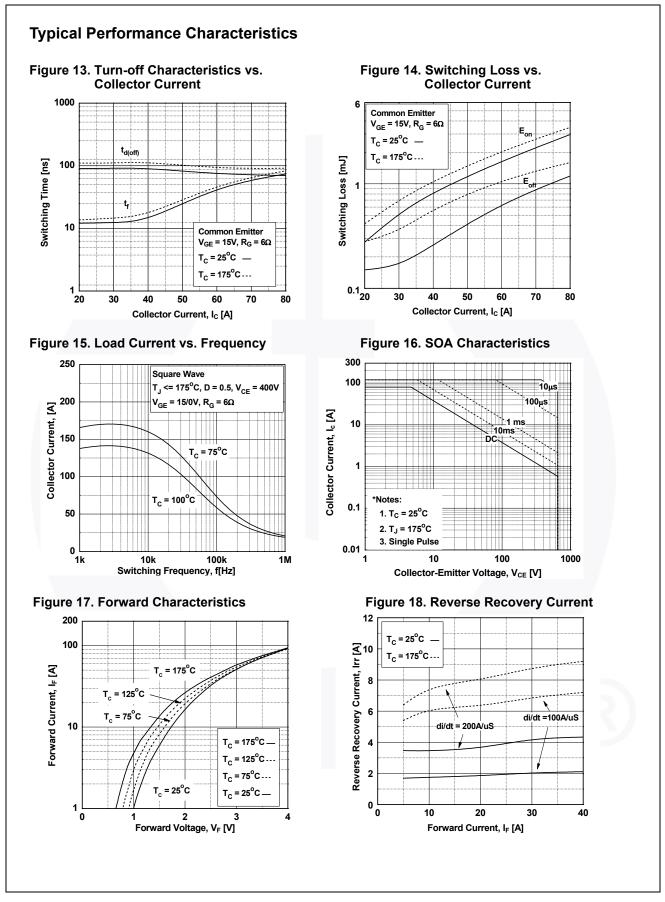
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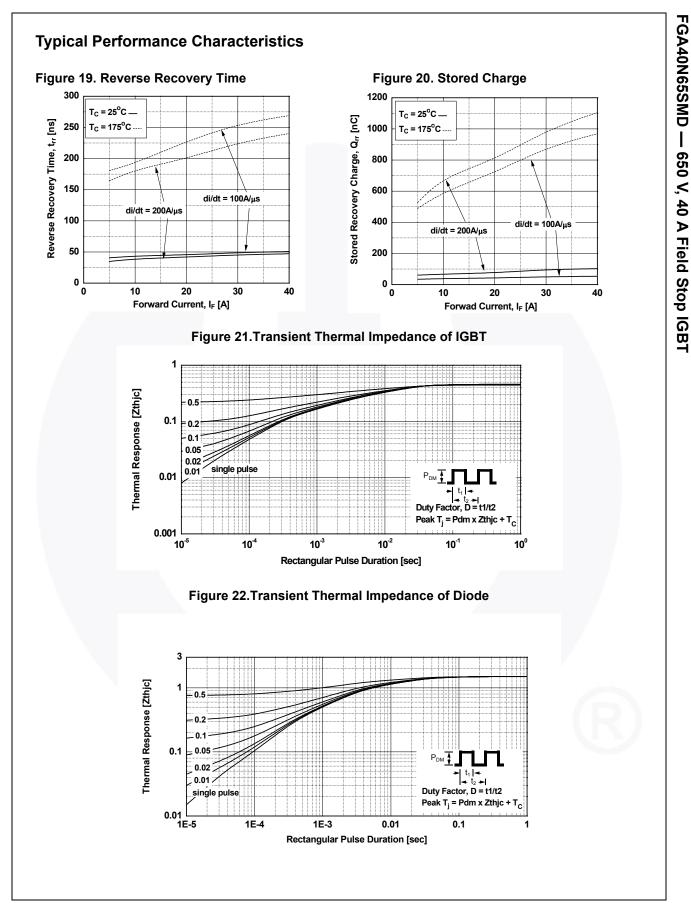




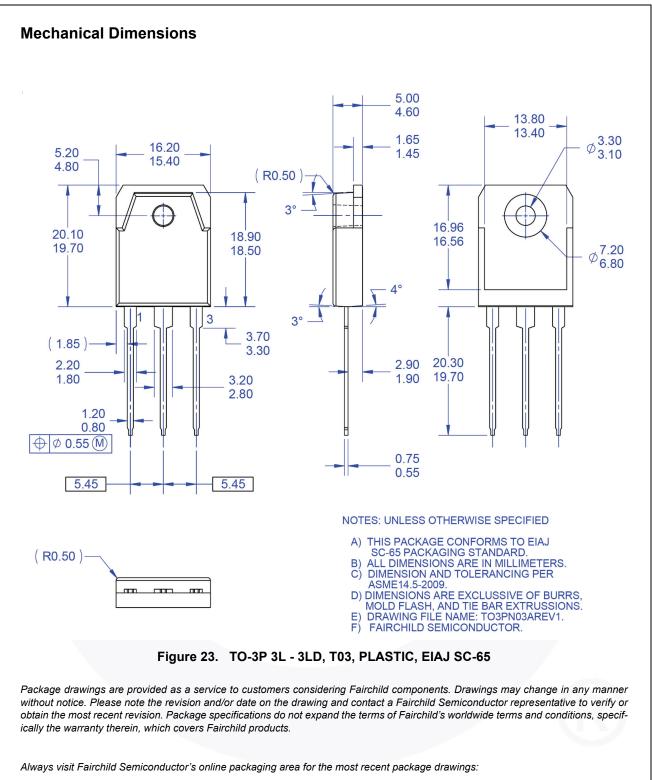
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