

## Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

[Molex Connector Corporation](#)  
[0442820004](#)

For any questions, you can email us directly:

[sales@integrated-circuit.com](mailto:sales@integrated-circuit.com)

**NOTES:**

**1) MATERIAL:**

HOUSING & INSULATOR: NYLON(PA), GLASS FILLED, UL94V-0, COLOR: BLACK  
TERMINALS: PHOSPHOR BRONZE, .012/(0.30) THICK  
SHIELD: BRASS

**2) FINISH:**

**TERMINALS:**

- A = SELECT GOLD IN CONTACT AREA: .000050/(0.00127) MIN.,  
\*SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MIN.,  
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.  
B = SELECT GOLD IN CONTACT AREA: .000030/(0.00076) MIN.,  
\*SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MIN.,  
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.  
C = SELECT GOLD IN CONTACT AREA: .000015/(0.00038) MIN.,  
\*SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MIN.,  
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.

**SHIELD:**

- \*100 MICROINCHES NICKEL OVER 50 MICROINCHES COPPER UNDERPLATE  
PCB GROUND TABS DIPPED IN TIN  
\*THE PRIMARY SHIPPING CARTON WILL BE LABELED "COMPLIANT TO RoHS  
DIRECTIVE 2002/95/EC AND ELV ANNEX II OF DIRECTIVE 2000/53/EC".  
CARTONS WITHOUT THIS LABEL MAY CONTAIN PRODUCT WITH TIN-LEAD  
IN THE PC TAILS AND/OR SHIELD.

**3) PRODUCT SPECIFICATION AND PROCESSING PARAMETERS: PS-43743-0001.**

**4) PACKAGING SPECIFICATION:**

CONNECTOR ASSEMBLIES TO BE PACKAGED USING TAPE & REEL SYSTEM  
PER PACKAGING DRAWING PK-70873-7006.

**COPLANARITY SPECIFICATION:**

WHEN AN ASSEMBLY IS PLACED UPON A LEVEL SURFACE, THE MAXIMUM  
GAP BETWEEN THE LEVEL SURFACE AND ALL TERMINAL TAILS AND  
FRONT SOLDER TAB IS .006/(0.15).

THRU HOLES .094±.002/(2.39±0.05) ARE FOR SHIELDS WITH LOCATION TABS ONLY.  
AVAILABLE WITH (4) OPTIONAL SIDE GROUND TABS (OMITTED FOR SIDE TO SIDE  
STACKABILITY).

AVAILABLE WITH (2) OPTIONAL LOCATION TABS FOR LOCATING COMPONENT  
TO PCB AND/OR GROUNDING TO PCB.

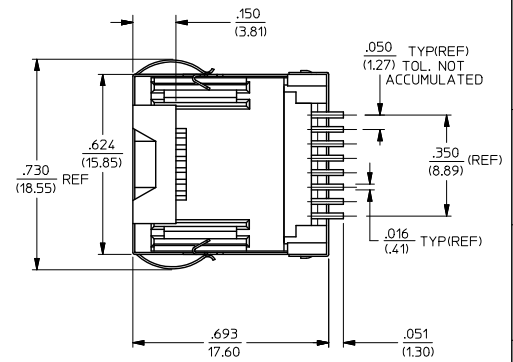
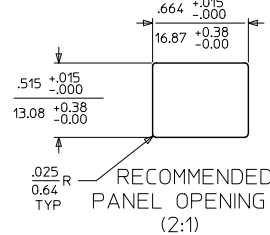
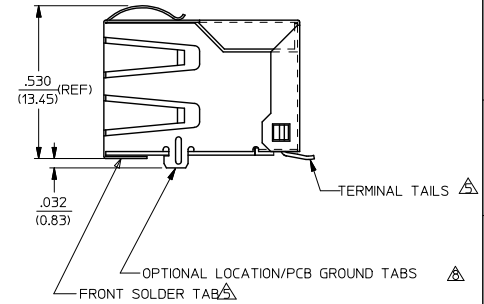
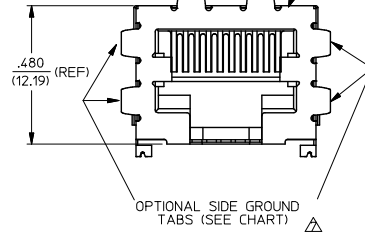
FOR ASSEMBLIES HAVING VOIDED CIRCUITS, THE TAIL PORTION IS CUT  
AND REMOVED. THE MATING PORTION OF THE TERMINAL REMAINS INTACT.

CONFORMS TO FCC REGULATION PART 68.5 FOR MODULAR JACKS.

THIS PART CONFORMS TO CLASS B REQUIREMENTS OF COSMETIC  
SPECIFICATION PS-45499-002.

OPTIONAL TOP GROUND TABS  
(SEE CHART)

SHIELDED SMT  
MODJACK ASSEMBLY



|            |   |     |     |     |      |
|------------|---|-----|-----|-----|------|
| 44282-0004 | A | NO  | NO  | YES | NONE |
| 44282-0012 | C | NO  | YES | NO  | NONE |
| 44282-0011 | B | NO  | YES | NO  | NONE |
| 44282-0010 | A | NO  | YES | NO  | NONE |
| 44282-0009 | C | YES | YES | NO  | NONE |
| 44282-0008 | B | YES | YES | NO  | NONE |
| 44282-0007 | A | YES | YES | NO  | NONE |
| 44282-0006 | C | NO  | YES | YES | NONE |
| 44282-0005 | B | NO  | YES | YES | NONE |
| 44282-0004 | A | NO  | YES | YES | NONE |
| 44282-0003 | C | YES | YES | YES | NONE |
| 44282-0002 | B | YES | YES | YES | NONE |
| 44282-0001 | A | YES | YES | YES | NONE |

44282-0001, 44282-0002  
& 44282-0003 SHOWN

| ASSEMBLY<br>MAT. NO. | TERMINAL<br>PLATING | SHIELD<br>SIDE TABS | SHIELD<br>TOP TABS | LOCATION<br>TABS | VOIDED<br>CIRCUITS |
|----------------------|---------------------|---------------------|--------------------|------------------|--------------------|
|----------------------|---------------------|---------------------|--------------------|------------------|--------------------|

|                   |    |    |    |   |   |   |   |   |   |   |   |   |
|-------------------|----|----|----|---|---|---|---|---|---|---|---|---|
| 1b_frame_C_P_ME_T | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 |
|-------------------|----|----|----|---|---|---|---|---|---|---|---|---|

Rev. E 2006/04/15

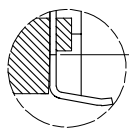
|                                                                                                                         |                                      |                                                                                       |  |                                           |  |                                                                    |                             |                               |
|-------------------------------------------------------------------------------------------------------------------------|--------------------------------------|---------------------------------------------------------------------------------------|--|-------------------------------------------|--|--------------------------------------------------------------------|-----------------------------|-------------------------------|
| <b>REVISED NOTES</b><br>EC NO: UCP2009-1244<br>DRAWN: JACUSTE (E) 2008/12/10<br>CHKD: BELL<br>APPR: SMITH<br>2008/12/10 | <b>QUALITY SYMBOLS</b><br>▽=0<br>▽=0 | <b>GENERAL TOLERANCES (UNLESS SPECIFIED)</b>                                          |  | <b>DIMENSION STYLE</b><br>IN/MM           |  | <b>SCALE</b><br>1:1                                                | <b>DESIGN UNITS</b><br>INCH | <b>THIRD ANGLE PROJECTION</b> |
|                                                                                                                         |                                      | 4 PLACES ±--- ±---<br>3 PLACES ±--- ±.010<br>2 PLACES ±0.25 ±---<br>1 PLACE ±--- ±--- |  | DRAWN BY<br>STAMBAUG<br>DATE<br>99/06/03  |  | TITLE<br>SINGLE PORT, SHIELDED<br>SIZE 8, 8 CIRCUIT<br>SMT MODJACK |                             |                               |
|                                                                                                                         |                                      | ANGULAR ±1/2°                                                                         |  | CHECKED BY<br>WIRKUS<br>DATE<br>99/06/03  |  | MOLEX INCORPORATED                                                 |                             |                               |
|                                                                                                                         |                                      | DRAFT WHERE APPLICABLE<br>MUST REMAIN<br>WITHIN DIMENSIONS                            |  | APPROVED BY<br>EDGLEY<br>DATE<br>99/06/03 |  | MATERIAL NO.<br>SEE CHART<br>DOCUMENT NO.<br>SD-44282-001          |                             |                               |

|     |     |
|-----|-----|
| 2   | D2  |
| 1   | D3  |
| SHT | REV |

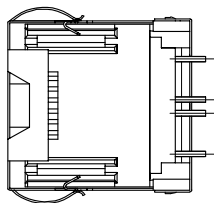
SHEET NO.  
1 OF 2

THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX  
INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION

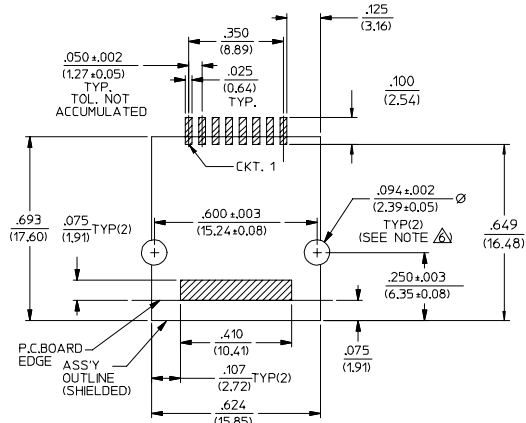
TERMINAL CUTOFF POINT



VIEW SHOWING TYPICAL TERMINAL  
CUTOFF OF TAIL (SEE NOTE 10) FOR VOIDED CIRCUITS.



TYPICAL VIEW SHOWING VOIDED CIRCUIT  
(CIRCUITS 2,3,6 & 7 SHOWN VOIDED)



PC BOARD LAYOUT FOR  
8 POSITION HOUSING  
(COMPONENT SIDE OF BOARD)

NOTES:

1. RECOMMENDED MINIMUM PCB THICKNESS: .034/0.86

|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        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