

Excellent Integrated System Limited

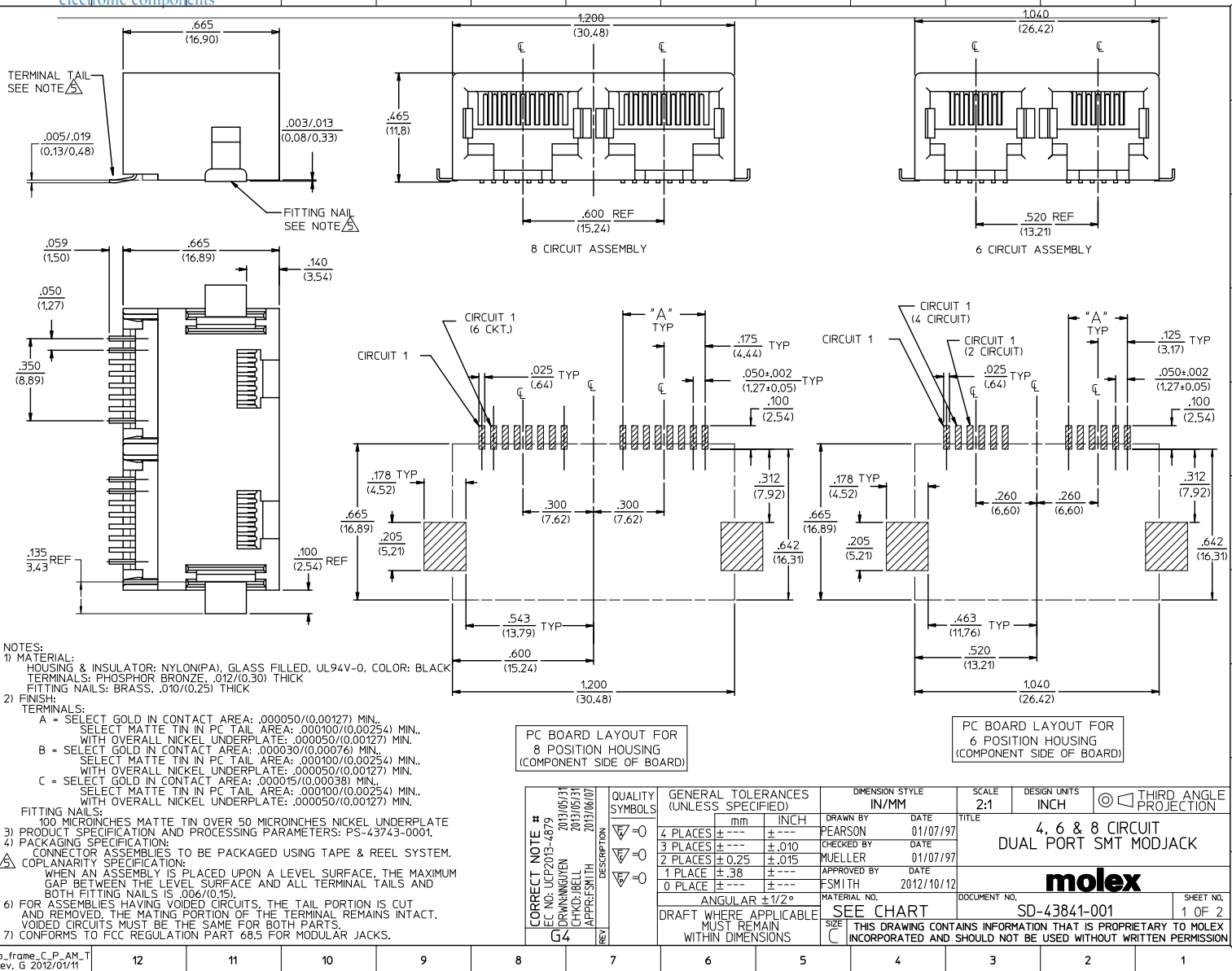
Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

[Molex Connector Corporation](#)
[0438410018](#)

For any questions, you can email us directly:

sales@integrated-circuit.com

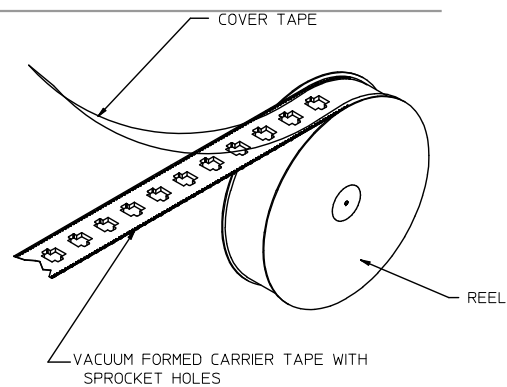


ASSEMBLY ITEM NO.	CONNECTOR SIZE	CIRCUIT SIZE	TERMINAL PLATING	TAPE & REEL PACKAGING NO.	VOIDED CIRCUITS
43841-0001	8	4	A	PK-70873-7004	NA
43841-0002	8	6	A	PK-70873-7004	
43841-0003	8	8	A	PK-70873-7004	
43841-0004	6	4	A	PK-70873-7003	
43841-0005	6	6	A	PK-70873-7003	
43841-0006	8	4	B	PK-70873-7004	
43841-0007	8	6	B	PK-70873-7004	
43841-0008	8	8	B	PK-70873-7004	
43841-0009	6	4	B	PK-70873-7003	
43841-0010	6	6	B	PK-70873-7003	
43841-0011	8	4	C	PK-70873-7004	
43841-0012	8	6	C	PK-70873-7004	
43841-0013	8	8	C	PK-70873-7004	
43841-0014	6	4	C	PK-70873-7003	
43841-0015	6	6	C	PK-70873-7003	
43841-0016	6	2	A	PK-70873-7003	
43841-0017	6	2	B	PK-70873-7003	
43841-0018	6	2	C	PK-70873-7003	NA

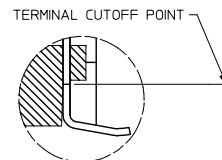
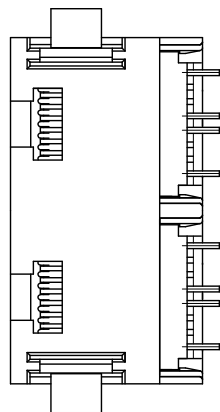
HOUSING SIZE	CIRCUIT SIZE	DIM. A
6	6	.250 (6.35)
6	4	.150 (3.81)
6	2	.050 (1.27)
8	8	.350 (8.89)
8	6	.250 (6.35)
8	4	.150 (3.81)

TYPICAL VIEW SHOWING VOIDED CIRCUIT
(CIRCUITS 2,3,6,7 SHOWN VOIDED)

SEE SHEET ONE EC NO: UCP2013-4679 DRAWING NO: 2013/05/31 CHKD: JELL 2013/05/31 APPR: SMTH 2013/06/07 REV	QUALITY SYMBOLS ▽=0 ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED) mm INCH 4 PLACES ± --- ± --- 3 PLACES ± --- ± .010 2 PLACES ± 0.25 ± .015 1 PLACE ± .38 ± --- 0 PLACE ± --- ± --- ANGULAR ±1/2° DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	DIMENSION STYLE IN/MM DRAWN BY DATE PEARSON 01/07/97 CHECKED BY DATE MUELLER 01/07/97 APPROVED BY DATE FSMITH 2012/10/12 MATERIAL NO. DOCUMENT NO. SD-43841-001 SHEET NO. 2 OF 2	SCALE 2:1 DESIGN UNITS INCH THIRD ANGLE PROJECTION 4, 6 & 8 CIRCUIT DUAL PORT SMT MODJACK molex	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION



TAPE AND REEL PACKAGING

VIEW SHOWING TYPICAL TERMINAL CUTOFF
OF TAIL (SEE NOTE 6) FOR VOIDED CIRCUITS.