

Excellent Integrated System Limited

Stocking Distributor

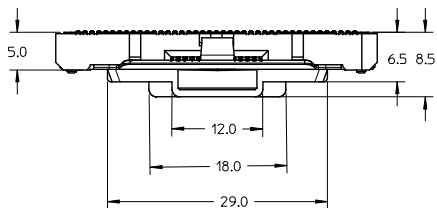
Click to view price, real time Inventory, Delivery & Lifecycle Information:

[Molex Connector Corporation](#)
[0475960232](#)

For any questions, you can email us directly:

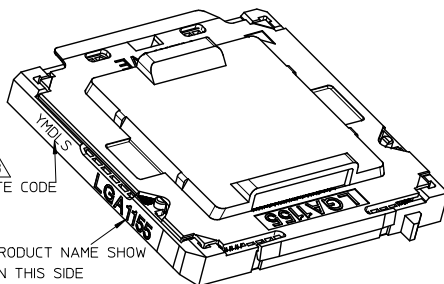
sales@integrated-circuit.com

10 9 8 7 6 5 4 3 2 1



DATE CODE

PRODUCT NAME SHOW ON THIS SIDE



NOTES:

1. MATERIAL:

HOUSING AND CAP: HIGH TEMPERATURE THERMOPLASTIC, UL94V-0
CONTACT: HIGH STRENGTH COPPER ALLOY
SOLDER BALL: LEAD FREE SnAg3.0Cu0.5

2. PLATING:

50u" MIN. NICKEL AT PADDLE AND CONTACT AREA.
GOLD PLATING THICKNESS AT CONTACT AREA REFER TO TABLE.

3. DATE CODE 'YMDS' IS DEFINED AS:

"YEAR/MONTH/DAY/***(I)**" IS MOLEX INTERNAL CONTROLLED).

4. 47596**** COMPLIANT TO RoHS DIRECTIVE 2002/95/EC AND ELV DIRECTIVE 2000/53/EC.

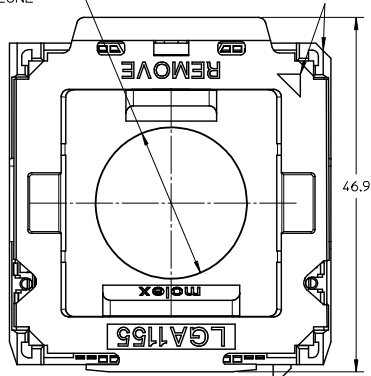
5. PRODUCT SPECIFICATION PS-47596-001 APPLIES.

6. PACKAGE DRAWING PK-47596-001 APPLIES.

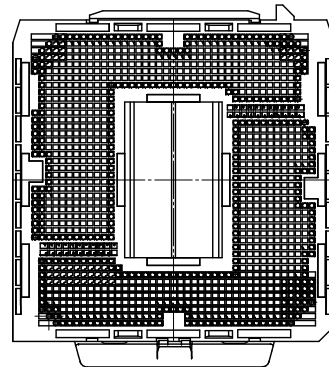
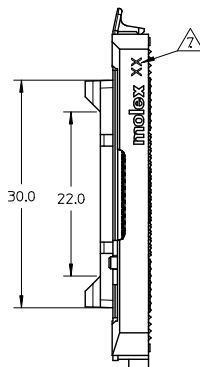
7. "XX" IS FOR CAVITY # (MOLEX INTERNAL USE)

Ø20
PICK AND PLACE
IN THIS ZONE

A1 CORNER

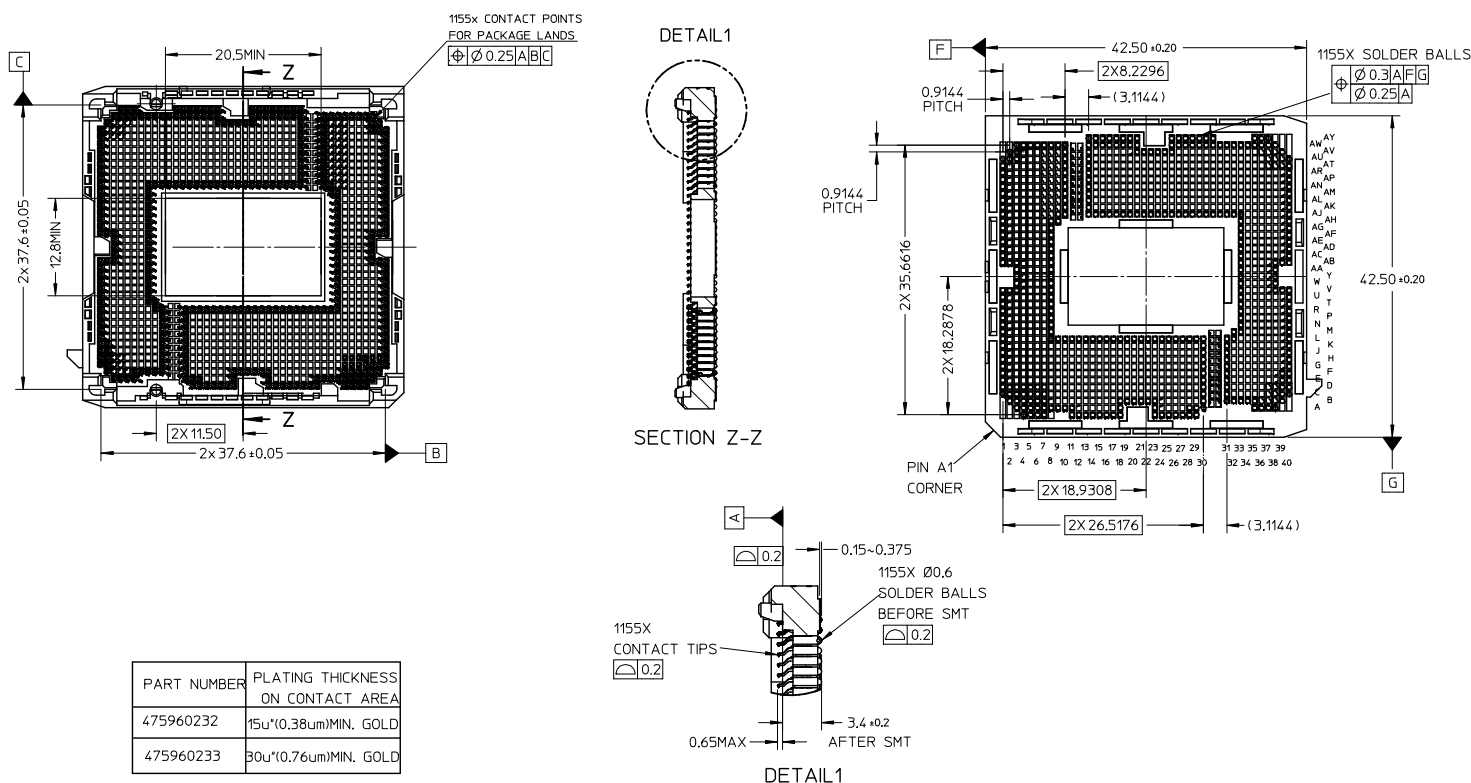


TOP VIEW

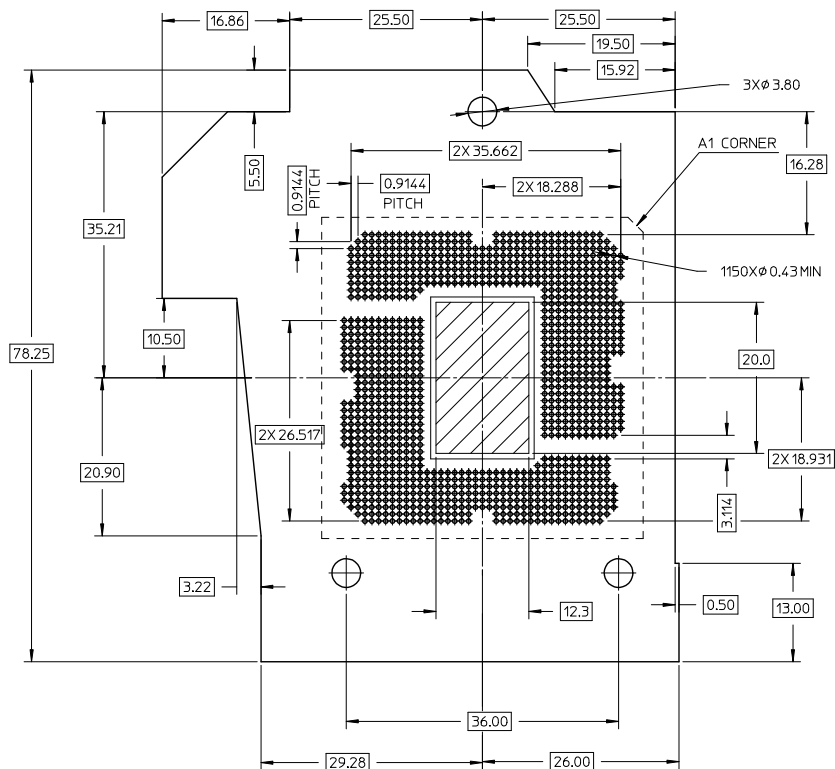


BOTTOM VIEW

ENTER DESCRIPTION EC NO: SH2013-0312 DRAWN: YZHENG CHKD: APPR: AYIN		DESCRIPTION 2013/03/28 2013/04/16	QUALITY SYMBOLS FA=0 FB=0 FC=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE IN/MM		SCALE 2:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION	
				mm INCH	DRAWN BY DATE	TITLE					
				4 PLACES ± --- ± ---	VLV	2008/12/11	LGA1155 SOCKET				
				3 PLACES ± --- ± ---	CHECKED BY	DATE					
				2 PLACES ± 0.25 ± ---	JKACHLIC	2008/12/11					
				1 PLACE ± 0.25 ± ---	APPROVED BY	DATE					
				ANGULAR ± 1 °		MOLEX MOLEX INCORPORATED					
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				MATERIAL NO.		DOCUMENT NO.		SHEET NO.			
				SEE TABLE		SD-47596-200		1 OF 3			
				SIZE A THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							



ENTER DESCRIPTION EC NO: SH2013-0312 DRAWN: YXZENG CHKD: YXZENG APPR: ATIN REV	QUALITY SYMBOLS ▽=0 ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE IN/MM		SCALE 2:1	DESIGN UNITS INCH	 THIRD ANGLE PROJECTION	
			mm	INCH	DRAWN BY VLV	DATE 2008/12/11	TITLE LGA1155 SOCKET		
		4 PLACES ± ---	± ---	CHECKED BY JKACHLIC	DATE 2008/12/11	 MOLEX INCORPORATED			
		3 PLACES ± ---	± ---	APPROVED BY	DATE				
		2 PLACES ± 0.25	± ---						
		1 PLACE ± 0.25	± ---						
ANGULAR ± 1 °		MATERIAL NO. SEE TABLE		DOCUMENT NO. SD-47596-200		SHEET NO. 2 OF 3			
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					



PCB TOP SIDE KEEP OUT ZONE LAYOUT
FOR REFERENCE

ENTER DESCRIPTION EC NO: SH2013-0312 DRAWN: YXZHENG CHKD: APPR: ATIN REV	QUALITY SYMBOLS FA=0 FB=0 FC=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE IN/MM		SCALE 2:1	DESIGN UNITS INCH	 THIRD ANGLE PROJECTION	
			mm	INCH	DRAWN BY VLV	DATE 2008/12/11	TITLE LGA1155 SOCKET		
		4 PLACES ± ---	± ---	CHECKED BY JKACHLIC	DATE 2008/12/11				
		3 PLACES ± ---	± ---	APPROVED BY	DATE	 MOLEX INCORPORATED			
		2 PLACES ± 0.25	± ---						
		1 PLACE ± 0.25	± ---						
		ANGULAR ± 1 °		MATERIAL NO. SEE TABLE		DOCUMENT NO. SD-47596-200		SHEET NO. 3 OF 3	
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					