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Fairchild Semiconductor DM74LS365AN

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August 1986 Revised March 2000

DM74LS365A Hex 3-STATE Buffer/Bus Driver

General Description

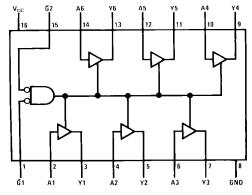
This device contains six independent gates each of which performs a non-inverting buffer function. The outputs have the 3-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard LS output with additional drive capability to permit the driving of bus lines without external resistors. When disabled, both the output transistors are turned OFF presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver. To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

Ordering Code:

Order Number	Package Number	Package Description
DM74LS365AM	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74LS365AN	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code

Connection Diagram



Function Table

Input Output G1 G2 Α Н Χ Χ Χ Н Χ Hi-Z L L Н Н ı

 $\mathbf{Y} = \mathbf{A}$

H = HIGH Logic Level L = LOW Logic Level

X = Either LOW or HIGH Logic Level Hi-Z = 3-STATE (Outputs are disabled)

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Datasheet of DM74LS365AN - IC BUFF/BUS DVR TRI-ST HEX 16DIP

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DM74LS365A

Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range $0^{\circ}\text{C to } + 70^{\circ}\text{C}$ Storage Temperature Range $-65^{\circ}\text{C to } + 150^{\circ}\text{C}$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
Гон	HIGH Level Output Current			-2.6	mA
I _{OL}	LOW Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	V _{CC} = Min, I _I = -18 mA				-1.5	V
V _{OH}	HIGH Level	V _{CC} = Min, I _{OH} = Max		2.4	3.4		V
	Output Voltage	$V_{IL} = Max, V_{IH} = Min$		2.4			V
V _{OL}	LOW Level	V _{CC} = Min, I _{OL} = Max			0.35	0.5	V
	Output Voltage	$V_{IL} = Max, V_{IH} = Min$					
		I _{OL} = 12 mA, V _{CC} = Min			0.25	0.4	
I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 7V				0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μΑ
I _{IL}	LOW Level	V _{CC} = Max, V _I = 0.5V (Note 3)	A Input			-20	μА
	Input Current	V _{CC} = Max, V _I = 0.4V (Note 4)	A Input			-0.4	m A
		$V_{CC} = Max, V_I = 0.4V$	G Input			-0.4	mA
I _{OZH}	Off-State Output Current with	$V_{CC} = Max, V_O = 2.4V$				20	μА
	HIGH Level Output Voltage Applied	$V_{IH} = Min, V_{IL} = Max$				20	
I _{OZL}	Off-State Output Current with	$V_{CC} = Max, V_O = 0.4V$				-20	
	LOW Level Output Voltage Applied	V _{IH} = Min, V _{IL} = Max	= Max			-20	μΑ
Ios	Short Circuit Output Current	V _{CC} = Max (Note 5)		-20		-100	mA
I _{CC}	Supply Current	V _{CC} = Max (Note 6)			14	24	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 3: Both \overline{G} inputs are at 2V.

Note 4: Both \overline{G} inputs at 0.4V.

Note 5: Not more than one output should be shorted at a time, and the duration should not exceed one second.

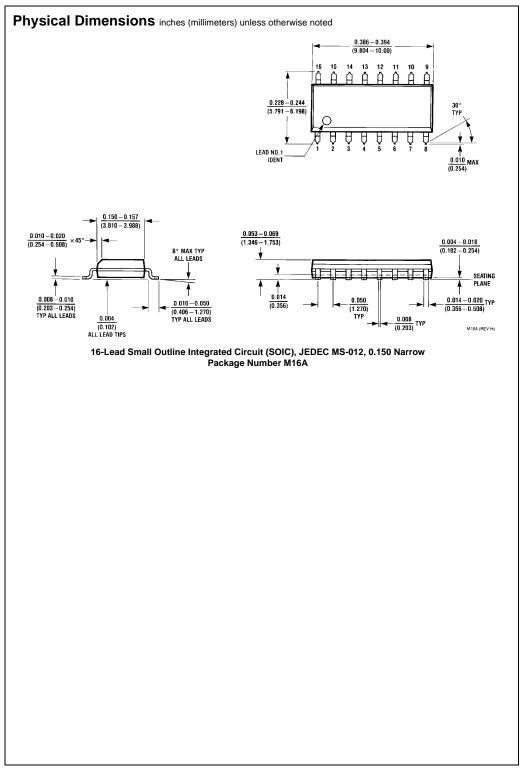
Note 6: $I_{\rm CC}$ is measured with the DATA inputs grounded and the OUTPUT CONTROLS at 4.5V.

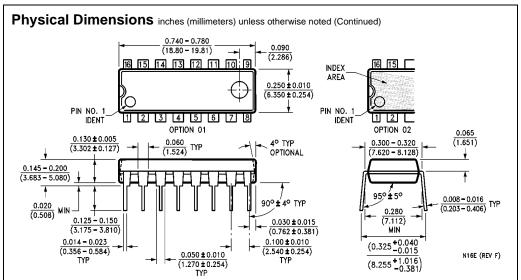
Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

	Parameter					
Symbol		C _L = 50 pF		C _L = 150 pF		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output		16		25	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output		16		25	ns
t _{PZH}	Output Enable Time to HIGH Level Output		30		40	ns
t _{PZL}	Output Enable Time to LOW Level Output		30		40	ns
t _{PHZ}	Output Disable Time from HIGH Level Output (Note 7)		20			ns
t _{PLZ}	Output Disable Time from LOW Level Output (Note 7)		20			ns
Note 7: $C_L = 5 \text{pF}$.						







16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

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