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**DMN2400UFB**

**N-CHANNEL ENHANCEMENT MODE MOSFET**

### Product Summary

$V_{(BR)DSS}$	$R_{DS(ON) \text{ max}}$	$I_D \text{ max}$ $T_A = +25^\circ\text{C}$
20V	0.55Ω @ $V_{GS} = 4.5\text{V}$	0.75A
	0.75Ω @ $V_{GS} = 2.5\text{V}$	0.63A

### Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **ESD Protected up to 1.5kV**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

### Description and Applications

This MOSFET is designed to minimize the on-state resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors

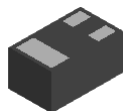
### Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: Collector Dot
- Terminals: Finish — NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.001 grams (Approximate)

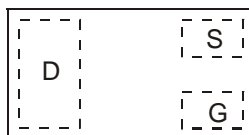


ESD PROTECTED TO 1.5kV

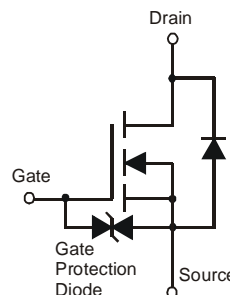
X1-DFN1006-3



Bottom View



Top View  
Package Pin Configuration





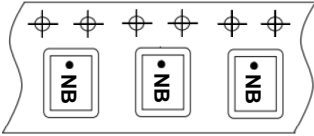
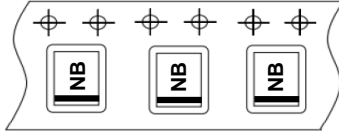

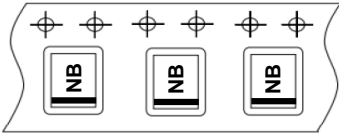
Equivalent Circuit

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2400UFB-7	X1-DFN1006-3	3,000/Tape & Reel
DMN2400UFB-7B	X1-DFN1006-3	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

**Marking Information**

<p><b>DMN2400UFB-7</b></p>	<div style="display: flex; justify-content: space-around;"> <div style="text-align: center;">  <p>Top View Dot Denotes Drain Side</p> </div> <div style="text-align: center;"> <p>From date code 1527 (YYWW), this changes to:</p>  <p>Top View Bar Denotes Gate and Source Side</p> </div> </div> <div style="display: flex; justify-content: space-around; margin-top: 10px;">   </div>
<p><b>DMN2400UFB-7B</b></p>	<div style="text-align: center;">  <p>Top View Bar Denotes Gate and Source Side</p> </div> <div style="text-align: center; margin-top: 10px;">  </div> <div style="text-align: right; margin-top: 10px;"> <p>NB = Part Marking Code</p> </div>

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±12	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	Steady State	T <sub>A</sub> = +25°C	I <sub>D</sub>	0.75	A
		T <sub>A</sub> = +85°C	I <sub>D</sub>	0.55	A
Continuous Drain Current (Note 5) V <sub>GS</sub> = 2.5V	Steady State	T <sub>A</sub> = +25°C	I <sub>D</sub>	0.63	A
		T <sub>A</sub> = +85°C	I <sub>D</sub>	0.45	A
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	3	A

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P <sub>D</sub>	0.47	mW
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	258	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-	-	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	100	nA	T <sub>J</sub> = +25°C, V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±1.0	μA	V <sub>GS</sub> = ±4.5V, V <sub>DS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±50	μA	V <sub>GS</sub> = ±10V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS</b> (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	-	0.9	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	-	-	0.55	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 600mA
		-	-	0.75		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 500mA
		-	-	0.9		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 350mA
Forward Transfer Admittance	Y <sub>fs</sub>	-	1.0	-	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 400mA
Diode Forward Voltage (Note 7)	V <sub>SD</sub>	-	0.7	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 150mA
<b>DYNAMIC CHARACTERISTICS</b> (Note 8)						
Input Capacitance	C <sub>iss</sub>	-	36.0	-	pF	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	-	5.7	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	4.2	-	pF	
Total Gate Charge	Q <sub>g</sub>	-	0.5	-	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA
Gate-Source Charge	Q <sub>gs</sub>	-	0.07	-	nC	
Gate-Drain Charge	Q <sub>gd</sub>	-	0.1	-	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	-	4.11	-	ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.5V, R <sub>L</sub> = 47Ω, R <sub>G</sub> = 10Ω, I <sub>D</sub> = 200mA
Turn-On Rise Time	t <sub>r</sub>	-	3.82	-	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	14.8	-	ns	
Turn-Off Fall Time	t <sub>f</sub>	-	9.6	-	ns	

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
  - Device mounted on minimum recommended pad layout test board, 10μs pulse duty cycle = 1%.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

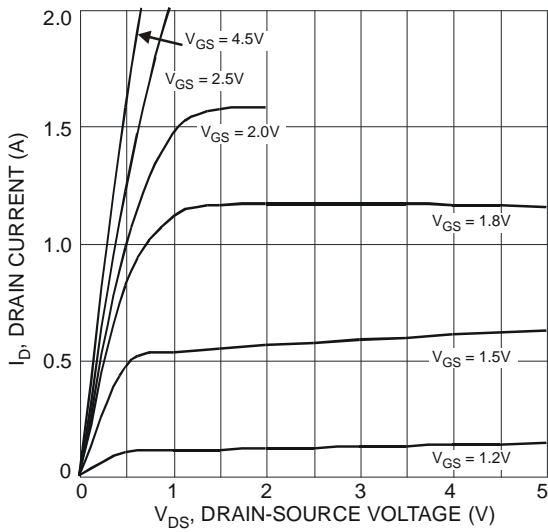


Fig. 1 Typical Output Characteristics

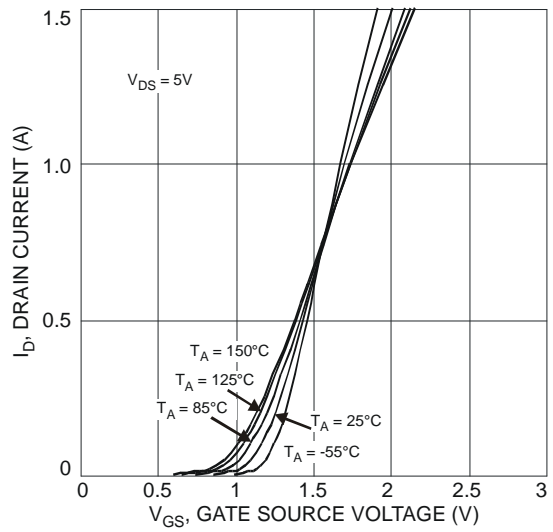


Fig. 2 Typical Transfer Characteristics

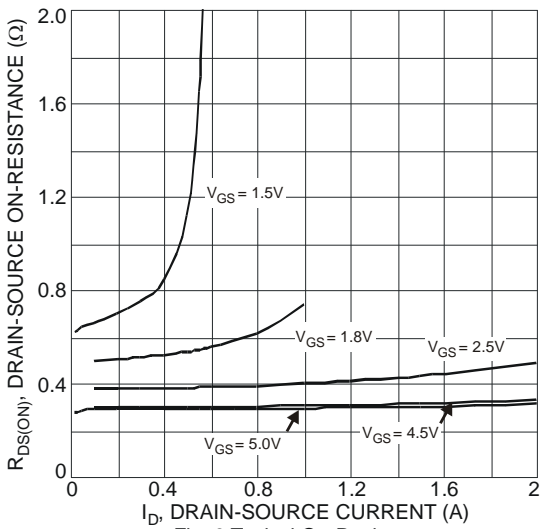


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

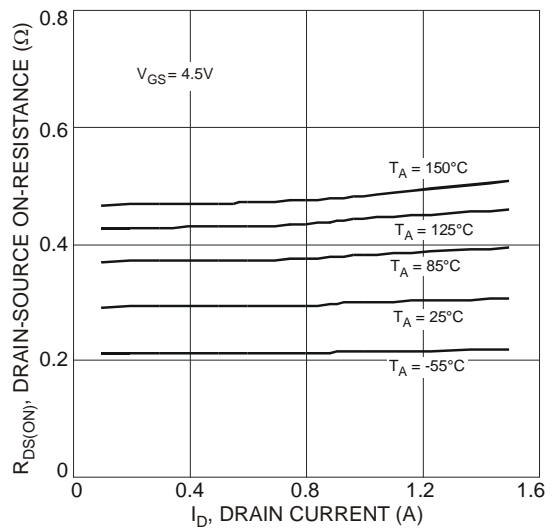


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

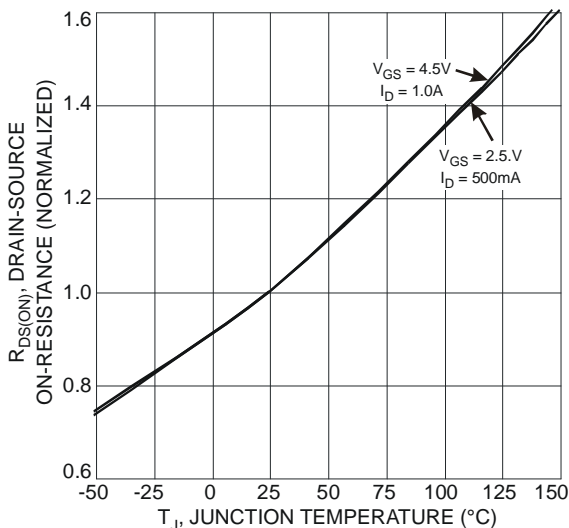


Fig. 5 On-Resistance Variation with Temperature

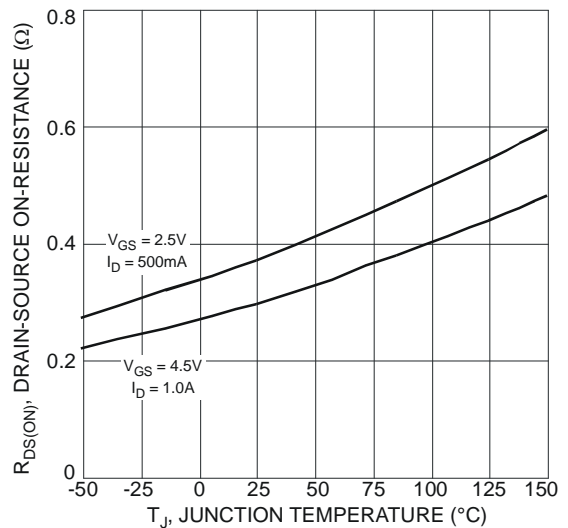


Fig. 6 On-Resistance Variation with Temperature

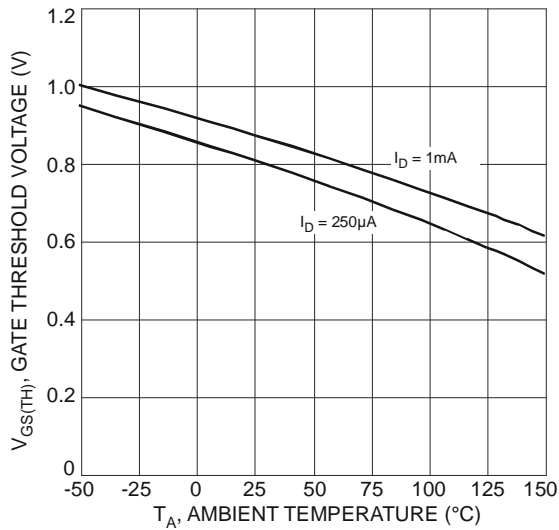


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

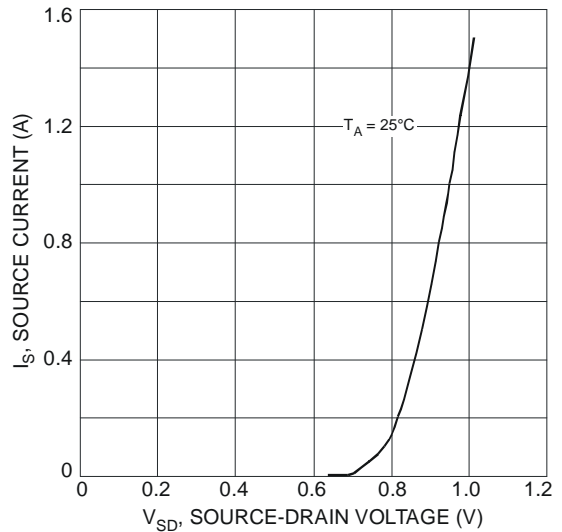


Fig. 8 Diode Forward Voltage vs. Current

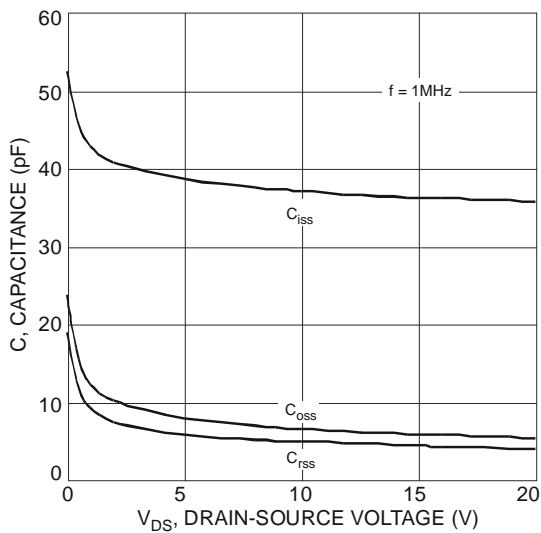


Fig. 9 Typical Capacitance

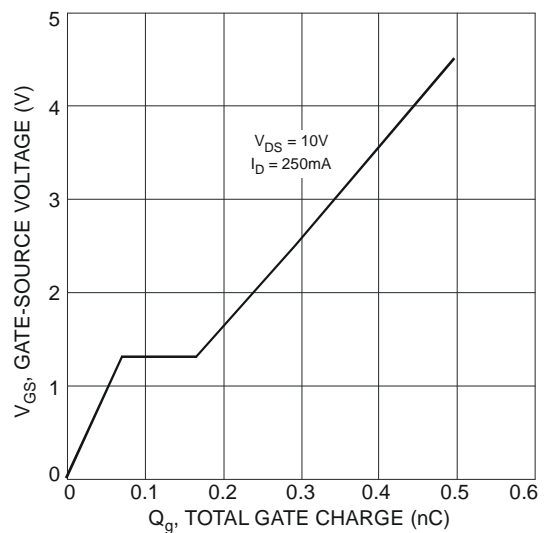


Fig. 10 Gate-Charge Characteristics

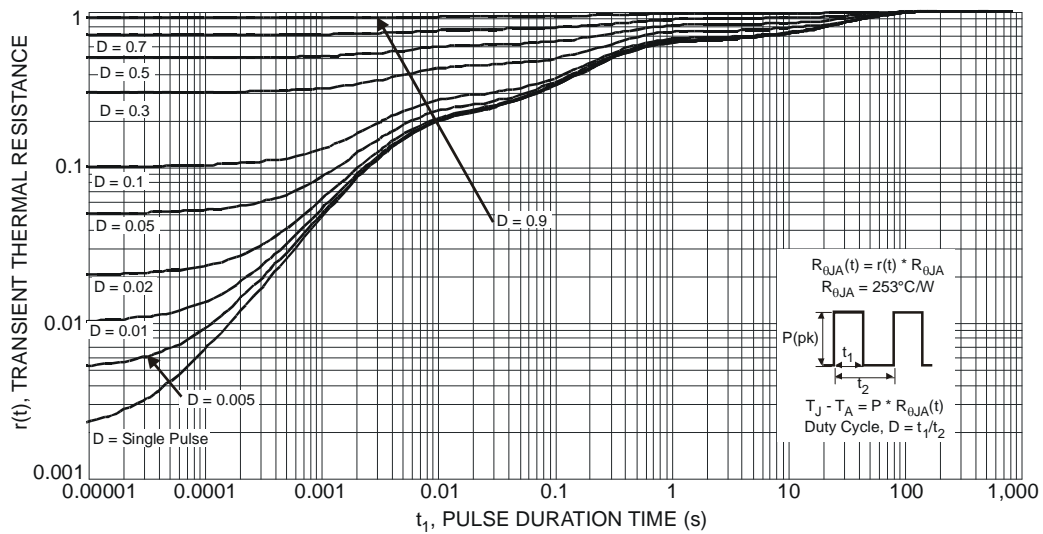
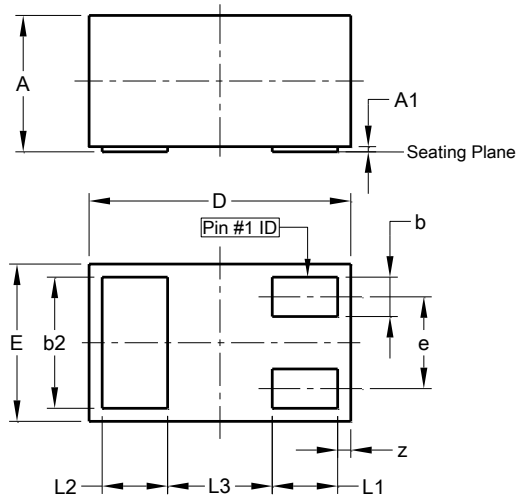


Fig. 11 Transient Thermal Response

### Package Outline Dimensions

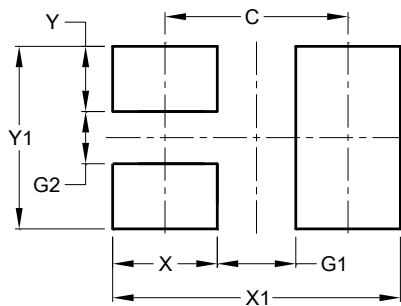
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



X1-DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0.00	0.05	0.03
b	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	-	-	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	-	-	0.40
z	0.02	0.08	0.05
All Dimensions in mm			

### Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.70
G1	0.30
G2	0.20
X	0.40
X1	1.10
Y	0.25
Y1	0.70

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