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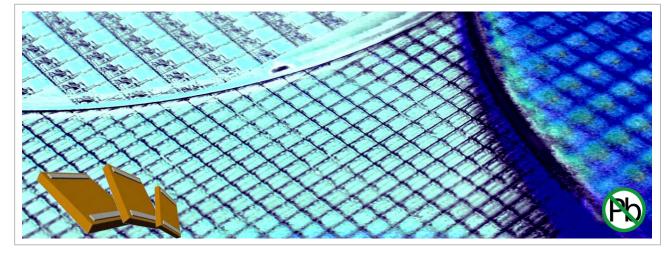
For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>







Rev 3.1



Key features

- Ultra low profile (100µm)
- High stability of capacitance value:
 - Temperature <±0.5% (-55°C to +150°C)
 - Voltage <0.1%/Volts
 - Negligible capacitance loss through ageing
- Unique high capacitance in EIA/0603 package size, up to 100 nF
- High reliability (FIT <0.017 parts / billion hours)</p>
- Low leakage current down to 100 pA
- Low ESL and Low ESR
- Suitable for lead free reflow-soldering *Please refer to our assembly Application Note for further recommendations

Thanks to the unique IPDiA Silicon capacitor technology, most of the problems encountered in demanding applications can be solved.

Low Profile Silicon Capacitors are available with thicknesses down to 80µm and are the most appropriate solution in applications with height constraints.

LPSC is the perfect choice for embedded technologies, modules, systems in package, when designers are looking at **utmost decoupling behaviours.**

The Silicon capacitor technology offers a capacitor integration capability (up to 250nF/mm²) which allows **downsizing** compared to Tantalum and MLCC.

Key applications

- All demanding applications, such as medical, telecom, computer industries
- Decoupling / Filtering / Charge pump (i.e.: Pacemakers / mobile phones)
- High reliability applications
- Devices with battery operations
- Extreme miniaturization
- Suitable for Embedded technologies

The IPDiA technology features **high reliability**, up to 10 times better than alternative capacitor technologies, such as Tantalum or MLCC, and eliminates cracking phenomena.

Silicon Capacitor technology also offers a very stable capacitor value over the full operating voltage & temperature range, with a high and stable insulation resistance.

This Silicon based technology is RoHS compliant and compatible with lead free reflow soldering process.





ipdia

LPSC425.xxx

Value

Electrical specification

		Capacitance value						
		10	15	22	33	47	68	C
	10 pF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	
ij	0.1 nF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	S T
P	0.1 nF 1 nF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	
	10 nF	100 nF: 935.121.425.610						F

(*)80 µm thickness on request

(**) Extended temperature range (up to +250 °C) available, see Xtreme Temperature Silicon Capacitor product: XTSC

(***) Other values on request

Capacitance range	100 nF ^(***)		
Capacitance tolerances	±15 % ^(***)		
Operating temperature range	-55 °C to 150 °C (**)		
Storage temperatures	- 70 °C to 165 °C		
Temperature coefficient	<±0.5 %, from -55 °C to +150 °C		
Breakdown voltage (BV)	11 VDC ^(***)		
Capacitance variation versus RVDC	0.1 % /V (from 0 V to RVDC)		
Equivalent Serial Inductor (ESL)	Max 250 pH		
Equivalent Serial Resistor (ESR)	Max 400mΩ ^(***)		
Insulation resistance	100GΩ min @ 3V,25°C		
Ageing	Negligible, < 0.001 % / 1000 h		
Reliability	FIT<0.017 parts / billion hours,		
Capacitor height	Max 100 μm ^(*)		

Parameters

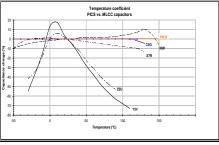


Fig.1 Capacitance change versus temperature variation compared with alternative dielectrics

Part Number

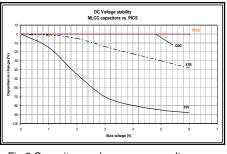


Fig.2 Capacitance change versus voltage variation compared with alternative dielectrics

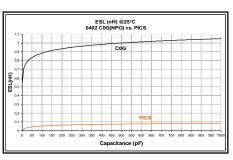
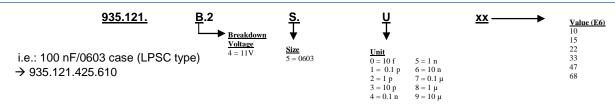


Fig.3 ESL versus capacitance value compared with alternative dielectrics



Termination and Outline

Termination

Lead-free nickel/solder coating compatible with automatic soldering technologies: reflow and manual.

Typical dimensions, all dimensions in mm.

Packaging

Tape and reel, tray, waffle pack or wafer delivery.

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For more information, please visit: http://www.ipdia.com To contact us, email to: sal s@india.com

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(0603 PCB footprint)

Package outline

