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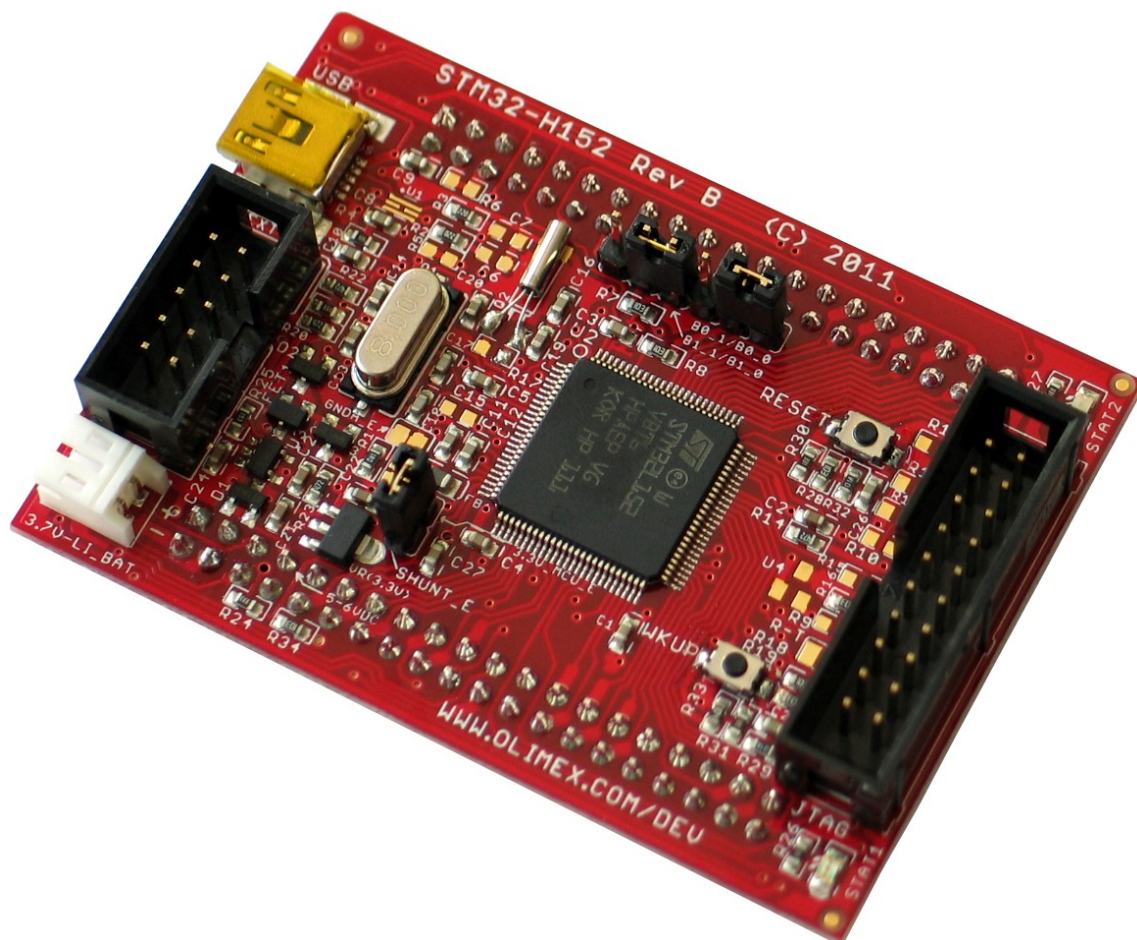
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STM32-H152 development board

Users Manual



Pb-free, Green

All boards produced by Olimex are ROHS compliant

Revision Initial, September 2011
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INTRODUCTION:

STM32-H152 is header board with STM32L152VBT6 ARM Cortex M3 microcontroller from STMicroelectronics. This microcontroller supports various communication interfaces such as USB, three USARTs, two SPIs, two I²C. There are available mini USB, JTAG and UEXT connectors, two buttons – WKUP and RESET, two status leds and extension connectors for most of the microcontroller's pins. All this allows you to build a different projects to be used in a wide range of applications.

BOARD FEATURES:

- MCU: **STM32L152VBT6** – 128 KB Flash, 16 KB Data RAM
- Mini USB
- 3.7V-LI_BAT connector
- Battery charger
- JTAG connector
- UEXT connector
- Two status leds
- Reset circuit
- RESET button
- WKUP button
- Extension connectors for most of the microcontroller pins
- FR-4, 1.5 mm, soldermask, component print
- Dimensions: 70.00 x 50.00 mm (2.76 x 1.97")

ELECTROSTATIC WARNING:

The **STM32-H152** board is shipped in protective anti-static packaging. The board must not be subject to high electrostatic potentials. General practice for working with static sensitive devices should be applied when working with this board.

BOARD USE REQUIREMENTS:

Cables: The cable you will need depends on the programmer/debugger you use. If you use ARM-JTAG-EW, ARM-USB-TINY or ARM-USB-TINY-H, you will need USB A-B cable, if you use ARM-USB-OCD or ARM-USB-OCD-H, you will need USB A-B cable and RS232 cable.

Hardware: Programmer/Debugger [ARM-USB-OCD](#), [ARM-USB-OCD-H](#), [ARM-USB-TINY](#), [ARM-USB-TINY-H](#), [ARM-JTAG-EW](#) or other compatible programming/debugging tool if you work with EW-ARM.

PROCESSOR FEATURES:

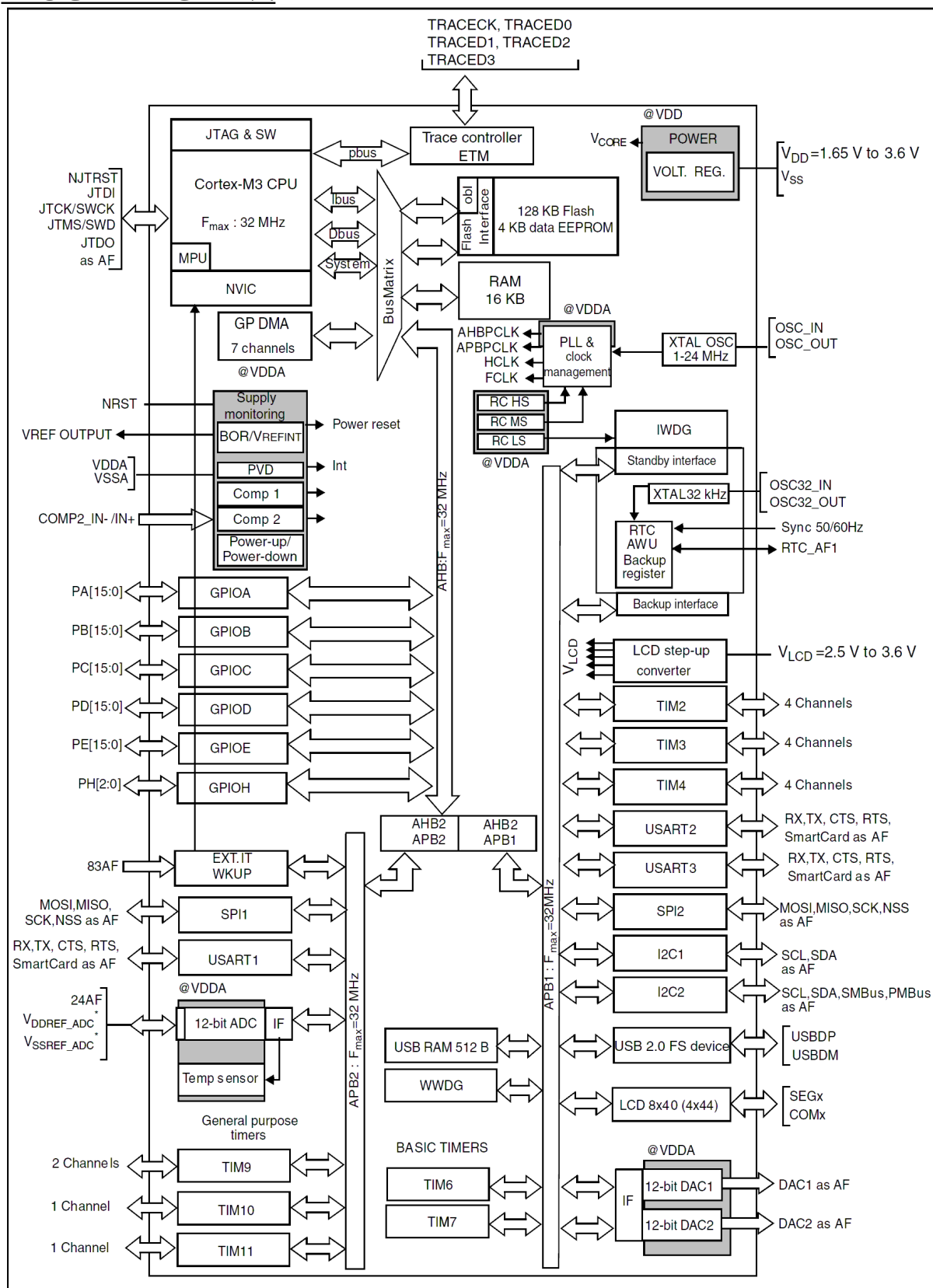
STM32-H152 board use ARM 32-bit Cortex™-M3 microcontroller **STM32L152VBT6** from STMicroelectronics with these features:

- Operating conditions
 - Operating power supply range: 1.65 V to 3.6 V (without BOR) or 1.8 V to 3.6 V (with BOR option)
 - Temperature range: -40 to 85 °C
- Low power features
 - 4 modes: Sleep, Low-power run (9 µA at 32 kHz), Low-power sleep (4.4 µA), Stop with RTC (1.45 µA), Stop (570 nA), Standby (300 nA)
 - Dynamic core voltage scaling down to 233 µA/MHz
 - Ultralow leakage per I/O: 50 nA
 - Fast wakeup from Stop: 8 µs
 - Three wakeup pins
- Core: ARM 32-bit Cortex™-M3 CPU
 - 32 MHz maximum frequency, 33.3 DMIPS peak (Dhrystone 2.1)
 - Memory protection unit
- Reset and supply management
 - Low power, ultrasafe BOR (brownout reset) with 5 selectable thresholds
 - Ultralow power POR/PDR
 - Programmable voltage detector (PVD)
- Clock management

- 1 to 24 MHz crystal oscillator
- 32 kHz oscillator for RTC with calibration
- Internal 16 MHz factory-trimmed RC
- Internal 37 kHz low consumption RC
- Internal multispeed low power RC, 64 kHz to 4 MHz with a consumption down to 1.5 μ A
- PLL for CPU clock and USB (48 MHz)
- Low power calendar RTC
 - Alarm, periodic wakeup from Stop/Standby
- Memories
 - 128 Kbyte of Flash memory with ECC
 - 4 Kbyte of data EEPROM with ECC
 - 16 Kbyte of RAM
- 83 fast I/Os (73 of which are 5 V-tolerant) all mappable on 16 external interrupt vectors
- Development support
 - Serial wire debug, JTAG and trace
- DMA: 7-channel DMA controller, supporting timers, ADC, SPIs, I2Cs and USARTs
- LCD 8×40 or 4×44 with step-up converter
- 12-bit ADC up to 1 Msps/24 channels
 - Temperature sensor and internal voltage reference
 - Operates down to 1.8 V
- 2 $\times$ 12-bit DACs with output buffers
- 2 ultralow power comparators
 - Window mode and wakeup capability
- 10 timers:
 - 6 $\times$ 16-bit general-purpose timers, each with up to 4 IC/OC/PWM channels
 - 2 $\times$ 16-bit basic timers
 - 2 $\times$ watchdog timers (independent and window)
- 8 communication interfaces:
 - 2 $\times$ I2C interfaces (SMBus/PMBus)
 - 3 $\times$ USARTs (ISO 7816 interface, LIN, IrDA capability, modem control)
 - 2 $\times$ SPIs (16 Mbit/s)
 - USB 2.0 full speed interface
- CRC calculation unit, 96-bit unique ID

Peripheral		STM32L152VBT6
Flash - Kbytes		128
RAM - Kbytes		16
Timers	General-purpose	6
	Basic	2
Communication interfaces	SPI	2
	I ² C	2
	USART	3
	USB	1
GPIOs		83
12-bit synchronized ADC		1
Number of channels		24 channels
12-bit DAC		2
Number of channels		2
CPU frequency		32 MHz
Operating voltage		1.8 V to 3.6 V (down to 1.65 V at power-down) with BOR option 1.65 V to 3.6 V without BOR option
Operating temperatures		Ambient temperatures: –40 to +85 °C Junction temperature: –40 to + 105 °C

BLOCK DIAGRAM:



AF = Alternate Function function on I/O port pin.

Internal Memory Map

Bank	Address Range	Component
7	0xE010 0000 - 0xFFFF FFFF	Cortex-M3 Internal Peripherals
6	0xC000 0000 - 0xE010 0000	Cortex-M3 Internal Peripherals
5	0xA000 0000 - 0xC000 0000	Cortex-M3 Internal Peripherals
4	0x8000 0000 - 0xA000 0000	Cortex-M3 Internal Peripherals
3	0x6000 0000 - 0x8000 0000	Cortex-M3 Internal Peripherals
2	0x4000 0000 - 0x6000 0000	Peripherals
1	0x2000 0000 - 0x4000 0000	SRAM
0	0x0000 0000 - 0x2000 0000	SRAM

APB Memory Space

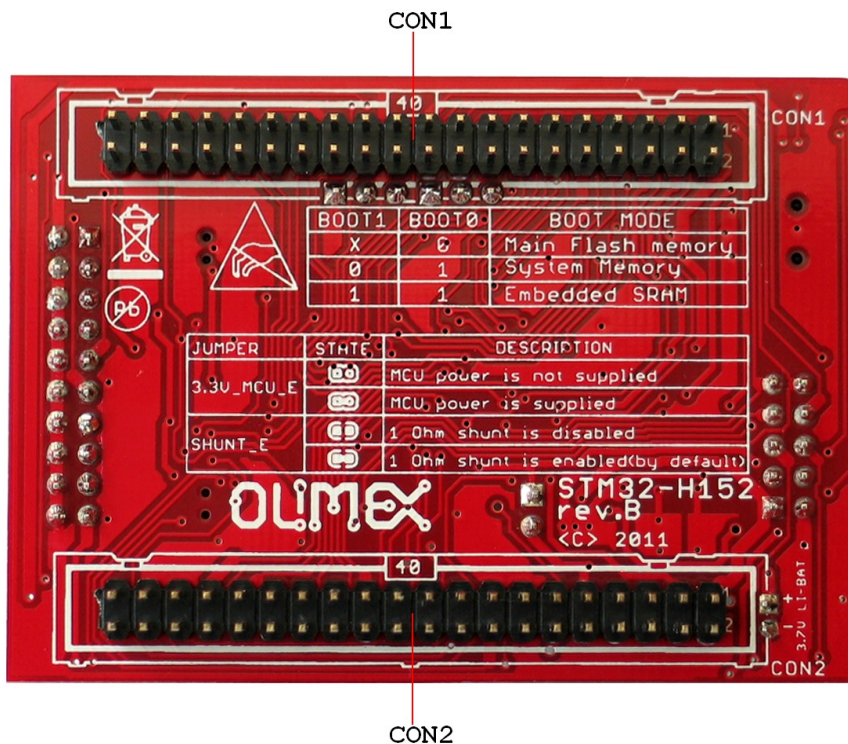
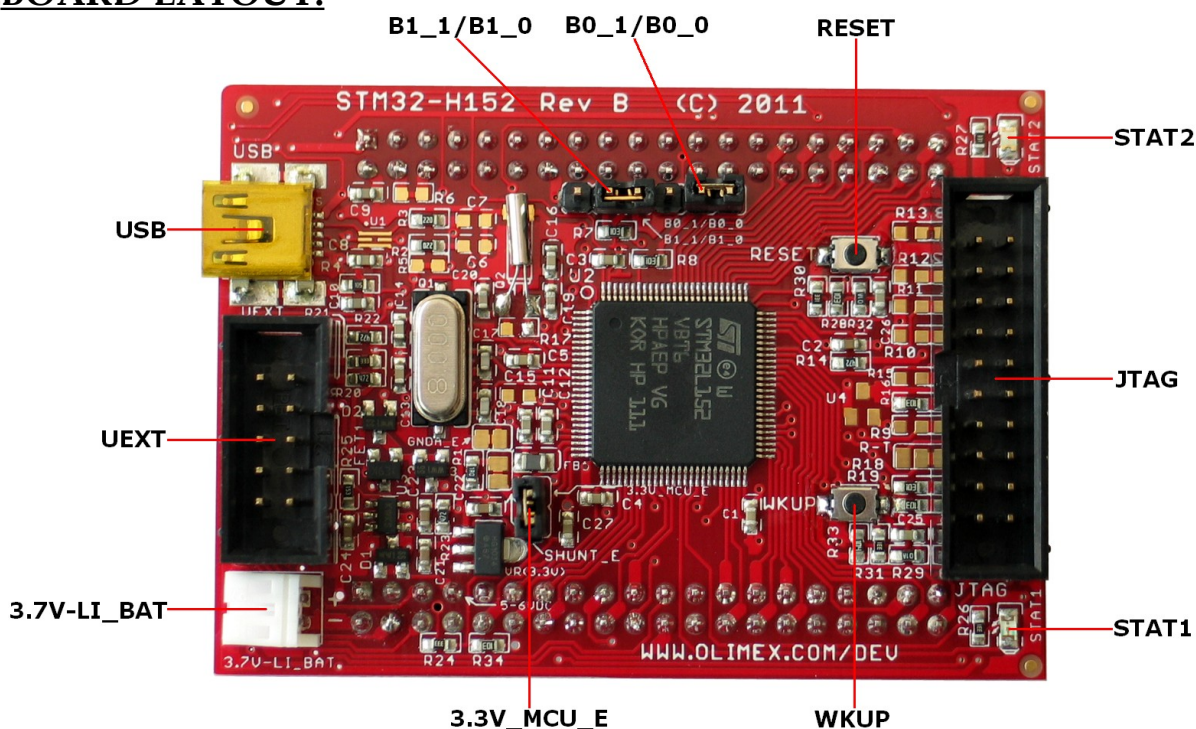
Segment	Address Range	Component
15	0xFFFF FFFF	reserved
14	0xE010 0000	reserved
13	0x6000 0000	reserved
12	0x4002 6400	DMA
11	0x4002 6000	reserved
10	0x4002 4000	Flash Interface
9	0x4002 3C00	RCC
8	0x4002 3800	reserved
7	0x4002 3400	CRC
6	0x4002 3000	reserved
5	0x4002 1800	Port H
4	0x4002 1400	Port E
3	0x4002 1000	Port D
2	0x4002 0C00	Port C
1	0x4002 0800	Port B
0	0x4002 0400	Port A
15	0x4002 0000	reserved
14	0x4001 3C00	USART1
13	0x4001 3800	reserved
12	0x4001 3400	SPI1
11	0x4001 3000	reserved
10	0x4001 2800	ADC
9	0x4001 2400	reserved
8	0x4001 1400	TIM11
7	0x4001 1000	TIM10
6	0x4001 0C00	TIM9
5	0x4001 0800	EXTI
4	0x4001 0400	SYSCFG
3	0x4001 0000	reserved
2	0x4000 7C00	COMP + RI
1	0x4000 7800	reserved
0	0x4000 7400	DAC1 & 2
15	0x4000 7000	PWR
14	0x4000 6200	reserved
13	0x4000 6000	512 byte USB
12	0x4000 5C00	USB Registers
11	0x4000 5800	I2C2
10	0x4000 5400	I2C1
9	0x4000 4C00	reserved
8	0x4000 4800	USART3
7	0x4000 4400	USART2
6	0x4000 3C00	reserved
5	0x4000 3800	SPI2
4	0x4000 3400	reserved
3	0x4000 3000	IWDG
2	0x4000 2C00	WWDG
1	0x4000 2800	RTC
0	0x4000 2400	LCD
15	0x4000 1C00	reserved
14	0x4000 1400	TIM7
13	0x4000 1000	TIM6
12	0x4000 0C00	reserved
11	0x4000 0800	TIM4
10	0x4000 0400	TIM3
9	0x4000 0000	TIM2

Detailed Internal Memory Layout

Address Range	Component
0x1FF8 001F	Option Bytes
0x1FF8 0000	reserved
0x1FF0 0FFF	System memory
0x1FF0 0000	reserved
0x0808 0FFF	Data EEPROM
0x0808 0000	reserved
0x0801 0FFF	Flash memory
0x0800 0000	Aligned to Flash or system memory depending on BOOT pins

[illegible]

BOARD LAYOUT:



POWER SUPPLY CIRCUIT:

STM32-H152 can take power from:

-USB - +5V_USB.

-J-link - +5V_JTAG.

-Battery connector - +3.7V lithium battery.

-Extension connector CON2 pin 7 (VIN) - (5÷6) VDC.

The programmed board power consumption is about 30 mA.

Note that if the battery is connected to 3,7V-Li BAT connector and some of other power sources(USB, JTAG, CON2 pin 7 (VIN)) are present and the battery is discharged then the battery will be charged until the charge complete.

RESET CIRCUIT:

STM32-H152 reset circuit includes R28 (10k), R30 (330Ω), R32 (100Ω/1%), C26 (100nF), STM32L152VBT6 pin 14 (NRST) and RESET button.

CLOCK CIRCUIT:

Quartz crystal (Q1) 8 MHz is connected to STM32L152VBT6 pin 12 (OSC_IN/PH0) and pin 13 (OSC_OUT/PH1).

Quartz crystal (Q2) 32.768 kHz is connected to STM32L152VBT6 pin 8 (PC14/OSC32_IN) and pin 9 (PC15/OSC32_OUT).

JUMPER DESCRIPTION:

R-T



When this jumper is closed, RST and TRST are connected. When this jumper is opened, RST and TRST are separated.

Default state is opened.

SHUNT_E



When this jumper is closed, 1 Ohm shunt is enabled. When this jumper is opened, 1 Ohm shunt is disabled.

Default state is closed.

GNDA_E



When this jumper is closed, the board analog ground is enabled. When this jumper is opened, the board analog ground is disabled.

Default state is closed.

3.3V_MCU_E



When this jumper is closed, STM32L152VBT6 power supply is enabled. When this jumper is opened, STM32L152VBT6 power supply is disabled.

Default state is closed.

B0_1/B0_0



This jumper, when is in position B0_1 – connects STM32L152VBT6 pin 94 (BOOT0) via R8 (10k) to 3.3V, when the jumper is in position B0_0 – connects STM32L152VBT6 pin 94 (BOOT0) via R8 (10k) to GND.

Default state is closed in position B0_0.

B1_1/B1_0



This jumper, when is in position B1_1 – connects STM32L152VBT6 pin 37 (PB2/BOOT1) via R7 (10k) to 3.3V, when the jumper is in position B1_0 – connects STM32L152VBT6 pin 37 (PB2/BOOT1) via R7 (10k) to GND.

Default state is closed in position B1_0.

The boot mode is selected depending on jumpers B0_1/B0_0 and B1_1/B1_0 configuration:

- When B0_1/B0_0 is closed in position B0_0 and B1_1/B1_0 position doesn't matter the boot mode is Main Flash Memory.
- When B0_1/B0_0 is closed in position B0_1 and B1_1/B1_0 is closed in position B1_0 – the boot mode is System Memory.
- When B0_1/B0_0 is closed in position B0_1 and B1_1/B1_0 is closed in position B1_1 – the boot mode is Embedded SRAM.

INPUT/OUTPUT:

Status led (red), with name **STAT1** is connected to STM32L152VBT6 pin 41 (PF10).

Status led (green), with name **STAT2** is connected to STM32L152VBT6 pin 42 (PF11).

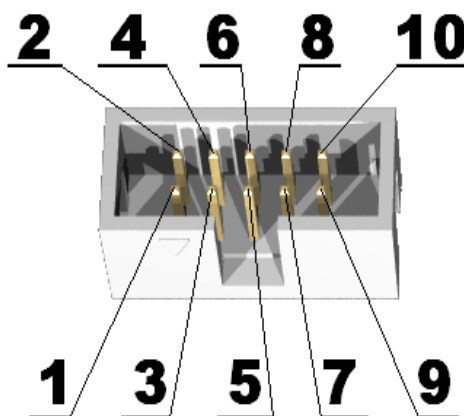
User button with name **WKUP** is connected to STM32L152VBT6 pin 23 (PA0/WKUP1).

User button with name **RESET** is connected to STM32L152VBT6 pin 14 (NRST).

EXTERNAL CONNECTORS DESCRIPTION:

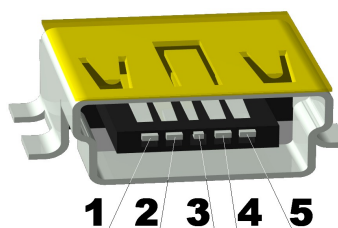
UEXT:

Pin #	Signal Name
1	VCC (3.3V)
2	GND
3	USART2_TX
4	USART2_RX
5	I2C1_SCL
6	I2C1_SDA
7	SPI1_MISO
8	SPI1_MOSI
9	SPI1_SCK
10	SPI1_NSS



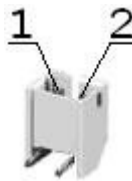
USB:

Pin #	Signal Name
1	+5V_USB
2	USB_DM
3	USB_DP
4	Not Connected
5	GND

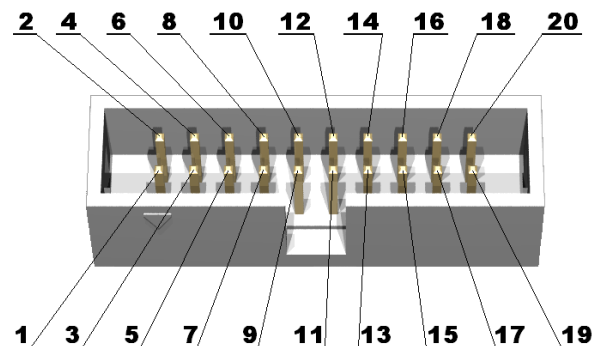


3.7V-LI BAT:

Pin #	Signal Name
1	VBAT
2	GND

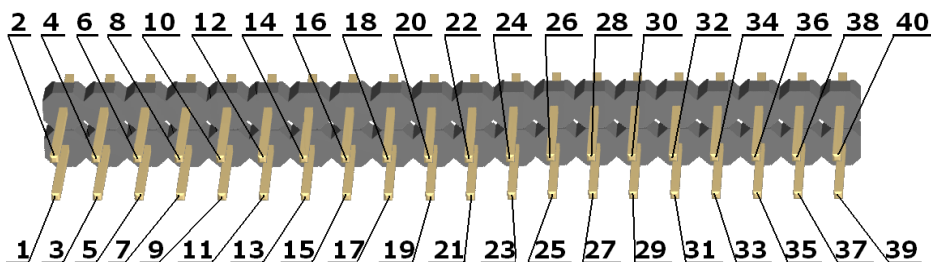


JTAG:



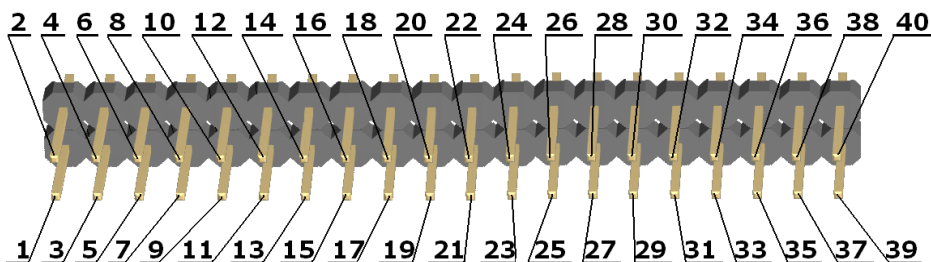
Pin #	Signal Name	Pin #	Signal Name
1	VCC (3.3V)	2	VCC (3.3V)
3	TRST	4	GND
5	TDI	6	GND
7	TMS/SWDIO	8	GND
9	TCK/SWCLK	10	GND
11	Via 10k to GND	12	GND
13	TDO	14	GND
15	RST	16	GND
17	Via 10k to GND	18	GND
19	+5V_JTAG	20	GND

CON1:



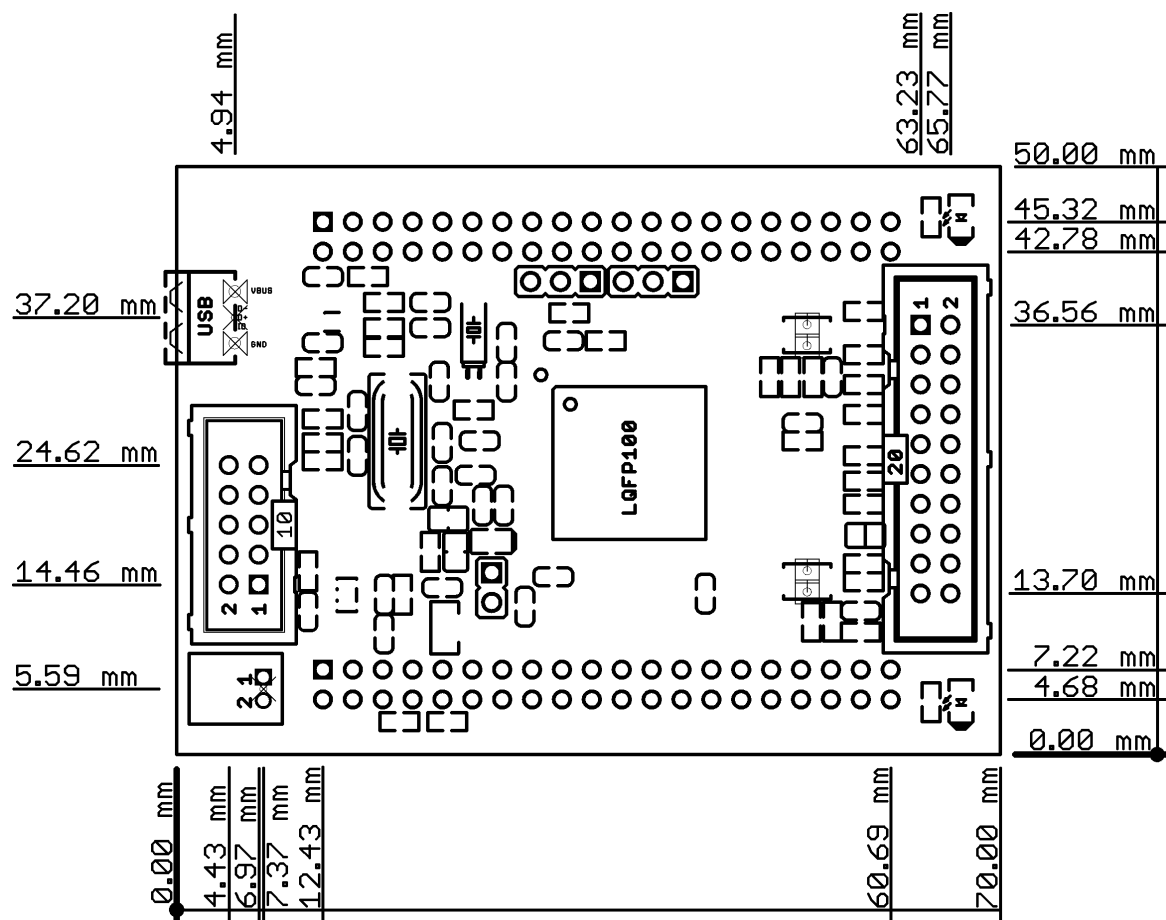
Pin #	Signal Name	Pin #	Signal Name
1	VCC (3.3V)	2	GND
3	VDDA	4	AGND
5	VREF+	6	VREF-
7	PE0	8	PE1
9	PE2	10	PE3
11	PE4	12	PE5
13	PE6	14	PE7
15	PE8	16	PE9
17	SPI1_NSS	18	SPI1_SCK
19	SPI1_MISO	20	SPI1_MOSI
21	PD15	22	PD14
23	PD13	24	PD12
25	PD11	26	PD10
27	PD9	28	PD8
29	PD7	30	USART2_RX
31	USART2_TX	32	PD4
33	PD3	34	PD2
35	PD1	36	PD0
37	PC13	38	PC12
39	PC11	40	PC10

CON2:



Pin #	Signal Name	Pin #	Signal Name
1	VCC (3.3V)	2	GND
3	VDDA	4	AGND
5	VREF+	6	VREF-
7	VIN	8	GND
9	VCC (3.3V)	10	PA3
11	PA4	12	PA5
13	PA6	14	PA7
15	PA8	16	PA9
17	PA10	18	PB0
19	PB1	20	PB5
21	I2C1_SCL	22	I2C1_SDA
23	PB8	24	PB9
25	PB10	26	PB11
27	PB12	28	PB13
29	PB14	30	PB15
31	PC0	32	PC1
33	PC2	34	PC3
35	PC4	36	PC5
37	PC6	38	PC7
39	PC8	40	PC9

MECHANICAL DIMENSIONS:



AVAILABLE DEMO SOFTWARE:

- [Demo software USB mouse and Blinking LED](#) for EW-ARM
- [Setup GCC+Eclipse for STM32 on MAC](#)

ORDER CODE:

STM32-H152 - assembled and tested board

How to order?

You can order to us directly or by any of our distributors.
Check our web www.olimex.com/dev for more info.

Revision history:

Boar's revision:	Rev. B, June 2011
Manual's revision:	Rev. Initial, September 2011

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