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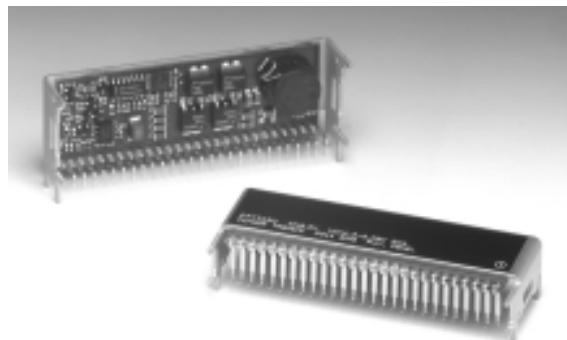
PT7711—5V

20-A Programmable
Integrated Switching Regulator



SLTS087A

(Revised 6/27/2002)



Features

- 5V Input Voltage
- 20-A Output Current
- 5-bit Programmable Output: 1.3V to 3.5V
- High Efficiency (93%)
- Differential Remote Sense
- Over-Current Protection
- 27-Pin Space-Saving Package
- Solderable Copper Case
- Compatible with PT7745 20A "Current Booster"

Description

The PT7711 Excalibur™ power module is a 20-A integrated switching regulator (ISR) housed a 27-pin space-saving copper package. Operating from a +5V input bus, the PT7711 produces a tightly regulated output voltage that is programmable over the range, 1.3V to 3.5V. The output voltage is selected via 5-bit input code, which is compatible with the VRM specifications defined by Intel®.

This regulator is most suitable for microprocessor or DSP applications requiring core or I/O logic supply voltages as low as 1.3V.

The PT7711 regulator incorporates output short-circuit protection, and a differential remote sense to compensate for voltage drop between the regulator and load.

Ordering Information

PT7711□ = 1.3 to 3.5 Volts

PT7745□ = 20-A Booster *

* Consult the related application note for information on current booster operation.

PT Series Suffix (PT1234x)

Case/Pin Configuration	Order Suffix	Package Code †
Vertical	N	(ENE)
Horizontal	A	(ENF)
SMD	C	(ENG)

† Previously known as package styles 1420 & 1430

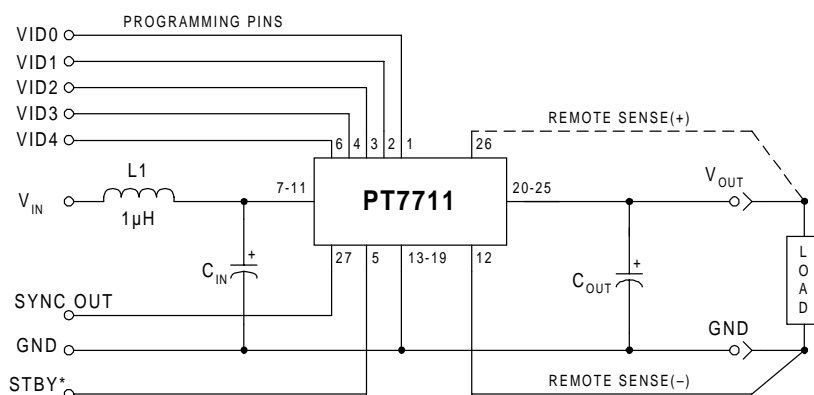
(Reference the applicable package code drawing for the dimensions and PC board layout)

Pin-Out Information

Pin	Function	Pin	Function
1	VID0	15	GND
2	VID1	16	GND
3	VID2	17	GND
4	VID3	18	GND
5	STBY*	19	GND
6	VID4	20	V _{out}
7	V _{in}	21	V _{out}
8	V _{in}	22	V _{out}
9	V _{in}	23	V _{out}
10	V _{in}	24	V _{out}
11	V _{in}	25	V _{out}
12	Rem Sense Gnd	26	Rem Sense V _{out}
13	GND	27	Sync Out
14	GND		

For STBY* pin: open = output enabled
ground = output disabled.

Standard Application



C_{in} = Required 1500µF electrolytic
C_{out} = Required 330µF electrolytic
L₁ = Optional 1µH input choke

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Programming Information

VID3	VID2	VID1	VID0	VID4=1 Vout	VID4=0 Vout
1	1	1	1	2.00V	1.30V
1	1	1	0	2.10V	1.35V
1	1	0	1	2.20V	1.40V
1	1	0	0	2.30V	1.45V
1	0	1	1	2.40V	1.50V
1	0	1	0	2.50V	1.55V
1	0	0	1	2.60V	1.60V
1	0	0	0	2.70V	1.65V
0	1	1	1	2.80V	1.70V
0	1	1	0	2.90V	1.75V
0	1	0	1	3.00V	1.80V
0	1	0	0	3.10V	1.85V
0	0	1	1	3.20V	1.90V
0	0	1	0	3.30V	1.95V
0	0	0	1	3.40V	2.00V
0	0	0	0	3.50V	2.05V

Logic 0 = Pin 12 potential (Rem Sense Gnd)

Logic 1 = Open circuit (no pull-up resistors)

VID 3 & VID4 must not be changed while the unit is operating.

PT7710 Product Family Comparison

Product	Input Voltage	Description	Adjust Method	Output Range
PT7711	5V	VID	5-Bit	1.3V–3.5V
PT7712	3.3V	VID	4-Bit	1.3V–2.05V
PT7713	3.3V	VID	4-Bit	1.8V–2.55V
PT7714	3.3V	VID	5-Bit	0.8V–1.575V
PT7715	5V	VID	5-Bit	1.075V–1.85V
PT7716	3.3V	VID	5-Bit	1.075V–1.85V
PT7744	3.3V	Booster	—	—
PT7745	5V	Booster	—	—

Specifications (Unless otherwise stated, $T_a = 25^\circ\text{C}$, $V_{in} = 5\text{V}$, $C_{in} = 1,500\mu\text{F}$, $C_{out} = 330\mu\text{F}$, $V_o = 3.3\text{V}$, and $I_o = I_{o,max}$)

Characteristics	Symbols	Conditions	PT7711			Units
			Min	Typ	Max	
Output Current	I_o	$T_a = +25^\circ\text{C}$, Natural convection $T_a = +60^\circ\text{C}$, 200 LFM, pkg N	0.1 (1) 0.1 (1)	—	20 20	A
Input Voltage Range	V_{in}	Over I_o range	4.5	—	5.5	V
Set-Point Voltage Tolerance	$V_o \text{ tol}$	All output voltages	—	± 10	± 25 (2)	mV
Temperature Variation	Reg_{temp}	$-40^\circ\text{C} \leq T_a \leq +85^\circ\text{C}$, $I_o = I_{o,min}$	—	± 0.75	—	% V_o
Line Regulation	Reg_{line}	Over V_{in} range	—	± 5	—	mV
Load Regulation	Reg_{load}	Over I_o range	—	± 5	—	mV
Total Output Voltage Variation	$\Delta V_o \text{ tol}$	Includes set-point, line, load, $-40^\circ\text{C} \leq T_a \leq +85^\circ\text{C}$	—	± 1	± 3	% V_o
Efficiency	η	$I_o = 10\text{A}$	$V_o = 3.3\text{V}$	—	93	%
			$V_o = 2.5\text{V}$	—	90	
			$V_o = 1.8\text{V}$	—	87	
		$I_o = 20\text{A}$	$V_o = 3.3\text{V}$	—	90	%
			$V_o = 2.5\text{V}$	—	85	
			$V_o = 1.8\text{V}$	—	80	
V_o Ripple (pk-pk)	V_r	20MHz bandwidth	—	35	—	mV _{pp}
Transient Response	t_{tr}	1A/ μs load step, 50% and 100% $I_{o,max}$	—	50	—	μSec
	V_{os}	V_o over/undershoot	—	± 100	—	mV
Over-Current Threshold	I_{TRIP}	Reset followed by auto-recovery	—	32	—	A
Switching Frequency	f_s	Over V_{in} range	300	350	400	kHz
STBY* Input Requirements	V_{IH}	Referenced to GND	2.0	—	Open (3)	V
	V_{IL}		-0.2	—	0.8	
	I_{IL}	Pin 5 to GND	—	0.3	—	mA
Standby Input Current	$I_{in \text{ standby}}$	Pin 5 to GND	—	30	—	mA
External Capacitance	C_{out}		330 (4)	—	15,000	μF
Operating Temperature Range	T_a	Over V_{in} Range	-40	—	+85 (5)	$^\circ\text{C}$
Storage Temperature	T_s	—	-40	—	+125	$^\circ\text{C}$
Mechanical Shock	—	Per Mil-STD-883D, Method 2002.3 1 msec, Half Sine, mounted to a fixture	—	500	—	G's
Mechanical Vibration	—	Mil-STD-883D, Method 2007.2 20-2000 Hz	Suffixes A, N	20 (6)	—	G's
			Suffix C	15 (6)	—	
Weight	—	Vertical/Horizontal	—	36	—	grams
Flammability	—	Materials meet UL 94V-0	—	—	—	—

Notes: (1) ISR—will operate down to no load with reduced specifications.

(2) If the remote sense ground is not used, pin 12 must be connected to pin 13 for optimum output voltage accuracy.

(3) The STBY* control (pin 5) has an internal pull-up. If it is left open-circuit, the module will operate when input power is applied. A low-leakage ($<1\mu\text{A}$) MOSFET must be used to control this pin. The open-circuit voltage may be as high as V_{in} .

(4) For operation below 0°C , C_{in} and C_{out} must have stable characteristics. Use either low ESR tantalum or Oscon® capacitors.

(5) See safe Operating Area curves or consult factory for the appropriate derating.

(6) The case pins on the through-hole package types (suffixes N & A) must be soldered. For more information see the applicable package outline drawing.

External Capacitors: The regulator require a minimum output capacitance of $330\mu\text{F}$ for proper operation. An input capacitance of $1500\mu\text{F}$ is also required. This must be rated for a minimum of 1.4Arms of ripple current. For transient or dynamic load applications, additional capacitance may be required. For further information refer to the application note regarding capacitor selection for this product.

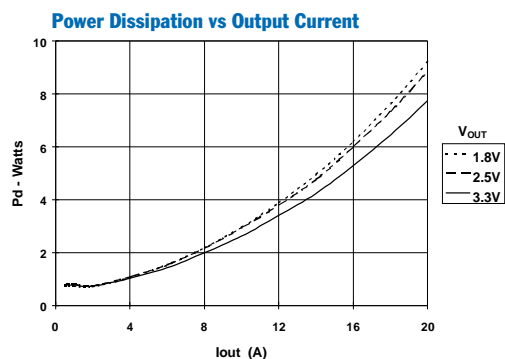
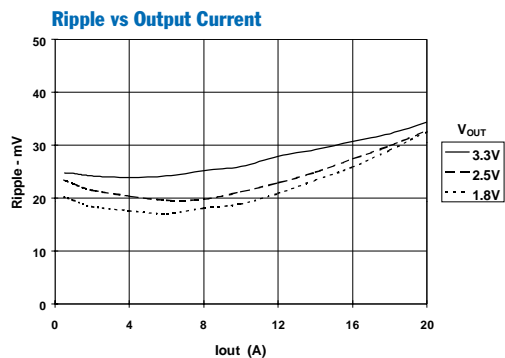
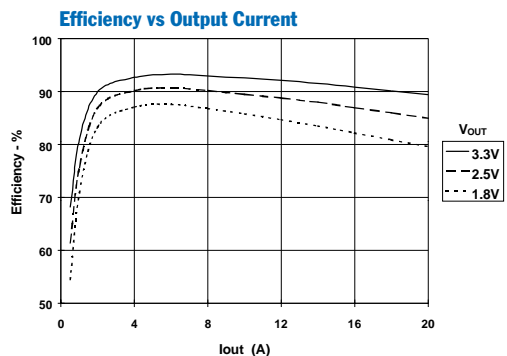
Input Filter: An input filter inductor is optional for most applications. The inductor must be sized to handle 20ADC with a typical value of $1\mu\text{H}$.

PT7711—5V

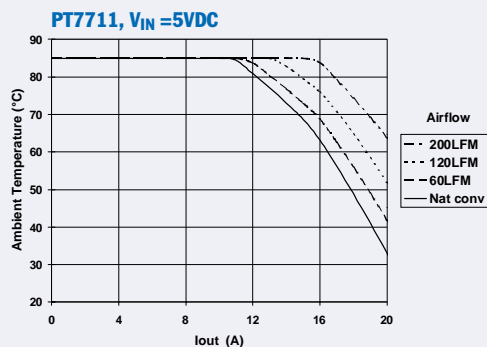
Typical Characteristics

20-A Programmable
Integrated Switching Regulator

Performance Characteristics, $V_{IN} = 5V$ (See Note A)



Safe Operating Area Curves (See Note B)



Note A: All characteristic data in the above graphs has been developed from actual products tested at 25°C. This data is considered typical for the ISR.

Note B: SOA curves represent operating conditions at which internal components are at or below manufacturer's maximum rated operating temperatures.

Application Notes

PT7710 Family, PT7744/PT7745

Capacitor Recommendations for the PT7710 Series of Regulators and Current Boosters

Input Capacitors

The recommended input capacitance is determined by the 1.4 ampere minimum ripple current rating and 1500 μ F minimum capacitance. Capacitors listed below must be rated for a minimum of twice (2 \times) the input voltage with +5V operation. Ripple current and $\leq 100\text{m}\Omega$ Equivalent Series Resistance (ESR) values are the major considerations along with temperature when selecting the proper capacitor.

Output Capacitors

The minimum required output capacitance is 330 μ F with a maximum ESR less than or equal to 100m Ω . Failure to observe this requirement may lead to regulator instability or oscillation. Electrolytic capacitors have poor ripple performance at frequencies greater than 400kHz, but excellent low frequency transient response. Above the ripple frequency ceramic decoupling capacitors are necessary to improve the transient response and reduce any microprocessor high frequency noise components apparent during higher current excursions. Preferred low ESR type capacitor part numbers are identified in the Table 1-1 below.

Tantalum Characteristics

Tantalum capacitors with a minimum 10-V rating are recommended on the output bus, but only the AVX TPS Series, Sprague 594/595 Series, or Kemet T495/T510 Series. These AVX, Sprague, and Kemet capacitors are specified over other types due to their higher surge current, excellent power dissipation and ripple current ratings. As a caution, the TAJ Series by AVX is not recommended. This series exhibits considerably higher ESR, reduced power dissipation and lower ripple current capability. The TAJ series is also less reliable compared to the TPS series when determining power dissipation capability.

Capacitor Table

Table 1-1 identifies the characteristics of capacitors from a number of vendors with acceptable ESR and ripple current (rms) ratings. The suggested minimum quantities per regulator for both the input and output buses are identified.

This is not an extensive capacitor list. Capacitors from other vendors are available with comparable specifications. The RMS ripple current rating and ESR (Equivalent Series Resistance at 100kHz) are the critical parameters necessary to insure both optimum regulator performance and long capacitor life.

Table 1-1 Capacitors Characteristic Data

Capacitor Vendor/ Series	Capacitor Characteristics					Quantity		Vendor Number
	Working Voltage	Value(μ F)	(ESR) Equivalent Series Resistance	105°C Maximum Ripple Current(Irms)	Physical Size(mm)	Input Bus	Output Bus	
Panasonic FC (SMT)	16V	2200	0.038 Ω	2000mA	18x16.5	1	1	EEVFC1C222N
	35V	330	0.065 Ω	1205mA	12.5x16.5		1	EEVFC1V331LQ
	FC (Radial)	10V	0.090 Ω	755mA	10x12.5		1	EEUFC1A561
United Chemi -Con LFZ Series	16V	1800	0.032 Ω	2000mA	18x15	1	1	EEUFC1C182S
	25V	330	0.090 Ω	760mA	10x12.5		1	LXZ25VB331M10X12LL
	16V	1500	0.038 Ω	1660mA	12.5x20	1	1	LXZ16VB222M12X20LL
Nichicon PL Series PM Series	16V	470	0.090 Ω	760mA	10x12.5		1	LXZ16VB471M10X12LL
	10V	680	0.090 Ω	770mA	10x15		1	UPL1A681MHH6
	10V	1800	0.044 Ω	1420mA	16x15	1	1	UPL1A182MHH6
Oscon SS (Radial) SVP (SMT)	25V	330	0.095 Ω	750mA	10x15		1	UPL1E331MPH6
	10V	330	0.025 $\Omega \pm 4 = 0.006\Omega$	>9800mA	10x10.5	4	N/R	10SS330M
AVX Tantalum TPS- Series	10V	330	0.02 $\Omega \pm 4 = 0.005\Omega$	>9800mA	9x8.3	4	(Note)	10SVP330M
	10V	330	0.1 $\Omega \pm 5 = 0.02\Omega$	3500mA	7.3Lx	5	1	TPSV337M010R0100
Sprague Tantalum 595D/594D	10V	330	0.06 Ω	1826mA	4.3Wx	5	1	TPSV337M010R0060
	10V	680	0.045 $\Omega \pm 4 = 0.011\Omega$	>4500mA	4.1H			
Kemet Tantalum T510/T495 Series	10V	330	0.045 $\Omega \pm 4 = 0.011\Omega$	>1660mA	7.3L x 5.7W x 4.0H	5	1	594D337X0010R2T
	10V	220	0.09 Ω			2	1	595D687X0010R2T (Surface Mount)
Sanyo Poscap TPB	10V	330	0.035 Ω	2000mA	4.3Wx7.3L x4.0H	5	1	510X337M010AS
	10V	220	0.07 $\Omega \pm 2 = 0.035\Omega$	>2000mA		6	2	T495X227M010AS (Surface Mount)
Sanyo Poscap TPB	10V	220	0.04 Ω	3000mA	7.2L x 4.3W x 3.1H	6	2	10TPB220M (Surface Mount)

Note: (N/R) is not recommended for this application, due to extremely low Equivalent Series Resistance (ESR)

PT7710 Series, PT7744, PT7745

For technical support and more information, see inside back cover or visit www.ti.com

Application Notes

PT7710 Family

Using the Standby Function on the PT7710 Excalibur™ Series of 20-A ISRs

For applications requiring On/Off control of the output voltage, the PT7710 family of Excalibur ISRs incorporate a standby function. This feature may be used for power-up/shutdown sequencing, or to change the output voltage while input power is applied. *See related notes:* "Pin-Coded Output Voltage Adjustment of the PT7710 Excalibur™ Series of 20-A ISRs."

The standby function is provided by the *STBY** control, pin 5. If pin 5 is left open-circuit the regulator operates normally, providing a regulated output whenever a valid supply voltage is applied to V_{in} (pins 7-11) with respect to GND (pins 13-19). Connecting pin 5 to ground¹ will set the regulator output to zero volts². This places the regulator in standby mode, and reduces the input current to typically 30mA (60mA max). If a ground signal is applied to pin 5 prior to power-up, the regulator output will be held at zero volts during the period that input power is applied.

The standby input must be controlled with an open-collector (or open-drain) discrete transistor (See Figure 1). Table 3-1 gives the input requirements.

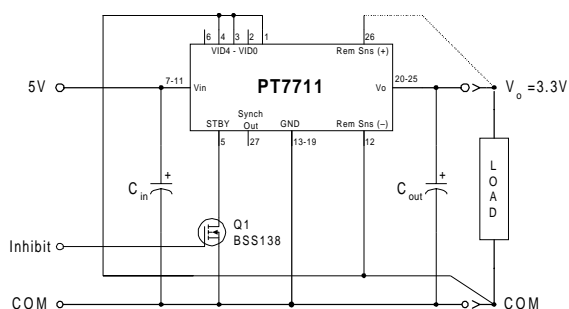
Table 3-1 Standby Control Input Requirements^(1,2)

Parameter	Min	Typ	Max
Enable	2V		Open Cct. ¹
Disable	-0.2V		0.8V
I_{stby} (low)			0.5mA
V_{stby} (o/c)		V_{in}	

Notes:

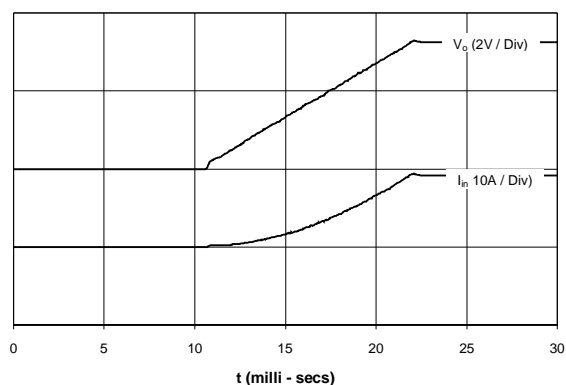
- The Standby input on the PT7710 regulator family must be controlled using an open-collector (or open-drain) discrete transistor. *Do Not* use a pull-up resistor. The control input has an open-circuit voltage equal to V_{in} . To set the regulator output to zero, the control pin must be "pulled" to less than 0.8Vdc with a 0.5mA sink to ground.
- When placed in the standby mode, the regulator output discharges the output capacitance with a low impedance to ground.
- The turn-off time of Q_1 , or rise time of the standby input is not critical with the PT7710 family. Turning Q_1 off slowly, over periods up to 100ms, will not affect regulator operation. However, a slow turn-off time will increase both the initial delay and rise-time of the output voltage.

Figure 3-1



Turn-On Time: Turning Q_1 in Figure 3-1 off, removes the low-voltage signal at pin 5 and enables the output. After a delay of approximately 10ms, the output voltage of a PT771x regulator rises to full regulation within 30ms³. Figure 3-2 shows the typical output voltage waveform of a PT7711 following the turn-off of Q_1 at time $t=0$ secs. The output voltage in Figure 3-1 is set to 3.3V by connecting VID0 (pin 1), VID2 (pin 3), and VID3 (pin 4) to the 'Rem Sense Gnd' (pin 12)*. The waveform was measured with a +5V input source voltage, and 10-A resistive load.

Figure 3-2



* Consult the data sheet for details on other VID codes.

Application Notes

PT7710 Family

Pin-Coded Output Voltage Adjustment of the PT7710 Excalibur™ Series of 20-A ISRs

The PT7710 Excalibur™ family of converters incorporate a pin-coded control to adjust the output voltage. This feature uses the control pins VID0–VID4 (pins 1–6). When the control pins are left open-circuit, the ISR output regulates at its default output voltage. Each control pin is internally connected to a precision resistor, which when grounded applies a weighted change to the output voltage. By selectively grounding VID0–VID4, the output voltage of these ISRs can be programmed in incremental steps over the specified output voltage range. The program codes and output voltage range offered by these ISRs are compatible with the Voltage ID specifications defined by Intel Corporation for VRMs (voltage regulator modules). The codes are used by both the Pentium® and Athlon® microprocessors. Refer to Figure 4-1 for the connection schematic, and the respective device Data Sheet for the programming code information.

Notes:

- The programming convention is as follows:-
Logic 0: Connect to pin 12 (Remote Sense Ground).
Logic 1: Open circuit/open drain (See notes 2, & 4)
- Do not connect pull-up resistors to the voltage programming pins.
- Use pin 12 (Remote Sense Ground) as the logic “0” reference. While the regular ground (pins 13-19) can also be used for programming, doing so will degrade the load regulation of the product. If the remote sense ground is not used, pin 12 must be connected to pin 13 for optimum output voltage accuracy.

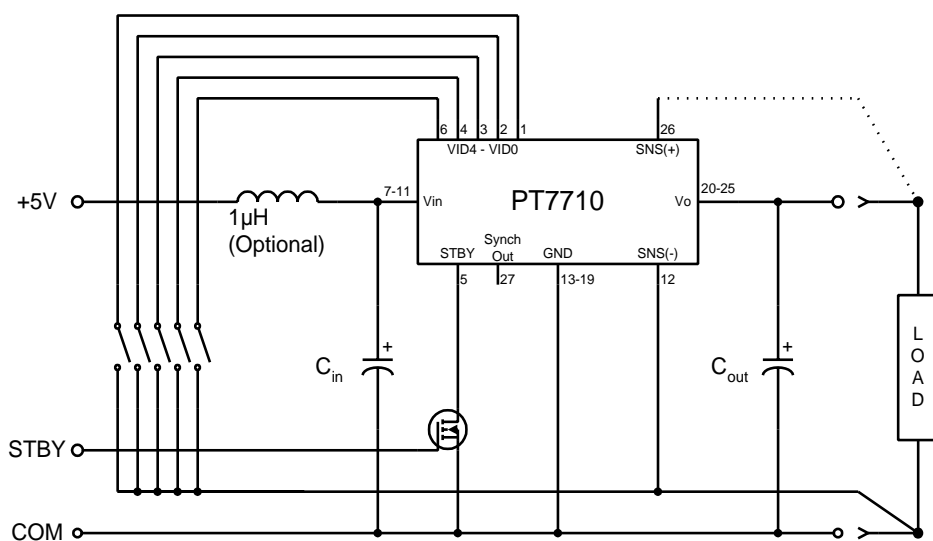
- If active devices are used to ground the voltage control pins, low-level open drain MOSFET devices should be used over bipolar transistors. The inherent $V_{ce(sat)}$ in bipolar devices introduces errors in the device's internal divider network. Discrete transistors such as the BSS138, 2N7002, or IRLML2402 are examples of appropriate devices.

Active Voltage Programming:

Special precautions should be taken when making changes to the output voltage program code while the unit is powered. This activity can induce current transients through the device as a result of the electrolytic output capacitors being either charged or discharged to the new output voltage set-point. The transient current can be minimized by making only incremental changes to the binary code, i.e. one LSB at a time. A minimum of 100µs settling time between each program state is also recommended. Making non-incremental changes to VID3 or VID4 is discouraged.

When active devices are used to program the output voltage, their state should be asserted prior to the input power being applied. An alternative is to pull pin 5 (STBY) control to GND during the application of input voltage, then assert the required program code and release pin 5. The module will then initiate a soft-start power-up to the desired program voltage. For more information on the use of the Standby function, consult the related application note, “Using the Standby Function on the PT7710 Excalibur™ Series of 20-A ISRs.”

Figure 4-1



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