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[Vishay Semiconductor/Diodes Division](#)
[52CPQ030](#)

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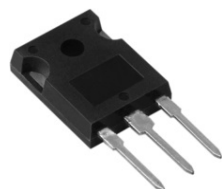
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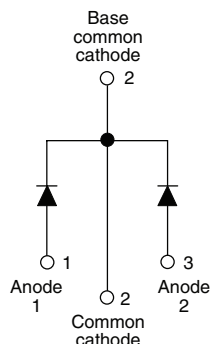
52CPQ030

Vishay High Power Products

Schottky Rectifier, 2 x 25 A



TO-247AC



FEATURES

- 150 °C T_J operation
- Center tap TO-247 package
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

The 52CPQ030 center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

PRODUCT SUMMARY

$I_{F(AV)}$	2 x 25 A
V_R	30 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	50	A
V_{RRM}		30	V
I_{FSM}	$t_p = 5 \mu s$ sine	2180	A
V_F	25 Apk, $T_J = 125^\circ C$ (per leg)	0.38	V
T_J	Range	- 55 to 150	$^\circ C$

VOLTAGE RATINGS

PARAMETER	SYMBOL	52CPQ030	UNITS
Maximum DC reverse voltage	V_R	30	V
Maximum working peak reverse voltage	V_{RWM}		

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current per leg See fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 132^\circ C$, rectangular waveform	25	A
per device			50	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse	2180	
		10 ms sine or 6 ms rect. pulse	600	
Non-repetitive avalanche energy per leg	E_{AS}	$T_J = 25^\circ C$, $I_{AS} = 6 A$, $L = 1.5 mH$	27	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical	6	A

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	25 A	$T_J = 25\text{ }^{\circ}\text{C}$	0.48	V	
		50 A		0.55		
		25 A	$T_J = 125\text{ }^{\circ}\text{C}$	0.38		
		50 A		0.49		
Maximum reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	1.9	mA	
		$T_J = 125\text{ }^{\circ}\text{C}$		450		
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.24	V	
Forward slope resistance	r_t			5.05	mΩ	
Maximum junction capacitance per leg	C_T	$V_R = 5\text{ V}_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		4600	pF	
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body		7.5	nH	
Maximum voltage rate of change	dV/dt	Rated V_R		10000	V/μs	

Note

(1) Pulse width < 300 μs , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation See fig. 4	0.8	°C/W
Maximum thermal resistance, junction to case per package		DC operation	0.4	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.25	
Approximate weight			6	g
			0.21	oz.
Mounting torque	minimum		6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style TO-247AC (JEDEC)	52CPQ030	



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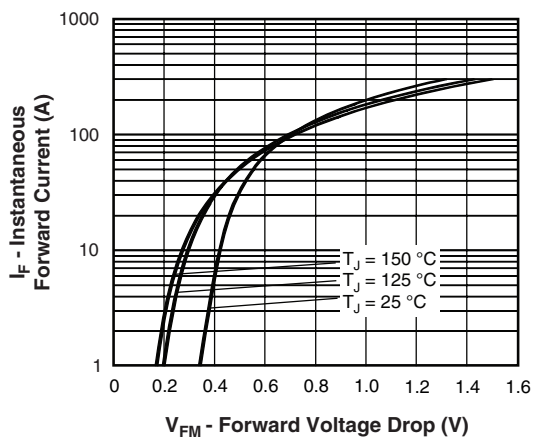


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

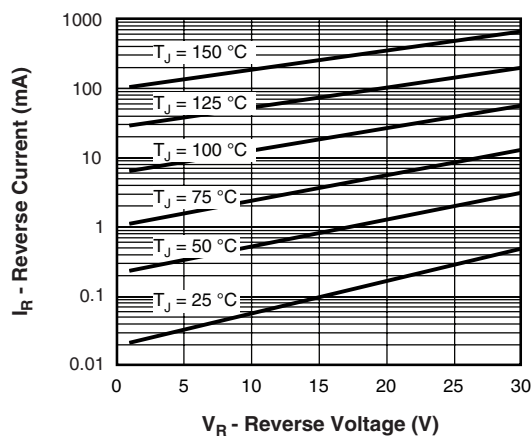


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

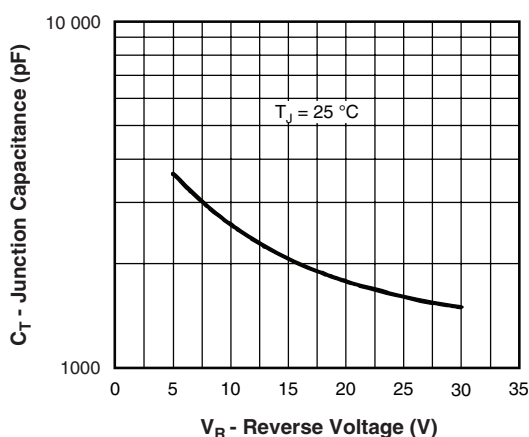


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

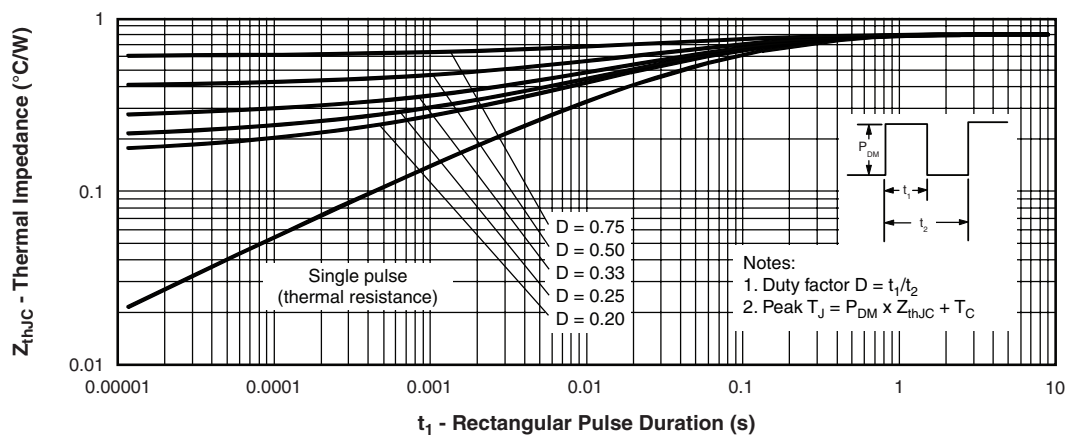


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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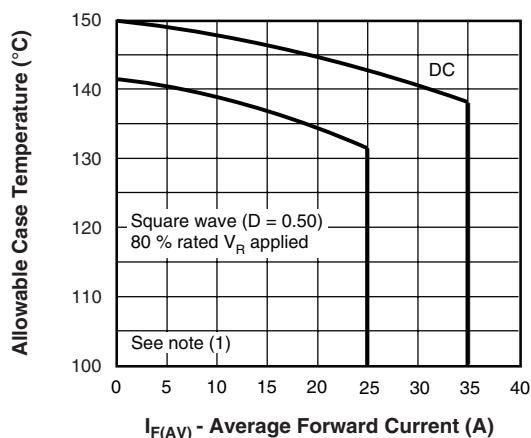


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

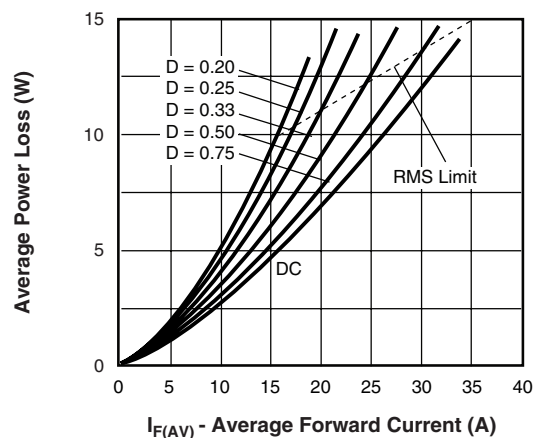


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

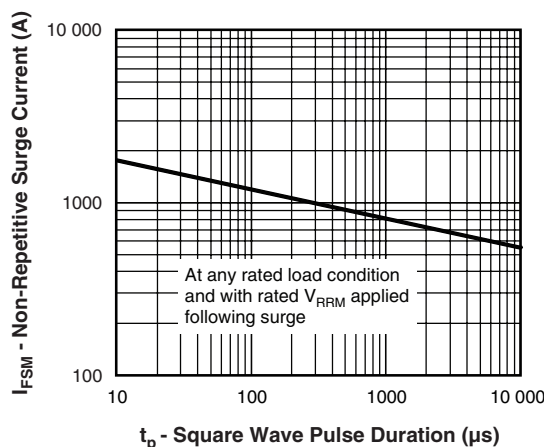


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

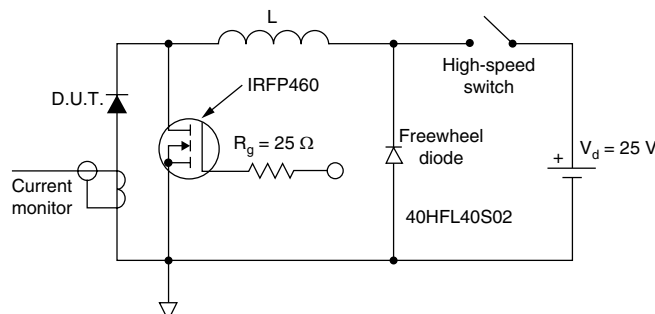


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R



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ORDERING INFORMATION TABLE

Device code	52	C	P	Q	030	-
	①	②	③	④	⑤	⑥

- 1** - Current rating (50 A)
- 2** - Circuit configuration:
C = Common cathode
- 3** - Package:
P = TO-247
- 4** - Schottky "Q" series
- 5** - Voltage code (030 = 30 V)
- 6** -
 - None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95223
Part marking information	http://www.vishay.com/doc?95226



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Vishay

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