

# **Excellent Integrated System Limited**

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Diodes Incorporated D1213A-04V-7

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Datasheet of D1213A-04V-7 - TVS DIODE 3.3VWM 17VC SMD

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com





D1213A-04V

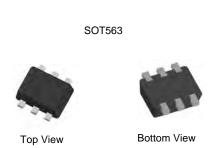
#### 4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

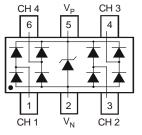
#### **Features**

- IEC 61000-4-2 (ESD): Air ±15kV, Contact ±8kV
- 4 Channels of ESD Protection
- Low Channel Input Capacitance of 0.85pF Typical
- Typically Used at High Speed Ports such as USB 2.0, IEEE1394, Serial ATA, DVI, HDMI, PCI
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper leadframe (Lead Free Plating) Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.003 grams (approximate)





**Device Schematic** 

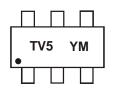
### Ordering Information (Note 4)

Part Number	Case	Packaging
D1213A-04V-7	SOT563	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

### **Marking Information**



TV5 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012)M = Month (ex: 9 = September)

#### Date Code Key

Year	2012	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		Α		В	(		D		Е		F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



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# **Maximum Ratings** ( $@T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Operating Supply Voltage	V <sub>P</sub> - V <sub>N</sub>	6.0	V	-
DC Voltage at any Channel Input	-	$(V_N - 0.5)$ to $(V_P + 0.5)$	V	-
Peak Pulse Current	I <sub>PP</sub>	5.0	Α	8/20μs, Per Fig. 3
ESD Protection – Contact Discharge	V <sub>ESD_Contact</sub>	±8	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	$V_{ESD\_Air}$	±15	kV	Standard IEC 61000-4-2

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	$P_{D}$	380	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	327	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Operating Supply Voltage	$V_P$	ı	3.3	5.5	V	_
Operating Supply Current (Note 6)	lР	ı	-	8.0	μΑ	$(V_P - V_N) = 3.3V$
Channel Leakage Current (Note 6)	I <sub>R</sub>	-	0.1	1.0	μΑ	$V_P = 5V$ , $V_N = 0V$
Reverse breakdown voltage	$V_{BR}$	6.0	-	-	V	$I_R = 1mA$
Clamping Voltage, Positive Transients	V <sub>CL1</sub>	ı	10.0	-	V	I <sub>PP</sub> = 1A (Note 7)
Clamping Voltage, Negative Transients	V <sub>CL2</sub>	ı	-1.7	-	V	I <sub>PP</sub> = -1A (Note 7)
Forward Voltage for Top Diode	$V_{FD1}$	0.60	0.80	0.95	V	I <sub>F</sub> = 8mA, any channel to V <sub>P</sub>
Forward Voltage for Bottom Diode	$V_{FD2}$	0.60	0.80	0.95	V	I <sub>F</sub> = 8mA, V <sub>N</sub> to and channel
Dynamic Resistance	$R_{DYN}$	ı	0.9	-	Ω	I <sub>PP</sub> = 1A (Note 7)
Channel Input Capacitance	Ст	-	0.85	1.2	pF	$V_{IN} = 1.65V, V_P = 3.3V,$ $V_N = 0V, f = 1MHz$

#### Notes:

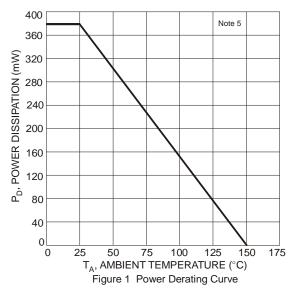
- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an  $8x20\mu s$  peak pulse current (I $_{pp}$ ) waveform. 8. Measured from any channel to  $V_N$
- 9. Measured from V<sub>P</sub> to V<sub>N</sub>.
- 10. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: http://www.diodes.com/destools/appnote\_dnote.html.

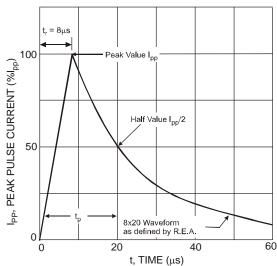
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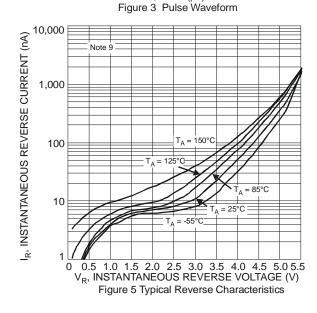
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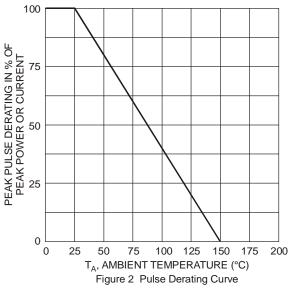


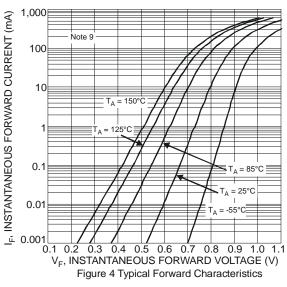
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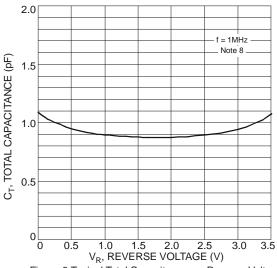


Figure 6 Typical Total Capacitance vs. Reverse Voltage

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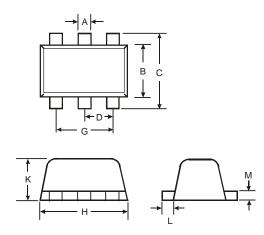
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# **Package Outline Dimensions**

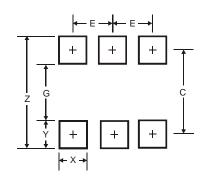
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT563						
Dim	Min	Max	Тур			
Α	0.15	0.30	0.20			
В	1.10	1.25	1.20			
С	1.55	1.70	1.60			
D			0.50			
G	0.90	1.10	1.00			
Н	1.50	1.70	1.60			
K	0.55	0.60	0.60			
L	0.10	0.30	0.20			
М	0.10	0.18	0.11			
All Dimensions in mm						

### **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
С	1.7
E	0.5



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D1213A-04V

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  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
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