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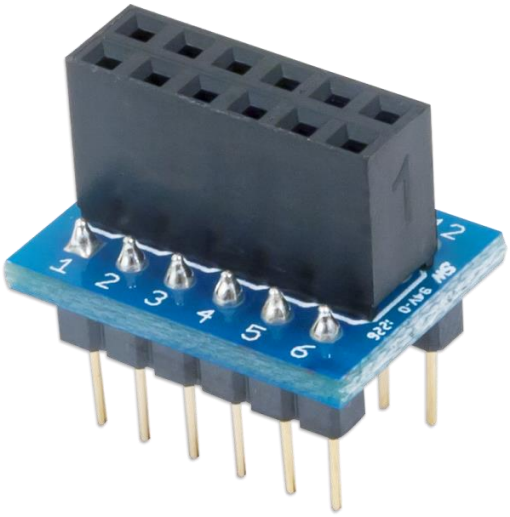
PmodDIP™ Reference Manual

Revised May 24, 2016
This manual applies to the PmodDIP rev. A

Overview

The Digilent PmodDIP is a 12-pin dual in-line package (DIP) that also includes a 2×6 female Pmod header, allowing users to easily separate and process any signals traveling to and from their 12-pin Pmod.



The PmodDIP.

Features include:

- 12-pin dual in-line package module
- 12-pin Pmod port for wire free breadboard connection
- Small size 0.5“ × 0.7” (1.8 cm × 1.3 cm)

1 Functional Description

The PmodDIP utilizes its 12 pass-through channels to route both digital and analog signals to and from the attached Pmod or other module. By being able to plug in the PmodDIP into a solderless breadboard, external processing of the signals can be easily performed.

2 Interfacing with the Pmod

The PmodDIP communicates with the host board via the GPIO protocol. As this is a pass-through module, all signals that are applied to each of the lanes will not be modified in any way.

2.1 Pinout Description Table

Pmod Header Pin	DIP Header	DIP Pin
1	J2	1
2	J2	2
3	J2	3
4	J2	4
5	J2	5
6	J2	6
7	J3	1
8	J3	2
9	J3	3
10	J3	4
11	J3	5
12	J3	6

Any external power applied to the PmodDIP must be within the limits that your system board and attached Pmod can handle.

3 Physical Dimensions

The pins on the pin header are spaced 100 mil apart. The PCB is 0.7 inches long and 0.5 inches wide.