

# RJK03M0DPA

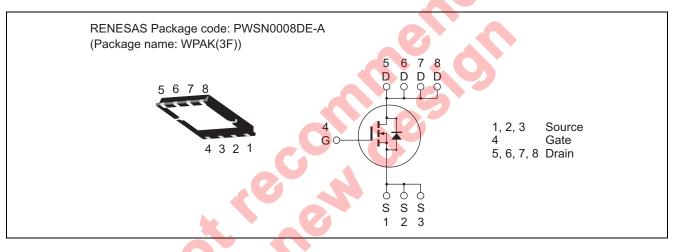
30V, 65A, 1.9mΩmax. N Channel Power MOS FET High Speed Power Switching

R07DS0764EJ0200 Rev.2.00 Feb 08, 2013

### Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- Pb-free
- Halogen-free

#### Outline



## Absolute Maximum Ratings

Symbol	Ratings	Unit		
V <sub>DSS</sub>	30	V		
V <sub>GSS</sub>	±20	V		
ID	65	А		
Note1 D(pulse)	260	А		
I <sub>DR</sub>	65	А		
I <sub>AP</sub> Note 2	24	А		
E <sub>AS</sub> Note 2	57.6	mJ		
	50	W		
θch-c <sup>Note3</sup>	2.5	°C/W		
Tch	150	°C		
Tstg	-55 to +150	°C		
	$V_{DSS}$ $V_{GSS}$ $I_D$ $I_{D(pulse)}^{Note1}$ $I_{DR}$ $I_{AP}^{Note 2}$ $E_{AS}^{Note 2}$ $Pch^{Note3}$ $\theta ch-c^{Note3}$ $Tch$	V <sub>DSS</sub> 30           V <sub>GSS</sub> ±20           I <sub>D</sub> 65           I <sub>D(pulse)</sub> <sup>Note1</sup> 260           I <sub>DR</sub> 65           I <sub>AP</sub> 65           I <sub>AP</sub> 65           Pch         Note 2           24         57.6           Pch         50           θch-c         150		

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tch = 25°C, Rg  $\ge$  50  $\Omega$ 

3. Tc = 25°C

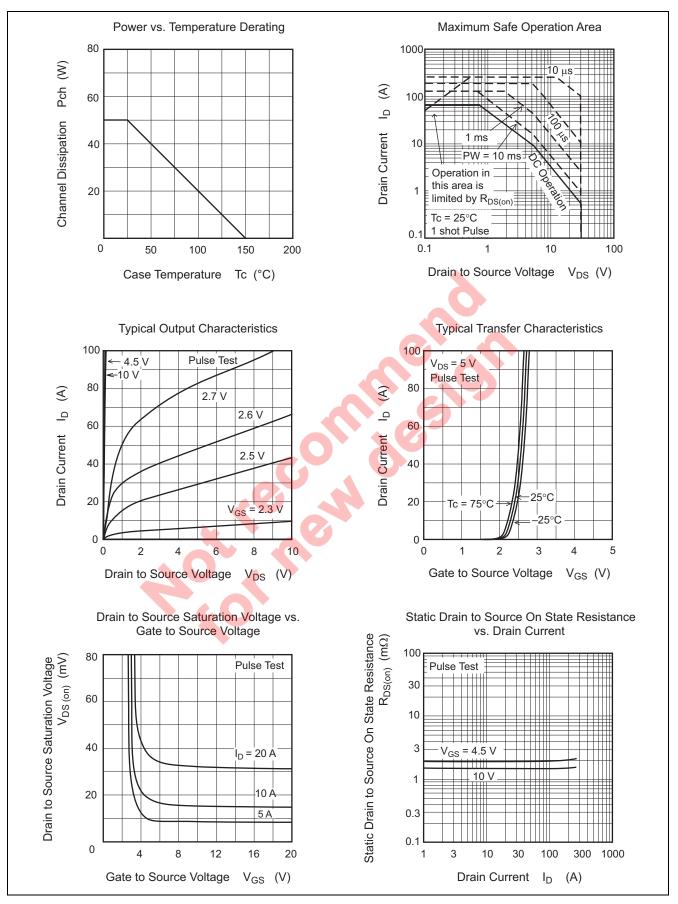


## **Electrical Characteristics**

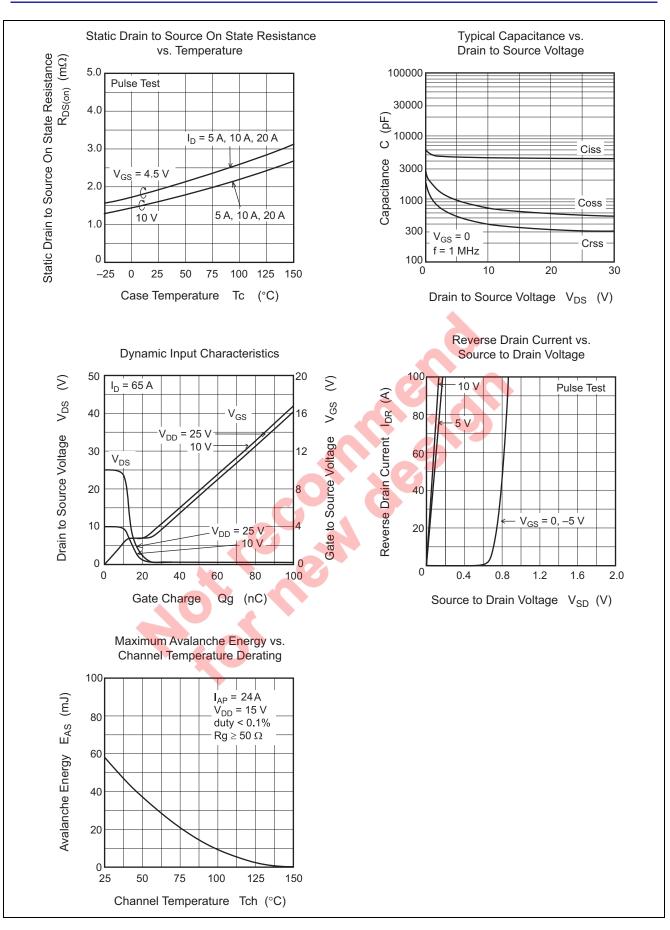
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	_	_	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>		_	± 0.5	μΑ	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		_	1	μA	$V_{DS} = 24 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.2	_	2.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Static drain to source on state	R <sub>DS(on)</sub>	_	1.6	1.9	mΩ	$I_D$ = 32.5 A, $V_{GS}$ = 10 V <sup>Note4</sup>
resistance	R <sub>DS(on)</sub>	_	1.9	2.5	mΩ	$I_D$ = 32.5 A, $V_{GS}$ = 4.5 V <sup>Note4</sup>
Forward transfer admittance	y <sub>fs</sub>	_	160	_	S	$I_D = 32.5 \text{ A}, V_{DS} = 5 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	4500	6300	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss		705	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		400	_	pF	f = 1 MHz
Gate Resistance	Rg	—	1.2	2.4	Ω	
Total gate charge	Qg	—	33.0	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	—	12.6	_	_ nC	V <sub>GS</sub> = 4.5 V
Gate to drain charge	Qgd	—	8.5	_	nC	I <sub>D</sub> = 65 A
Turn-on delay time	t <sub>d(on)</sub>	_	7.5		ns	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 32.5 A
Rise time	tr	—	4.5		ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t <sub>d(off)</sub>	_	72		ns	$R_{L} = 0.31 \Omega$
Fall time	t <sub>f</sub>	_	24		ns	$Rg = 4.7 \Omega$
Body–drain diode forward voltage	V <sub>DF</sub>	_	0.83	1.08	V	$I_F = 65 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body–drain diode reverse recovery	t <sub>rr</sub>		10.9	_	ns	I <sub>F</sub> =65 A, V <sub>GS</sub> = 0
time				5		di <sub>F</sub> / dt = 500 A/ μs
Notes: 4. Pulse test	, ce					

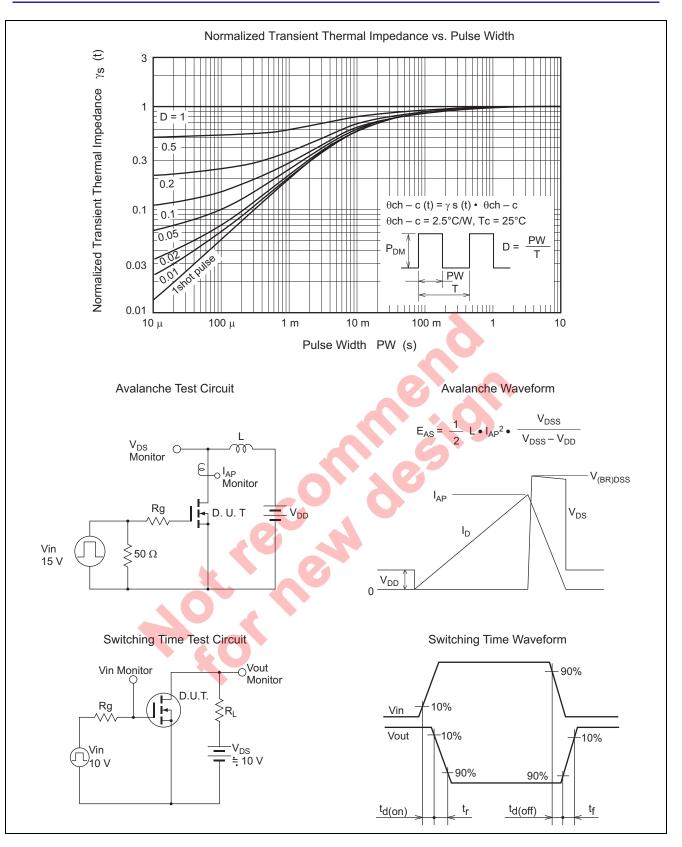


### **Main Characteristics**

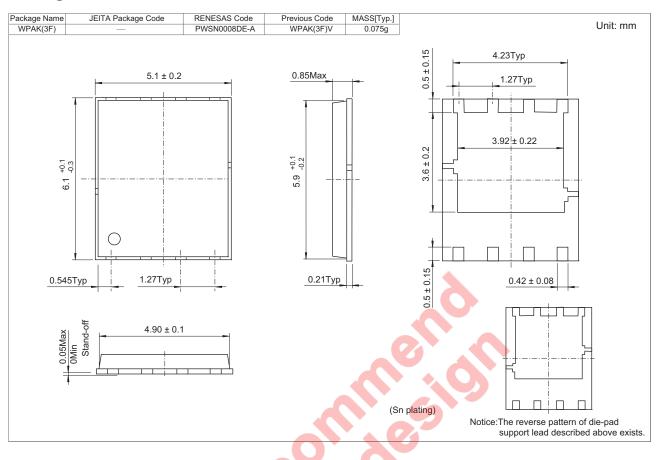








### **Package Dimensions**



### **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RJK03M0DPA-00-J5A	3000 pcs	Taping

Note: The symbol of 2nd "-" is occasionally presented as "#".

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