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**Burr-Brown Products
from Texas Instruments**



**OPA356
OPA2356**

SBOS212A – NOVEMBER, 2001

200MHz, CMOS OPERATIONAL AMPLIFIER

FEATURES

- **UNITY-GAIN BANDWIDTH: 450MHz**
- **WIDE BANDWIDTH: 200MHz GBW**
- **HIGH SLEW RATE: 360V/ μ s**
- **LOW NOISE: 5.8nV/ $\sqrt{\text{Hz}}$**
- **EXCELLENT VIDEO PERFORMANCE:**
DIFF GAIN: 0.02%, DIFF PHASE: 0.05°
0.1dB GAIN FLATNESS: 75MHz
- **INPUT RANGE INCLUDES GROUND**
- **RAIL-TO-RAIL OUTPUT (within 100mV)**
- **LOW INPUT BIAS CURRENT: 3pA**
- **THERMAL SHUTDOWN**
- **SINGLE-SUPPLY OPERATING RANGE: 2.5V to 5.5V**
- **MicroSIZE PACKAGES**

APPLICATIONS

- **VIDEO PROCESSING**
- **ULTRASOUND**
- **OPTICAL NETWORKING, TUNABLE LASERS**
- **PHOTODIODE TRANSIMPEDANCE AMPS**
- **ACTIVE FILTERS**
- **HIGH-SPEED INTEGRATORS**
- **ANALOG-TO-DIGITAL (A/D) CONVERTER INPUT BUFFERS**
- **DIGITAL-TO-ANALOG (D/A) CONVERTER OUTPUT AMPLIFIERS**
- **BARCODE SCANNERS**
- **COMMUNICATIONS**

DESCRIPTION

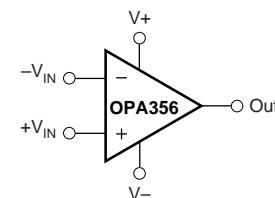
The OPAX356 series high-speed, voltage-feedback CMOS operational amplifiers are designed for video and other applications requiring wide bandwidth. The OPAX356 is unity gain stable and can drive large output currents. Differential gain is 0.02% and differential phase is 0.05°. Quiescent current is only 8.3mA per channel.

OPAX356 is optimized for operation on single or dual supplies as low as 2.5V ($\pm 1.25V$) and up to 5.5V ($\pm 2.75V$). Common-mode input range for the OPAX356 extends 100mV below ground and up to 1.5V from V+. The output swing is within 100mV of the rails, supporting wide dynamic range.

The OPAX356 series is available in single (SOT23-5 and SO-8), and dual (MSOP-8 and SO-8) versions. Multichannel versions feature completely independent circuitry for lowest crosstalk and freedom from interaction. All are specified over the extended -40°C to $+125^{\circ}\text{C}$ range.

OPAX356 RELATED PRODUCTS

FEATURES	PRODUCT
200MHz, Rail-to-Rail Output, CMOS, Shutdown	OPAx355
38MHz, Rail-to-Rail Input/Output, CMOS	OPAx350
75MHz, Rail-to-Rail Output	OPAx631
150MHz, Rail-to-Rail Output	OPAx634
Differential Input/Output, 3.3V Supply	THS412x



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Supply Voltage, V+ to V-	7.5V
Signal Input Terminals, Voltage ⁽²⁾	(V-) - 0.5V to (V+) + 0.5V
Current ⁽²⁾	10mA
Output Short-Circuit ⁽³⁾	Continuous
Operating Temperature	-55°C to +150°C
Storage Temperature	-65°C to +150°C
Junction Temperature	+160°C
Lead Temperature (soldering, 10s)	+300°C

NOTE: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied. (2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current limited to 10mA or less. (3) Short-circuit to ground one amplifier per package.



ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

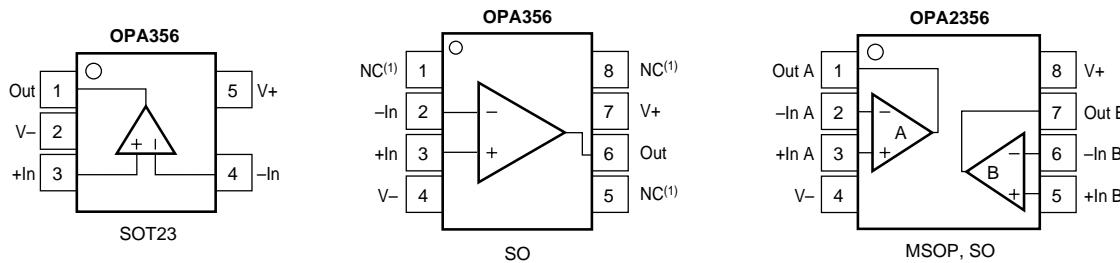
PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE-LEAD	PACKAGE DESIGNATOR ⁽¹⁾	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER ⁽²⁾	TRANSPORT MEDIA, QUANTITY
OPA356AIDBV "	SOT23-5 "	DBV "	-40°C to +125°C "	OAAI "	OPA356AIDBVT OPA356AIDBVR	Tape and Reel, 250 Tape and Reel, 3000
OPA356AID "	SO-8 "	D "	-40°C to +125°C "	OPA356A "	OPA356AID OPA356AIDR	Rails, 100 Tape and Reel, 2500
OPA2356AIDGKT "	MSOP-8 "	DGK "	-40°C to +125°C "	AYI "	OPA2356AIDGKT OPA2356AIDGKR	Tape and Reel, 250 Tape and Reel, 2500
OPA2356AID "	SO-8 "	D "	-40°C to +125°C "	OPA2356A "	OPA2356AID OPA2356AIDR	Rails, 100 Tape and Reel, 2500

NOTES: (1) For the most current specifications and package information, refer to our web site at www.ti.com. (2) Models labeled with "T" indicate smaller quantity tape and reel, "R" indicates large quantity tape and reel and "D" indicates rails of specified quantity.

PIN CONFIGURATIONS

Top View



NOTE: (1) NC means no internal connection.

ELECTRICAL CHARACTERISTICS: $V_S = +2.7V$ to $+5.5V$ Single Supply

Boldface limits apply over the specified temperature range, $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

At $T_A = +25^\circ\text{C}$, $R_F = 604\Omega$, $R_L = 150\Omega$, Connected to $V_S/2$, unless otherwise noted.

PARAMETER	CONDITION	OPA356AIDBV, AID, OPA2356AIDGK, AID			UNITS
		MIN	TYP	MAX	
OFFSET VOLTAGE Input Offset Voltage	V_{OS} $V_S = +5V$ Specified Temperature Range		± 2	± 9	mV
vs Temperature vs Power Supply	dV_{OS}/dT $PSRR$ $V_S = +2.7V$ to $+5.5V$, $V_{CM} = V_S/2 - 0.15V$		± 7 ± 80	± 15 ± 350	$\mu\text{V}^\circ\text{C}$ $\mu\text{V/V}$
INPUT BIAS CURRENT Input Bias Current	I_B		3	± 50	pA
Input Offset Current	I_{OS}		± 1	± 50	pA
NOISE Input Noise Voltage Density	e_n	$f = 1\text{MHz}$	5.8		$\text{nV}/\sqrt{\text{Hz}}$
Current Noise Density	I_n	$f = 1\text{MHz}$	50		$\text{fA}/\sqrt{\text{Hz}}$
INPUT VOLTAGE RANGE Common-Mode Voltage Range	V_{CM}	$(V-) - 0.1$	80	$(V+) - 1.5$	V
Common-Mode Rejection Ratio	CMRR	66 66			dB
					dB
INPUT IMPEDANCE Differential			$10^{13} \parallel 1.5$		$\Omega \parallel \text{pF}$
Common-Mode			$10^{13} \parallel 1.5$		$\Omega \parallel \text{pF}$
OPEN-LOOP GAIN	OPA356 OPA2356	$V_S = +5V$, $0.3V < V_O < 4.7V$ $V_S = +5V$, $0.3V < V_O < 4.7V$ $V_S = +5V$, $0.4V < V_O < 4.6V$	84 80 80	92	dB
					dB
					dB
FREQUENCY RESPONSE Small-Signal Bandwidth	f_{-3dB}	$G = +1$, $V_O = 100\text{mVp-p}$, $R_F = 0\Omega$	450		MHz
	f_{-3dB}	$G = +2$, $V_O = 100\text{mVp-p}$, $R_L = 50\Omega$	100		MHz
	f_{-3dB}	$G = +2$, $V_O = 100\text{mVp-p}$, $R_L = 150\Omega$	170		MHz
	f_{-3dB}	$G = +2$, $V_O = 100\text{mVp-p}$, $R_L = 1\text{k}\Omega$	200		MHz
	f_{-3dB}	$G = +10$, $R_L = 1\text{k}\Omega$	200		MHz
Gain-Bandwidth Product	GBW	$G = +2$, $V_O = 100\text{mVp-p}$, $R_F = 560\Omega$	75		MHz
Bandwidth for 0.1dB Gain Flatness	$f_{0.1dB}$	$V_S = +5V$, $G = +2$, 4V Output Step	300/–360		$\text{V}/\mu\text{s}$
Slew Rate	SR	$G = +2$, $V_O = 200\text{mVp-p}$, 10% to 90%	2.4		ns
Rise-and-Fall Time		$G = +2$, $V_O = 2\text{Vp-p}$, 10% to 90%	8		ns
Settling Time, 0.1% 0.01%		$V_S = +5V$, $G = +2$, 2V Output Step	30		ns
Overload Recovery Time		$V_S = +5V$, $G = +2$, 2V Output Step	120		ns
Harmonic Distortion		$V_{IN} \bullet \text{Gain} = V_S$	8		ns
2 nd Harmonic					dBc
3 rd Harmonic					dBc
Differential Gain Error		$G = +2$, $f = 1\text{MHz}$, $V_O = 2\text{Vp-p}$, $R_L = 200\Omega$	–81		%
Differential Phase Error		$G = +2$, $f = 1\text{MHz}$, $V_O = 2\text{Vp-p}$, $R_L = 200\Omega$	–93		degrees
Channel-to-Channel Crosstalk	OPA2356	NTSC, $R_L = 150\Omega$	0.02		
		NTSC, $R_L = 150\Omega$	0.05		
		$f = 5\text{MHz}$	–90		
OUTPUT					
Voltage Output Swing from Rail		$V_S = +5V$, $R_L = 150\Omega$, $A_{OL} > 84\text{dB}$	0.2	0.3	V
Voltage Output Swing from Rail		$V_S = +5V$, $R_L = 1\text{k}\Omega$	0.1		V
Voltage Output Swing from Rail		$I_O = \pm 100\text{mA}$	0.8	1	V
Ouput Current, Continuous ⁽¹⁾	I_O		± 60		mA
Maximum Output Current, Peak ⁽¹⁾	I_O	$V_S = +5V$	± 100		mA
Maximum Output Current, Peak ⁽¹⁾	I_O	$V_S = +3V$			mA
Short Circuit Current			± 80		mA
Closed-Loop Output Impedance		$f < 100\text{kHz}$	$+250/-200$		Ω
			0.02		
POWER SUPPLY					
Specified Voltage Range	V_S		2.7		V
Operating Voltage Range				2.5 to 5.5	V
Quiescent Current (per amplifier)	I_Q	$V_S = +5V$, $I_O = 0$ Specified Temperature Range		8.3	mA
				11	mA
				14	mA

ELECTRICAL CHARACTERISTICS: $V_S = +2.7V$ to $+5.5V$ Single Supply (Cont.)

Boldface limits apply over the specified temperature range, $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$.

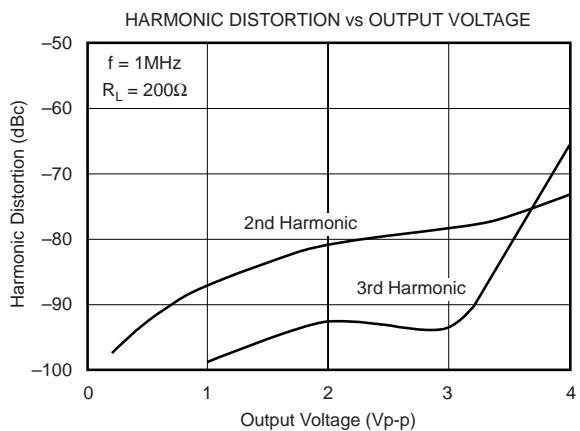
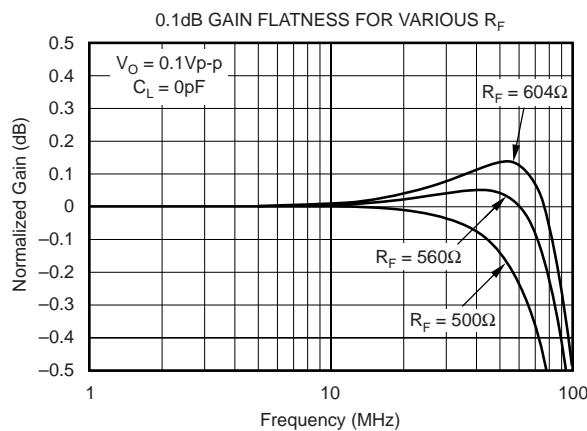
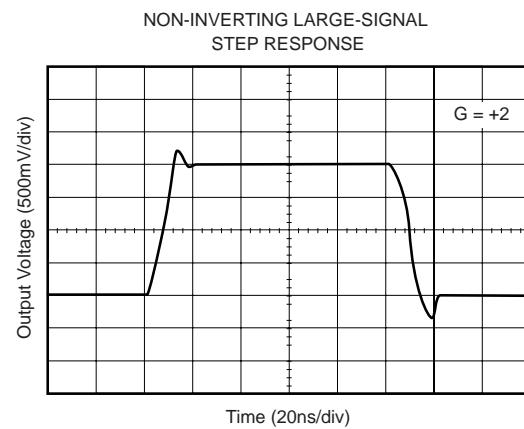
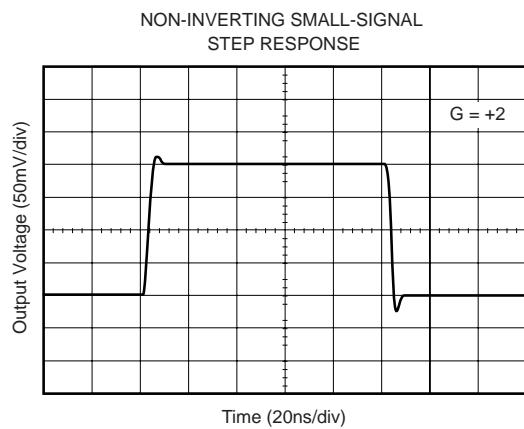
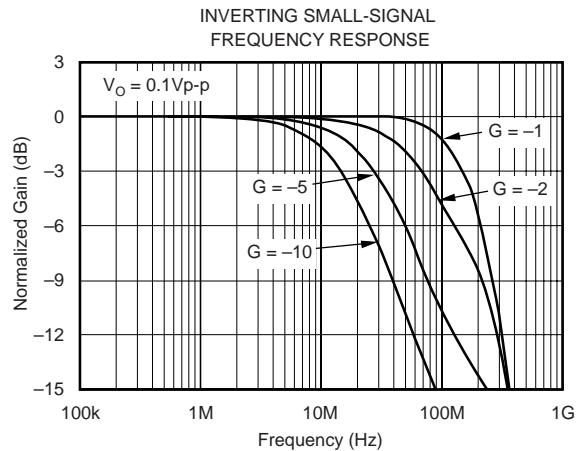
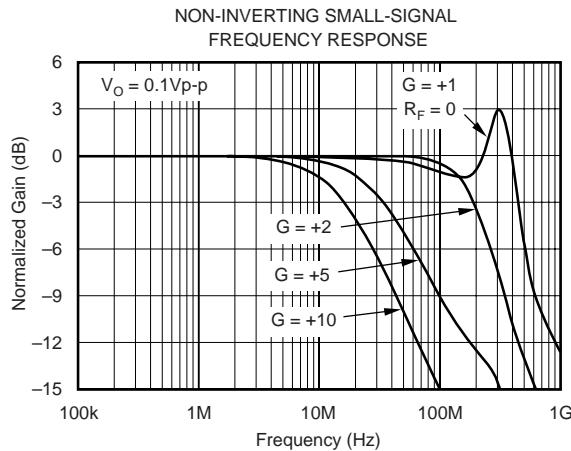
At $T_A = +25^{\circ}\text{C}$, $R_F = 604\Omega$, $R_L = 150\Omega$, Connected to $V_S/2$, unless otherwise noted.

PARAMETER	CONDITION	OPA356AIDBV, AID, OPA2356AIDGK, AID			UNITS
		MIN	TYP	MAX	
THERMAL SHUTDOWN					
Junction Temperature					
Shutdown		160			$^{\circ}\text{C}$
Reset from Shutdown		140			$^{\circ}\text{C}$
TEMPERATURE RANGE					
Specified Range		-40		125	$^{\circ}\text{C}$
Operating Range		-55		150	$^{\circ}\text{C}$
Storage Range		-65		150	$^{\circ}\text{C}$
Thermal Resistance	θ_{JA}				$^{\circ}\text{C}/\text{W}$
SOT23-5, MSOP-8			150		$^{\circ}\text{C}/\text{W}$
SO-8			125		$^{\circ}\text{C}/\text{W}$

NOTES: (1) See typical characteristic "Output Voltage Swing vs Output Current".

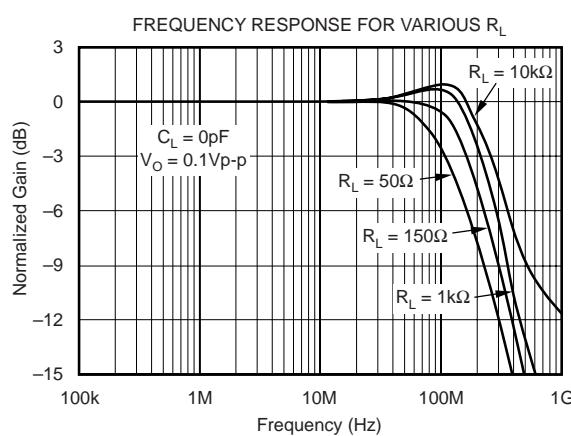
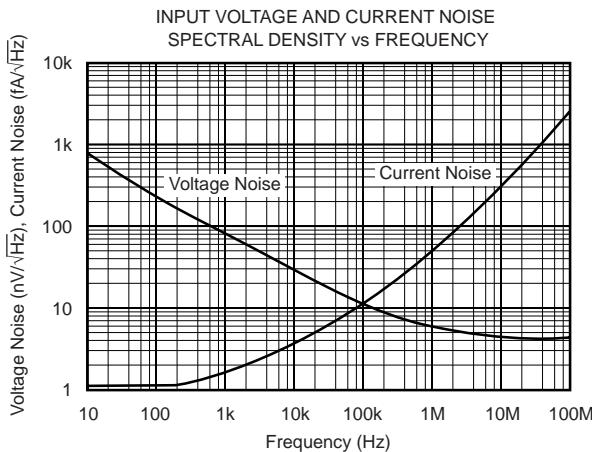
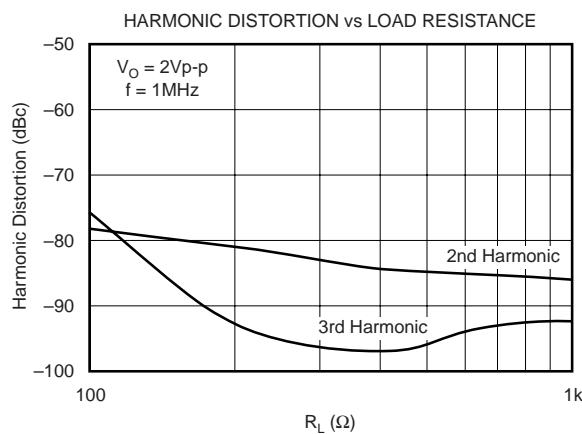
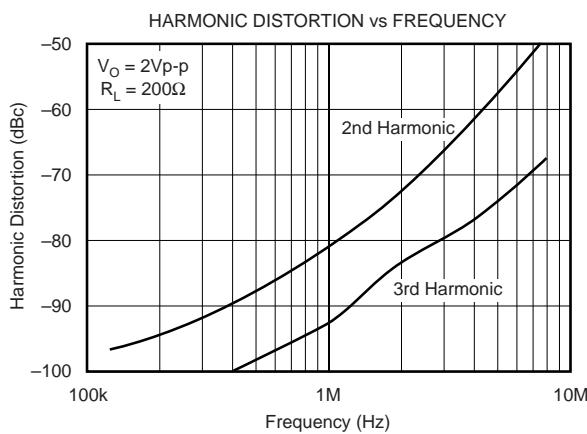
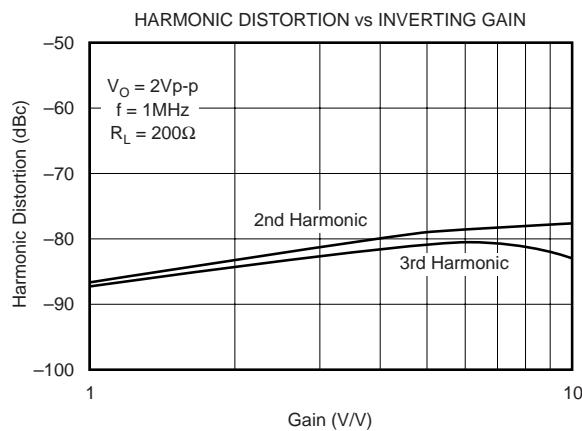
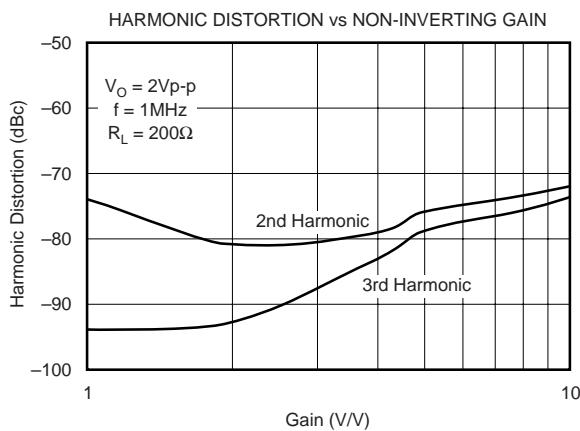
TYPICAL CHARACTERISTICS

At $T_A = +25^\circ\text{C}$ and $V_S = 5\text{V}$, $G = +2$, $R_F = 604\Omega$, $R_L = 150\Omega$ connected to $V_S/2$, unless otherwise noted.



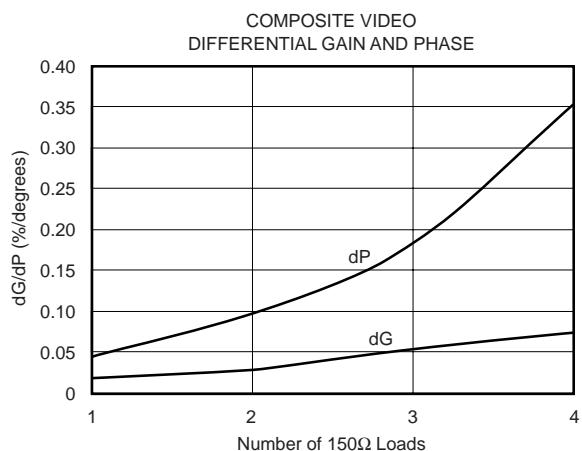
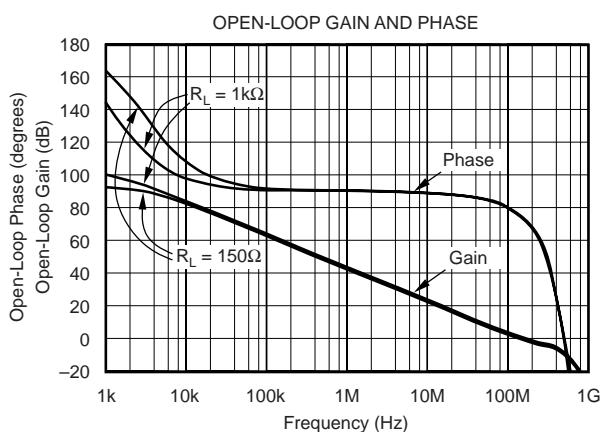
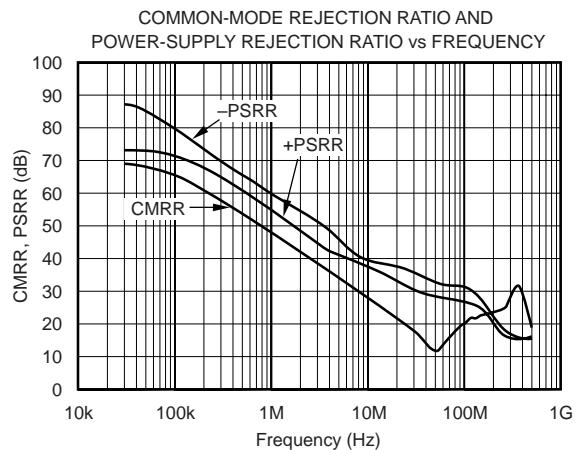
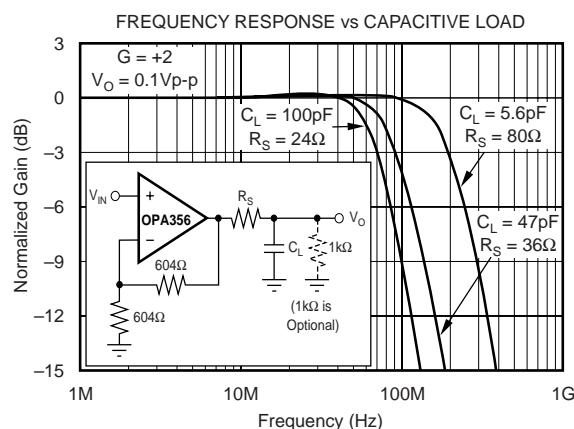
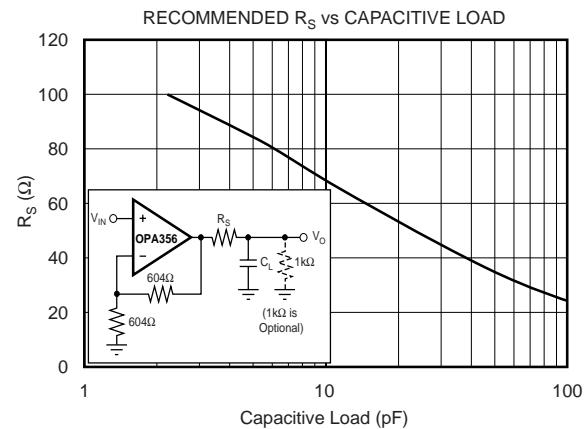
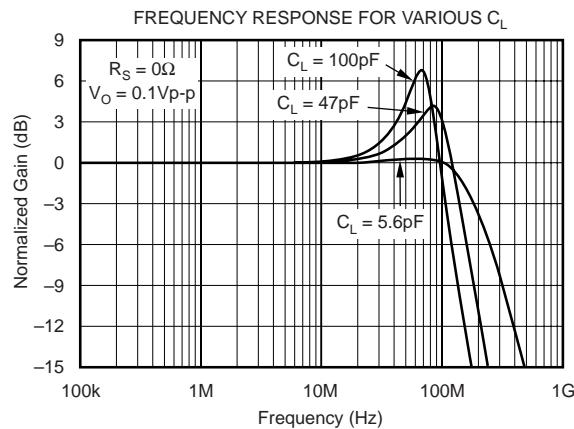
TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$ and $V_S = 5\text{V}$, $G = +2$, $R_F = 604\Omega$, $R_L = 150\Omega$ connected to $V_S/2$, unless otherwise noted.



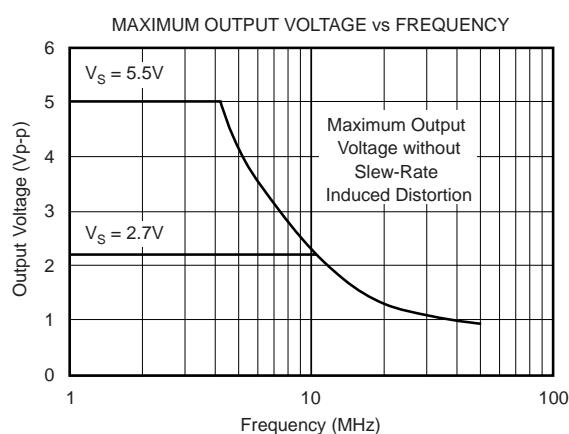
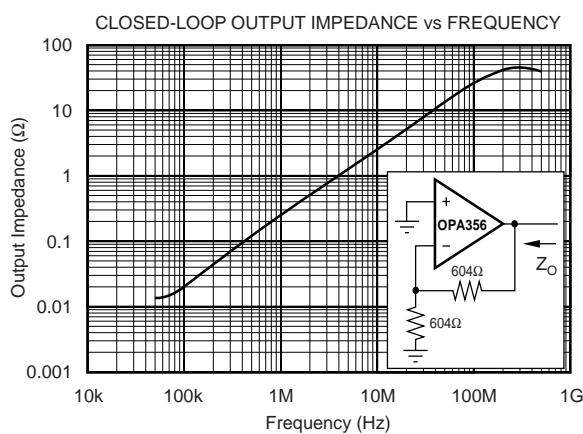
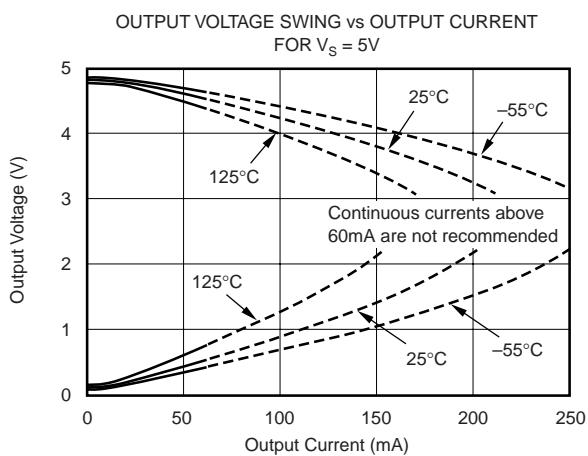
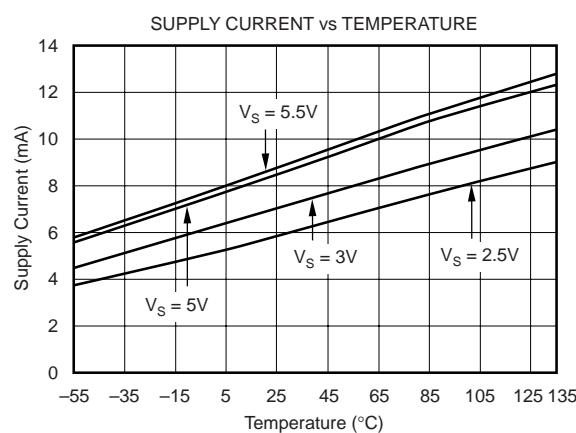
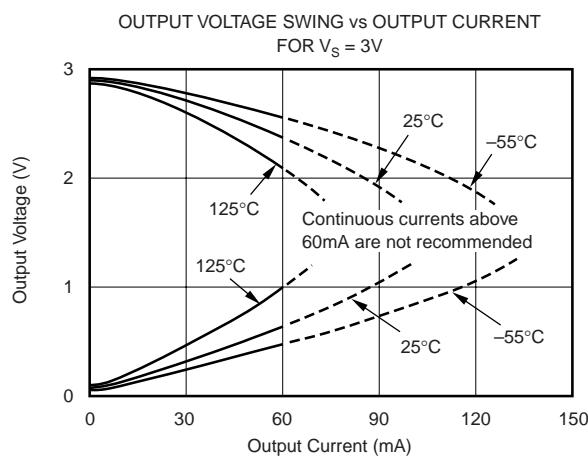
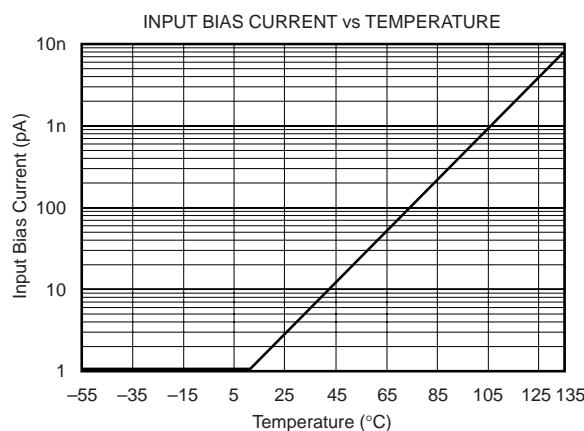
TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$ and $V_S = 5\text{V}$, $G = +2$, $R_F = 604\Omega$, $R_L = 150\Omega$ connected to $V_S/2$, unless otherwise noted.



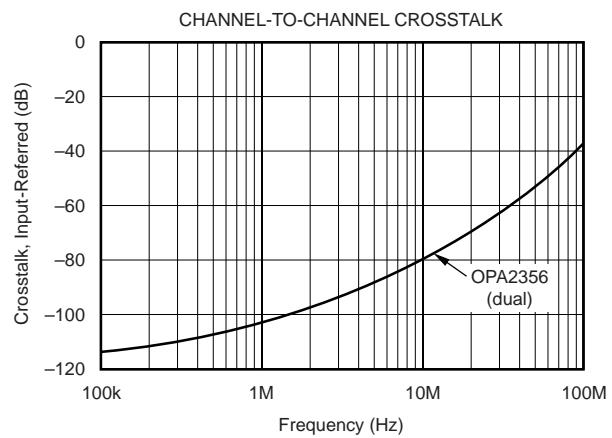
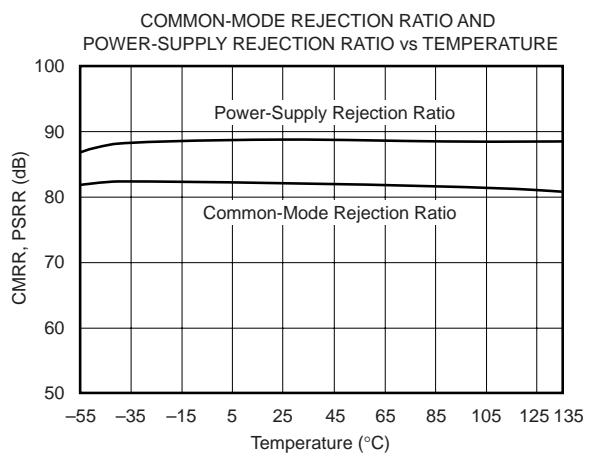
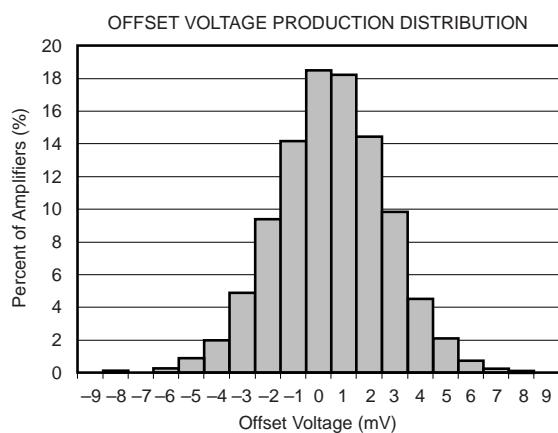
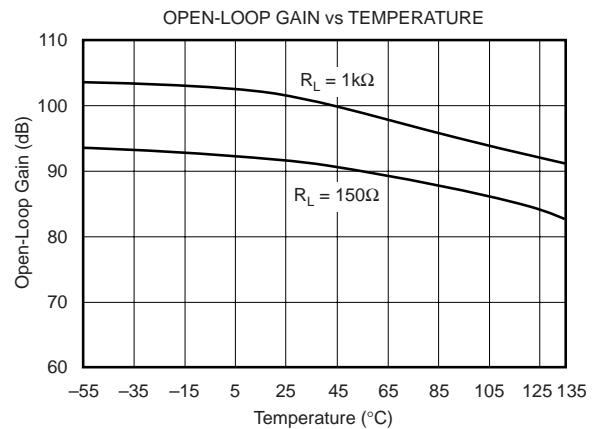
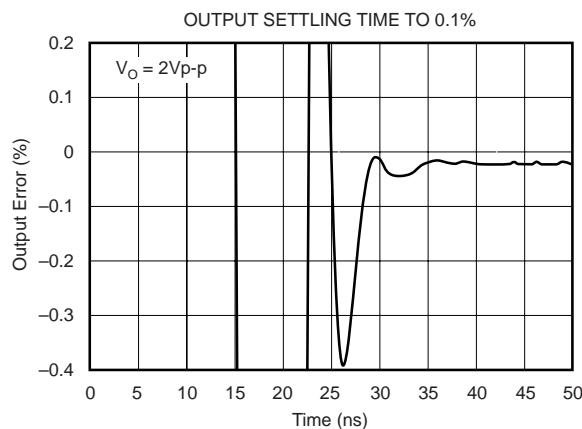
TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$ and $V_S = 5\text{V}$, $G = +2$, $R_F = 604\Omega$, $R_L = 150\Omega$ connected to $V_S/2$, unless otherwise noted.



TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$ and $V_S = 5\text{V}$, $G = +2$, $R_F = 604\Omega$, $R_L = 150\Omega$ connected to $V_S/2$, unless otherwise noted.



APPLICATIONS INFORMATION

The OPAX356 series is a CMOS, high-speed, voltage feedback, operational amplifier designed for video and other general-purpose applications. It is available as a single or dual op amp.

The amplifier features a 200MHz gain bandwidth and 360V/ μ s slew rate, but it is unity-gain stable and can be operated as a +1V/V voltage follower.

Its input common-mode voltage range includes ground, allowing the OPAX356 to be used in virtually any single-supply application up to a supply voltage of +5.5V.

PCB LAYOUT

Good high-frequency PC board layout techniques should be employed for the OPAX356. Generous use of ground planes, short direct signal traces, and a suitable bypass capacitor located at the V+ pin will assure clean, stable operation. Large areas of copper also provide a means of dissipating heat that is generated within the amplifier in normal operation.

Sockets are definitely not recommended for use with any high-speed amplifier.

A 10 μ F ceramic bypass capacitor is the minimum recommended value; adding a 1 μ F or larger tantalum capacitor in parallel can be beneficial when driving a low-resistance load. Providing adequate bypass capacitance is essential to achieving very low harmonic and intermodulation distortion.

OPERATING VOLTAGE

The OPAX356 is specified over a power-supply range of +2.7V to +5.5V (± 1.35 to ± 2.75 V). However, the supply voltage may range from +2.5V to +5.5V (± 1.25 V to ± 2.75 V). Supply voltages higher than 7.5V (absolute maximum) can permanently damage the amplifier.

Parameters that vary significantly over supply voltage or temperature are shown in the "Typical Characteristics" section of this data sheet.

OUTPUT DRIVE

The OPAX356 output stage is capable of driving a standard back-terminated 75 Ω video cable. By back-terminating a transmission line, it does not exhibit a capacitive load to its driver. A properly back-terminated 75 Ω cable does not appear as capacitance; it presents only a 150 Ω resistive load to the OPAX356 output.

The output stage can supply high short-circuit current (typically over 200mA). Therefore, an on-chip thermal shutdown circuit is provided to protect the OPAX356 from dangerously high junction temperatures. At 160°C, the protection circuit will shut down the amplifier. Normal operation will resume when the junction temperature cools to below 140°C.

NOTE: It is not recommended to run a continuous DC current in excess of ± 60 mA. Refer to the graph of "Output Voltage Swing vs Output Current", shown in the "Typical Characteristics" section of this data sheet.

INPUT AND ESD PROTECTION

All OPAX356 pins are static protected with internal ESD protection diodes tied to the supplies, as shown in Figure 1. These diodes will provide overdrive protection if the current is externally limited to 10mA by the source or by a resistor.

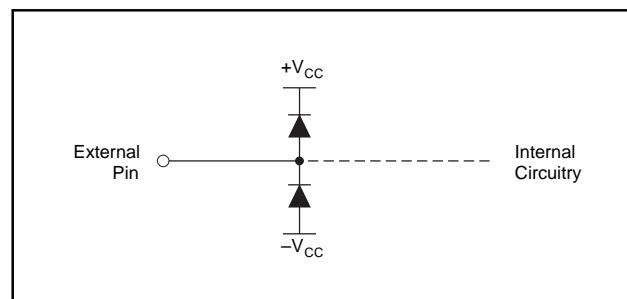


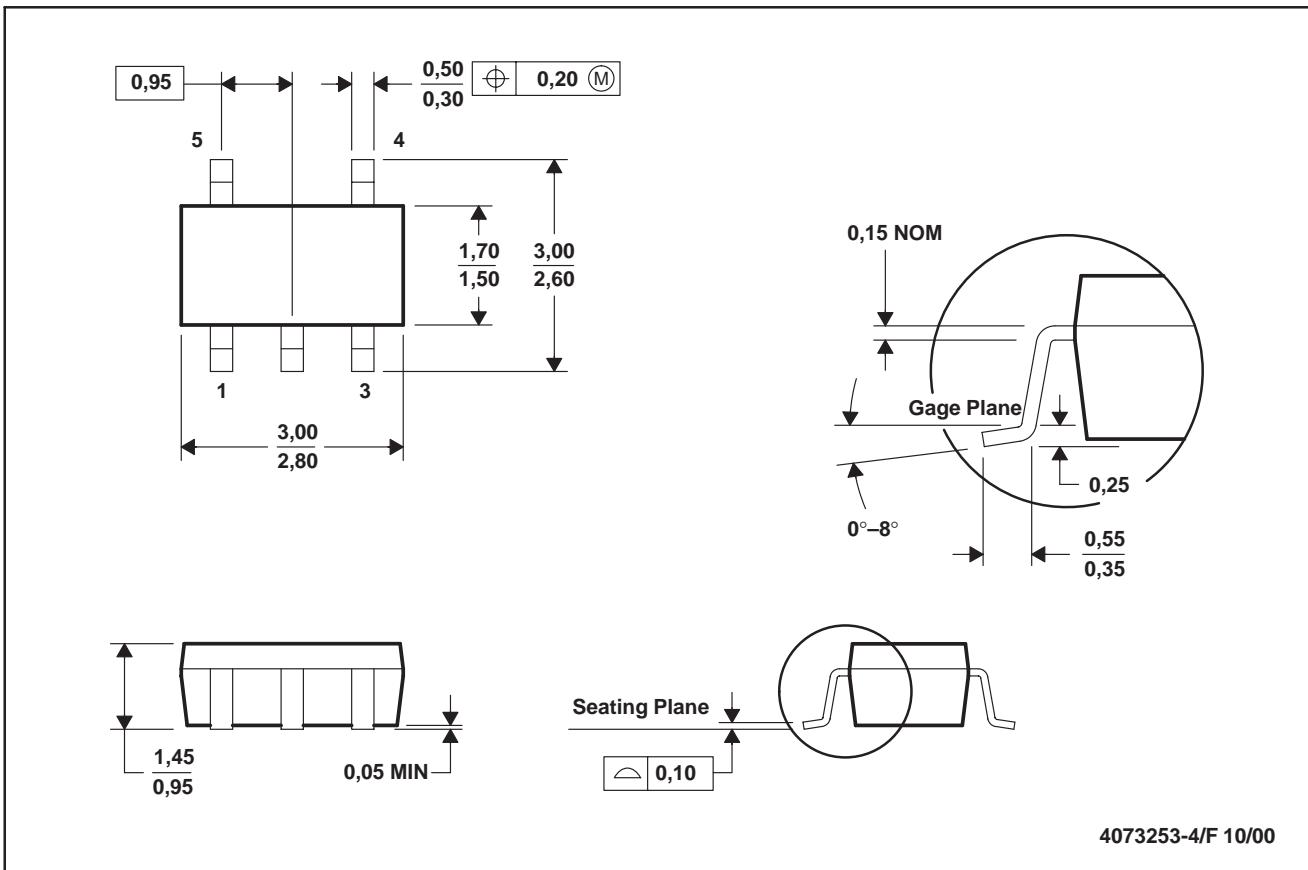
FIGURE 1. Internal ESD Protection.

PACKAGE DRAWINGS

MPDS018D – FEBRUARY 1996 – REVISED JANUARY 2001

DBV (R-PDSO-G5)

PLASTIC SMALL-OUTLINE



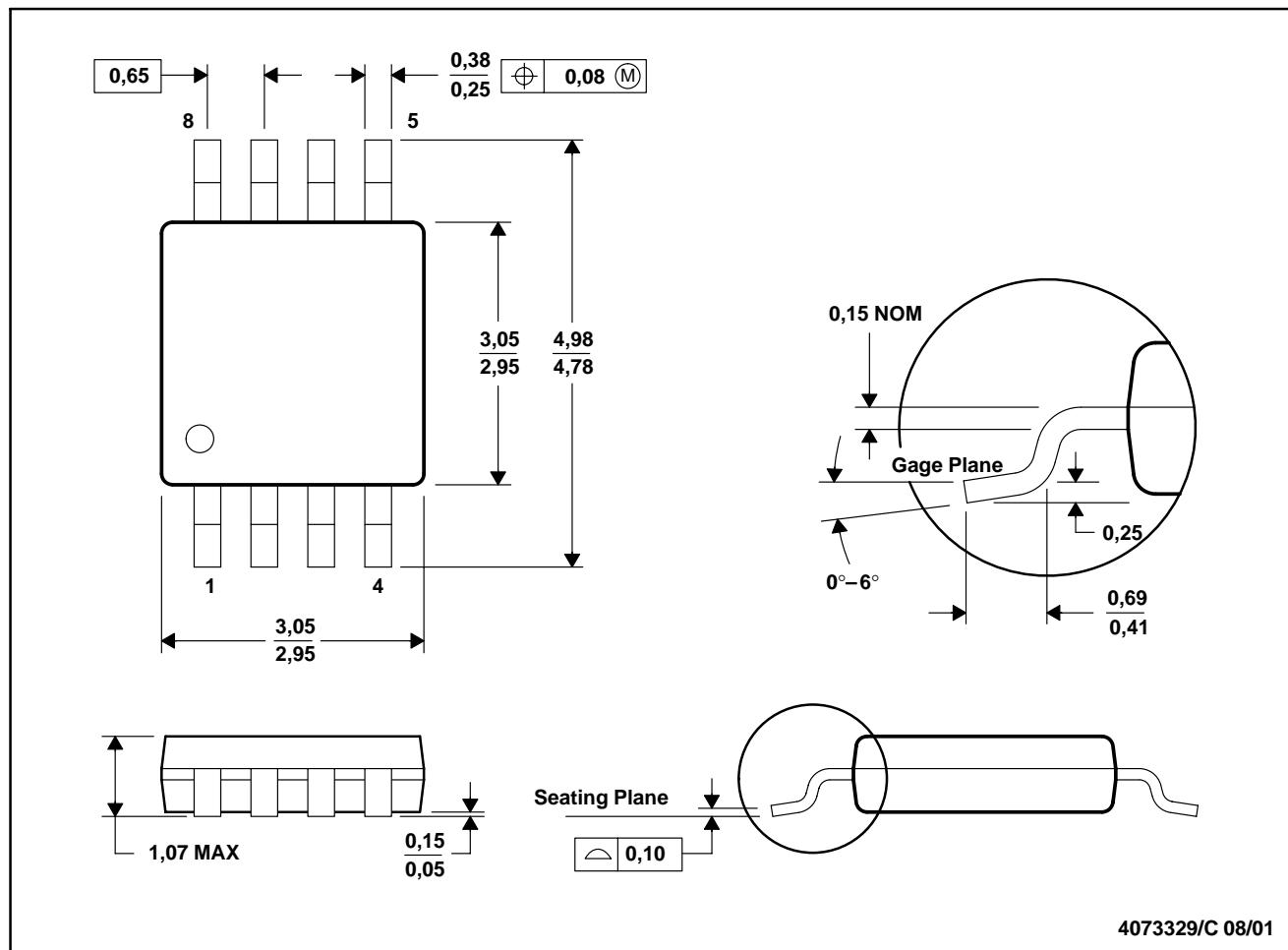
NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion.
 D. Falls within JEDEC MO-178

PACKAGE DRAWINGS (Cont.)

MPDS028B – JUNE 1997 – REVISED SEPTEMBER 2001

DGK (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion.
 D. Falls within JEDEC MO-187

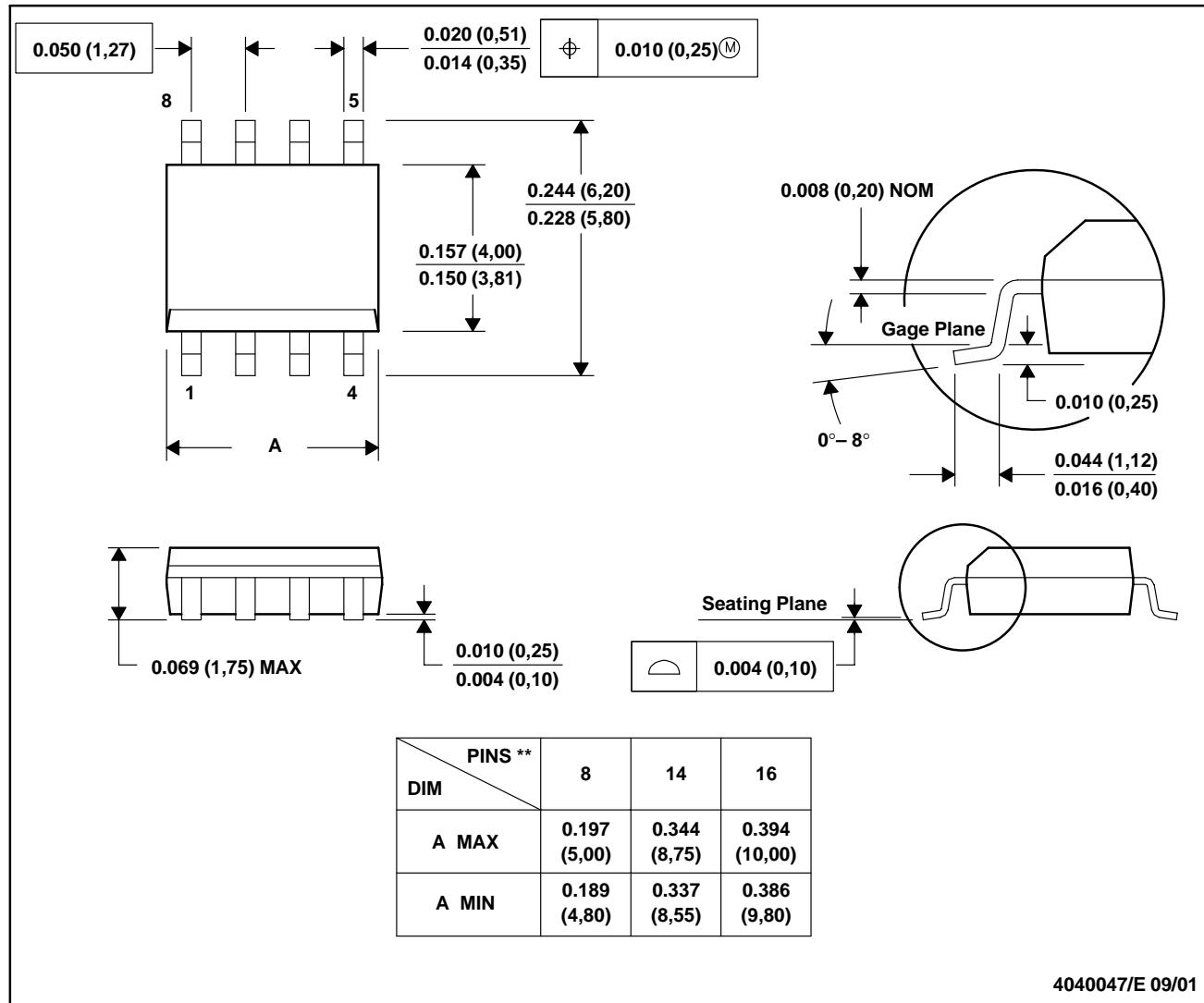
PACKAGE DRAWINGS (Cont.)

MSOI002B – JANUARY 1995 – REVISED SEPTEMBER 2001

D (R-PDSO-G)**

PLASTIC SMALL-OUTLINE PACKAGE

8 PINS SHOWN



NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion, not to exceed 0.006 (0,15).
 D. Falls within JEDEC MS-012

4040047/E 09/01

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
OPA2356AID	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OPA 2356A	Samples
OPA2356AIDG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OPA 2356A	Samples
OPA2356AIDGKR	ACTIVE	VSSOP	DGK	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-2-260C-1 YEAR	-40 to 125	AYI	Samples
OPA2356AIDGKRG4	ACTIVE	VSSOP	DGK	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-2-260C-1 YEAR	-40 to 125	AYI	Samples
OPA2356AIDGKT	ACTIVE	VSSOP	DGK	8	250	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-2-260C-1 YEAR	-40 to 125	AYI	Samples
OPA2356AIDGKTG4	ACTIVE	VSSOP	DGK	8	250	Green (RoHS & no Sb/Br)	CU NIPDAUAG	Level-2-260C-1 YEAR	-40 to 125	AYI	Samples
OPA2356AIDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OPA 2356A	Samples
OPA2356AIDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OPA 2356A	Samples
OPA356AID	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OPA 356A	Samples
OPA356AIDBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OAAI	Samples
OPA356AIDBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OAAI	Samples
OPA356AIDBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OAAI	Samples
OPA356AIDBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OAAI	Samples
OPA356AIDG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OPA 356A	Samples
OPA356AIDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	-40 to 125	OPA 356A	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) **Eco Plan** - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) **Lead/Ball Finish** - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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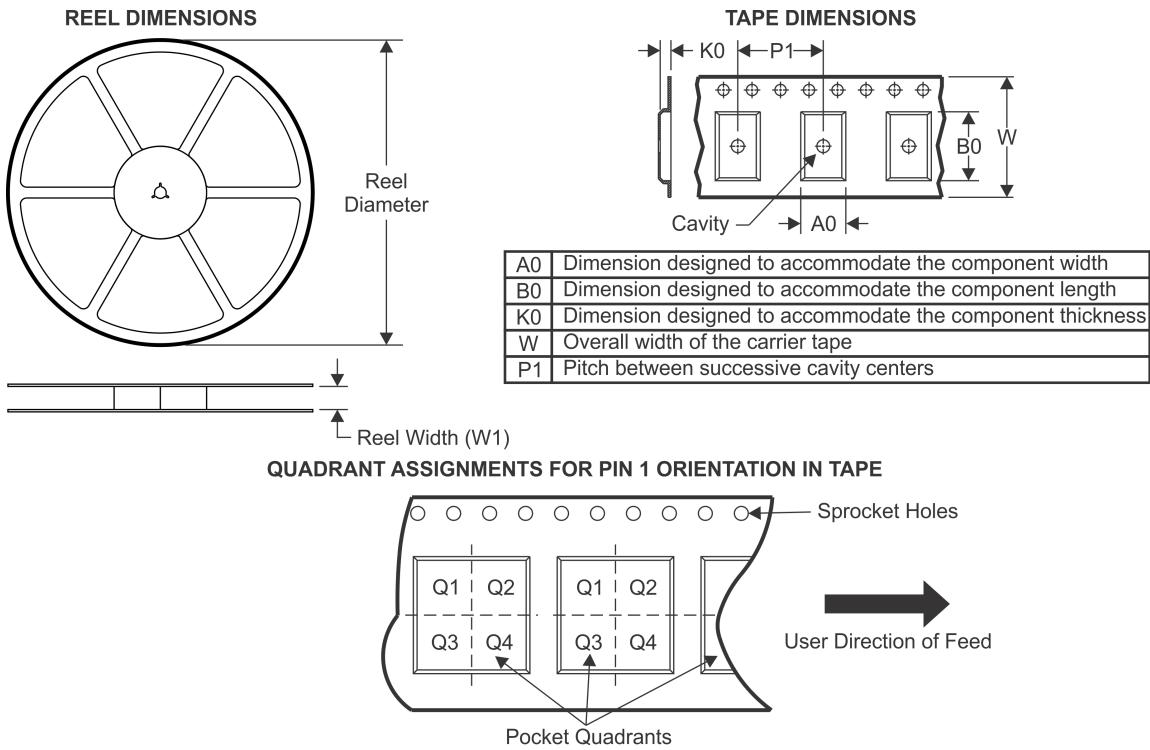
OTHER QUALIFIED VERSIONS OF OPA356 :

- Automotive: [OPA356-Q1](#)

NOTE: Qualified Version Definitions:

- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

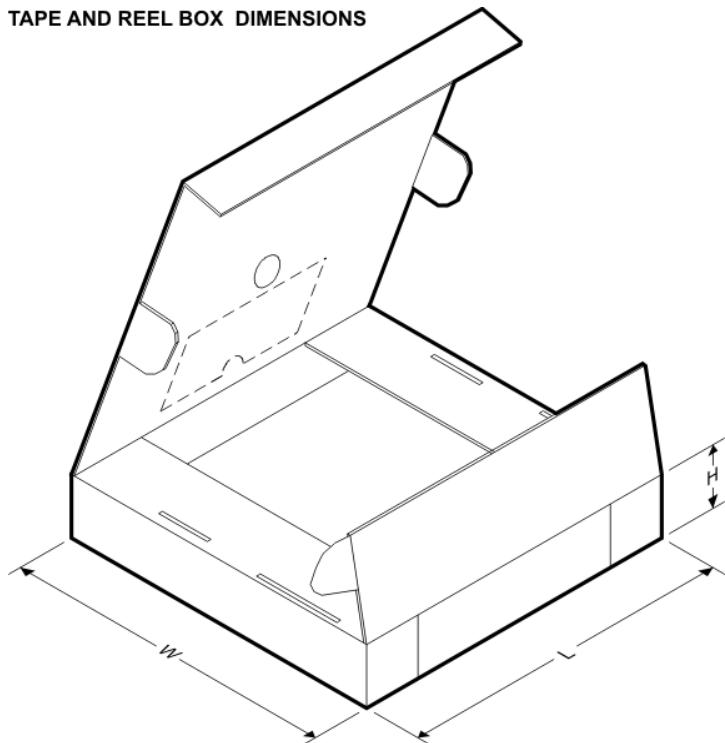
TAPE AND REEL INFORMATION



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
OPA2356AIDGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
OPA2356AIDGKT	VSSOP	DGK	8	250	180.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
OPA2356AIDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
OPA356AIDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
OPA2356AIDGKR	VSSOP	DGK	8	2500	367.0	367.0	35.0
OPA2356AIDGKT	VSSOP	DGK	8	250	210.0	185.0	35.0
OPA2356AIDR	SOIC	D	8	2500	367.0	367.0	35.0
OPA356AIDR	SOIC	D	8	2500	367.0	367.0	35.0

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