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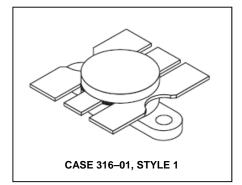
The RF Line NPN Silicon Power Transistor 80W, 3.0-200MHz, 28V

Designed primarily for wideband large-signal output amplifier stages in the 30-200 MHz frequency range.

- Guaranteed performance at 150 MHz, 28 Vdc Output power = 80 W Minimum gain = 10 dB
- Built-in matching network for broadband operation
- 100% tested for load mismatch at all phase angles with 30:1 VSWR
- Gold metallization system for high reliability applications

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Product Image



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	35	Vdc
Collector–Base Voltage	V _{CBO}	65	Vdc
Emitter–Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous Peak	Ι _C	9.0 13.5	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	220 1.26	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R _{0JC}	0.8	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, I _B = 0)	V _{(BR)CEO}	35	_	_	Vdc
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, V _{BE} = 0)	V _{(BR)CES}	65	—	_	Vdc
Collector–Base Breakdown Voltage (I _C = 50 mAdc, I _E = 0)	V _{(BR)CBO}	65	_	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 5.0 mAdc, I _C = 0)	V _{(BR)EBO}	4.0	—	_	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	I _{CBO}	_	_	5.0	mAdc

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ON CHARACTERISTICS

ON CHARACTERISTICS					
DC Current Gain (I _C = 4.0 Adc, V _{CE} = 5.0 Vdc)	h _{FE}	10	_	80	_
DYNAMIC CHARACTERISTICS	•	•			•
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	Cob	_	100	130	pF
NOTE: 1. This device is designed for RF operation. The total device dissigned for RF operation. The total device dissigned (T = 25%)	0	,	the device is	operated as a	(continued n RF amplifie
ELECTRICAL CHARACTERISTICS — continued (T _C = 25°)	+	+	-		
Characteristic	Symbol	Min	Тур	Max	Unit
NARROW BAND FUNCTIONAL TESTS (Figure 1)					
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 80 W, f = 150 MHz)	G _{PE}	10	13	-	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 80 W, f = 150 MHz)	η	55	-	-	%
Load Mismatch (V _{CC} = 28 Vdc, P _{out} = 80 W CW, f = 150 MHz, VSWR = 30:1 all phase angles)	ψ	No Degradation in Output Power			

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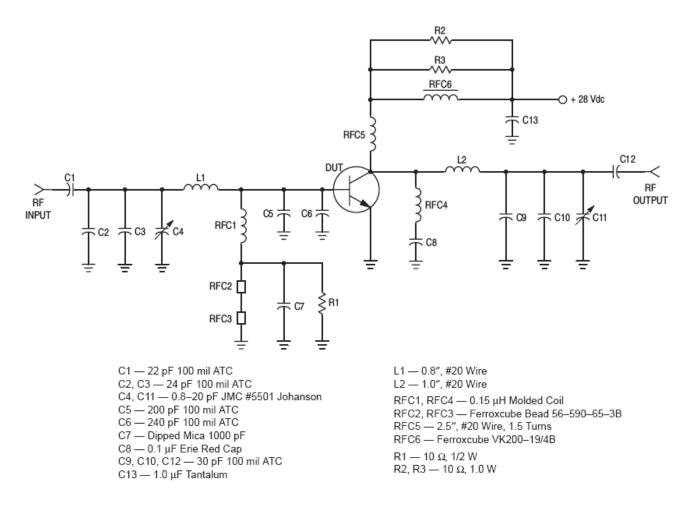


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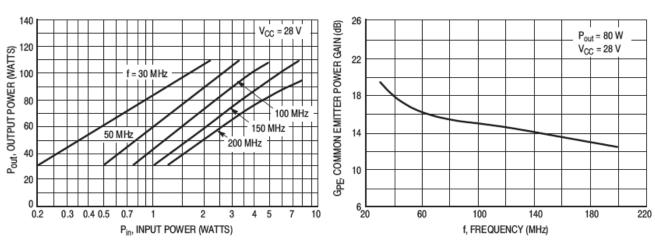


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TYPICAL PERFORMANCE CURVES

Figure 2. Output Power versus Input Power

Figure 3. Power Gain versus Frequency

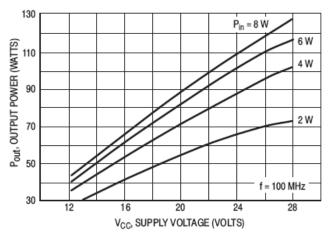


Figure 4. Output Power versus Supply Voltage

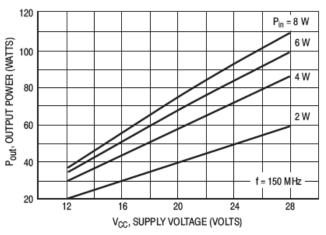


Figure 5. Output Power versus Supply Voltage

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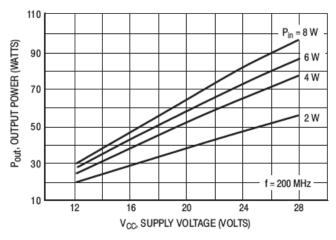


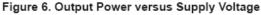
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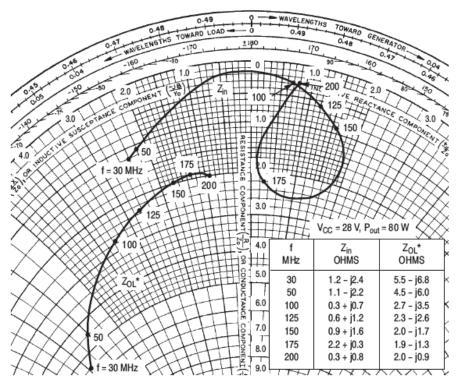


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 Z_{OL}^{\star} = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

Figure 7. Series Equivalent Input-Output Impedance

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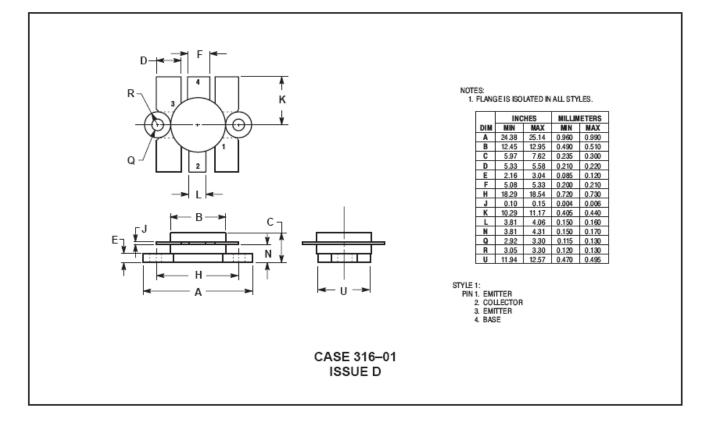
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PACKAGE DIMENSIONS



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