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Vishay Semiconductor/Diodes Division 8ETH03

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8ETH03PbF

Vishay High Power Products

Hyperfast Rectifier, 8 A FRED Pt[™]

FEATURES

- · Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

	Base	
		2
TO-220AC	0 1 Cathode	O 3 Anode

PRODUCT SUMMARY				
t _{rr} 35 ns				
I _{F(AV)}	8 A			
V _R	300 V			

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V _{RRM}		300	V
Average rectified forward current	I _{F(AV)}	T _C = 155 °C	8	А
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	100	A
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	300	-	-	
Forward voltage V _F	I _F = 8 A	-	1.0	1.25	V	
	I _F = 8 A, T _J = 125 °C	-	0.83	1.00		
Reverse leakage current I _R		$V_{R} = V_{R}$ rated	-	0.02	20	
		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	6.0	200	μΑ
Junction capacitance	CT	V _R = 300 V	-	31	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply





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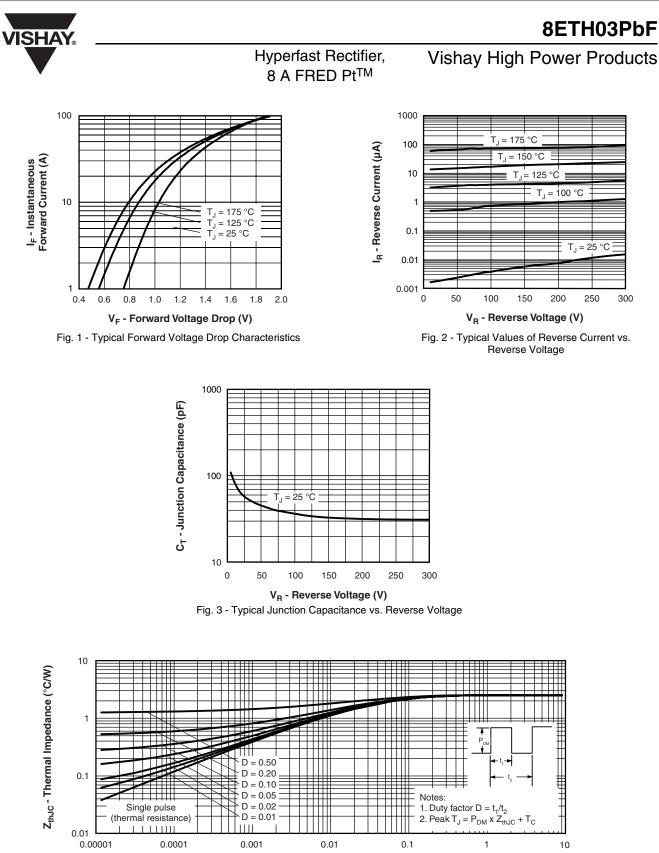
Hyperfast Rectifier, 8 A FRED PtTM



DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = -50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	-	35	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	27	-	ns
		T _J = 125 °C		-	40	-	
De la constante	T _J = 25 °C	$I_F = 8 A$	-	2.2	-	•	
Peak recovery current	Peak recovery current	T _J = 125 °C	dI _F /dt = - 200 A/µs V _R = 200 V	-	5.3	-	A
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	30	-	nC
	T _J = 125 °C		-	106	-	no	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	1.45	2.5	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	-
Weight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-220AC		8ET	H03	





t₁ - Rectangular Pulse Duration (s)

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite Datasheet of 8ETH03 - DIODE GEN PURP 300V 8A TO220AC

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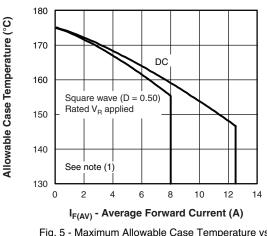
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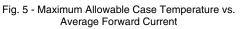
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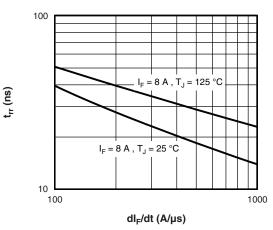
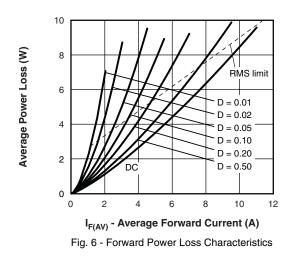


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt



125 = 8 A . T 1-Q_{rr} (nC) 100 ۱. 8 25 °C 10 1000 100 dl_r/dt (A/µs) Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{l} \mbox{(1)} \mbox{ Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ \mbox{Pd} = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6); } \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = \mbox{Rated } V_R \end{array}$





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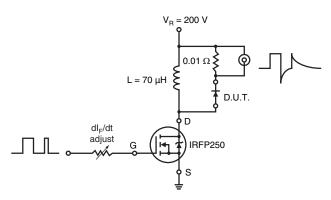


Fig. 9 - Reverse Recovery Parameter Test Circuit

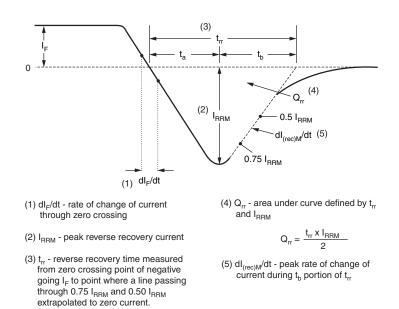


Fig. 10 - Reverse Recovery Waveform and Definitions



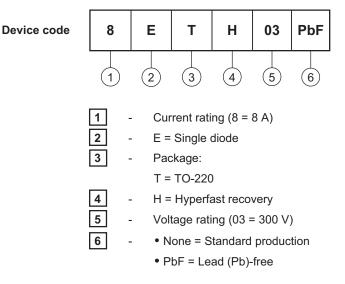
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ORDERING INFORMATION TABLE



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95221			
Part marking information	http://www.vishay.com/doc?95224		





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