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Vishay Semiconductor/Diodes Division 30EPH03

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>





30EPH03

Vishay High Power Products

Ultrafast Rectifier, 30 A FRED Pt[™]

FEATURES

- Ultrafast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- · Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

300 V series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

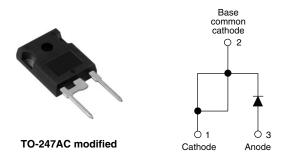
The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V _{RRM}		300	V	
Average rectified forward current	I _{F(AV)}	T _C = 143 °C	30	•	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	300	А	
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	300	-	-	
Forward voltage	V _F	I _F = 30 A	-	1.08	1.25	V
		I _F = 30 A, T _J = 125 °C	-	0.9	1.00	
Reverse leakage current		V _R = V _R rated	-	0.05	60	μA
	I _R	$T_J = 125 \ ^{\circ}C, \ V_R = V_R \text{ rated}$	-	280	600	
Junction capacitance	CT	V _R = 300 V	-	90	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	3.5	-	nH



55 ns

30 A

300 V

PRODUCT SUMMARY

t_{rr}

 $I_{F(AV)}$

 V_{R}



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	50 A/µs, V _R = 30 V	/ 5	55		
Reverse recovery time t	t _{rr}	T _J = 25 °C	-	-	38	-	ns
		T _J = 125 °C		-	52	-	
Peak recovery current		T _J = 25 °C	I _F = 30 A dI _F /dt = - 200 A/μs	-	2.8	-	А
	I _{RRM}	IRRM	T _J = 125 °C	$V_{\rm B} = 200 \text{ V}$	-	7.3	-
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	53	-	nC
		T _J = 125 °C		-	190	-	nc

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	0.5	0.9		
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	40	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-		
Weight			-	6.0	-	g	
			-	0.22	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)	
Marking device		Case style TO-247AC modified		30E	PH03		



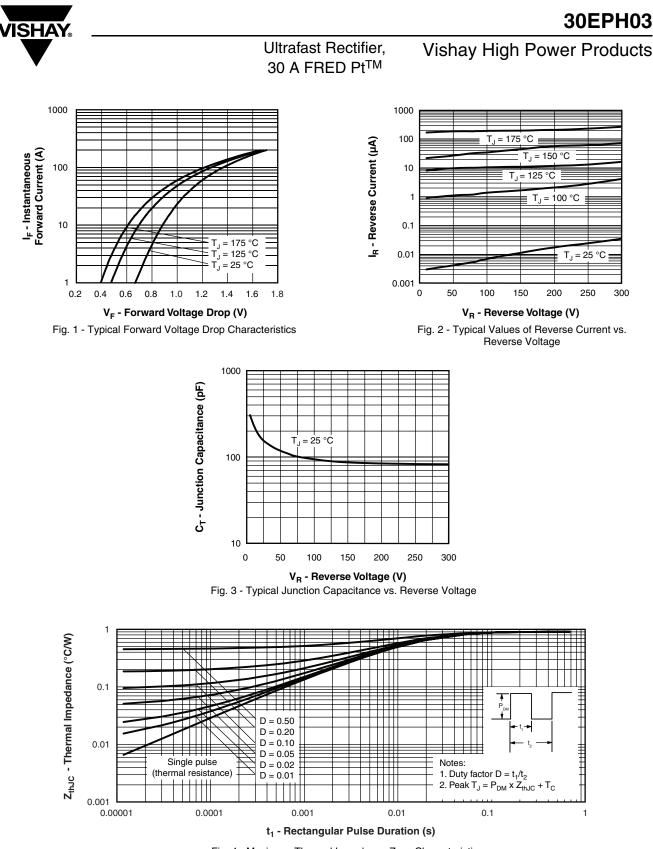


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite Datasheet of 30EPH03 - DIODE GEN PURP 300V 30A TO247AC

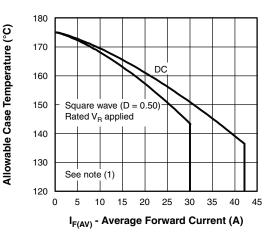
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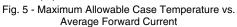
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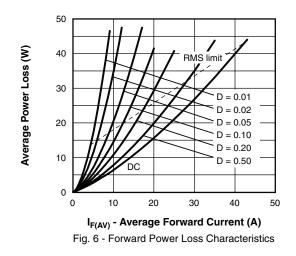
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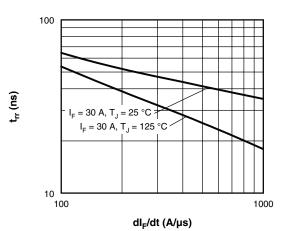






Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$





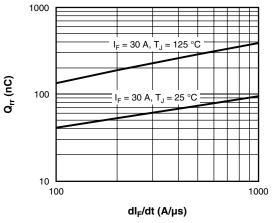


Fig. 8 - Typical Stored Charge vs. dI_F/dt





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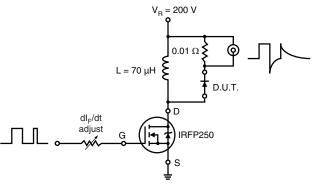
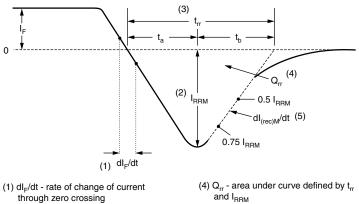


Fig. 9 - Reverse Recovery Parameter Test Circuit



(2) I_{RRM} - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

and I_{RRM}

 $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{}$

(5) dI_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



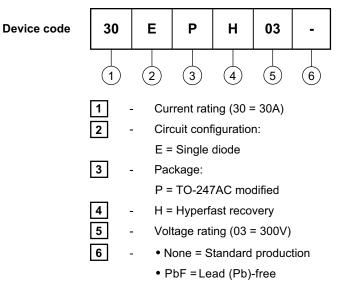
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ORDERING INFORMATION TABLE



Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95253				
Part marking information	http://www.vishay.com/doc?95255			





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